

## HCSL CRYSTAL OSCILLATOR IN CERAMIC LCC PACKAGE - XO75HCSL Series

### FEATURES

- RoHS Compliant (Pb-Free), HCSL Compatible Output
- Designed for PCI Express Applications, Intel Chipset, etc.
- Very Low Phase Jitter, No Internal PLL Avoids Cascading PLL Problems
- Complimentary Output, Tri-state Enable/Disable Standard, 7x5x1.8 mm SMD package

### SPECIFICATIONS

**Frequency Range** 27 MHz to 220 MHz

**Input Voltage (Vcc)** B = +3.3 VDC  $\pm$  5%

**Input Current** 30 mA Maximum

**Storage Temperature** -55°C to 125°C

**Overall Frequency Stability** 50 =  $\pm$ 50 ppm; 25 =  $\pm$ 25 ppm

**Temperature Range** A = 0°C to 70°C; B = -40°C to 85°C

**Standard Stability** 50A =  $\pm$ 50 ppm / 0°C to 70°C

**Electric Option (Duty Cycle)** 3 = Tristate 55/45%; 2 = No tristate 55/45%

**Output Load** 50 Ohms

**Logic "1" Level (Voh)** 660 mV (Min), 740 mV (Typ), 850 mV (Max)

**Logic "0" Level (Vol)** -150 mV (Min), 0 mV (Typ), 150 mV (Max)

**Output Swing (Vopp)** 650 mV Minimum

**Rise/Fall Time (Tr/Tf)** 0.5 ns Maximum, at 20% to 80% Vp-p

**Start-up time** 2 ms Maximum

**Phase Jitter (RMS, 1 Sigma)** 1 ps Max for fj = 12KHz to 20MHz

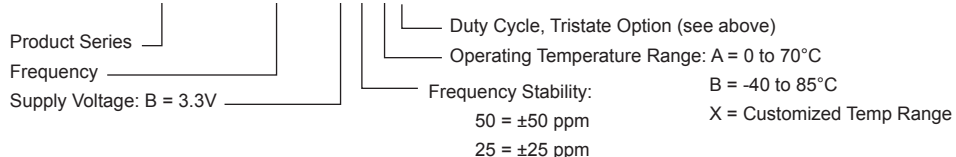
**Tristate Function** Input (Pin 1) High (> 0.7Vcc) or open: Output (Pin 4, 5) active

Input (Pin 1) Low (< 0.3Vcc): Output disabled in high impedance

**Enable/Disable Time** 2 ms Maximum - Enable; 200 ns Maximum - Disable

### Creating a Part Number

#### XO75HCSL-125M000-B50A3



### OUTLINE DRAWING

