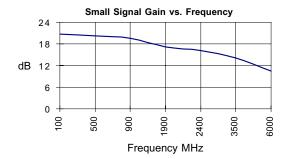




## **Product Description**

Stanford Microdevices' SGA-2463 is a high performance cascadeable 50-ohm amplifier designed for operation at voltages as low as 2.6V. This RFIC uses the latest Silicon Germanium Heterostructure Bipolar Transistor (SiGe HBT) process featuring 1 micron emitters with  $F_T$  up to 50 GHz.

This circuit uses a darlington pair topology with resistive feedback for broadband performance as well as stability over its entire temperature range. Internally matched to 50 ohm impedance, the SGA-2463 requires only DC blocking and bypass capacitors for external components.



# **SGA-2463**

DC-2000 MHz Silicon Germanium HBT Cascadeable Gain Block



#### **Product Features**

- DC-2000 MHz Operation
- Single Voltage Supply
- Low Current Draw: 20mA at 2.6V typ.
- High Output Intercept: +19.5dBm typ. at 1950 MHz
- Low Noise Figure: 2.6dB typ. at 850 MHz

## **Applications**

- Oscillator Amplifiers
- Cordless Phones
- IF/ RF Buffer Amplifier
- Drivers for CATV Amplifiers

Symbol	Parameters: Test Conditions: Z <sub>0</sub> = 50 Ohms, Id = 20 mA, T = 25°C		Units	Min.	Тур.	Max.
P <sub>1dB</sub>	Output Power at 1dB Compression	f = 850 MHz f = 1950 MHz	dBm dBm		9.2 6.6	
S <sub>21</sub>	Small Signal Gain	f = DC - 1000 MHz f = 1000 - 2000 MHz	dB dB	18.2	20.2 18.1	
S <sub>12</sub>	Reverse Isolation	f = DC - 1000 MHz f = 1000 - 2000 MHz	dB dB		23.2 23.1	
S <sub>11</sub>	Input VSWR	f = DC - 2000 MHz	-		1.6	
S <sub>22</sub>	Output VSWR	f = DC - 2000 MHz	-		1.3	
IP <sub>3</sub>	Third Order Intercept Point Power out per Tone = -10 dBm	f = 850 MHz f = 1950 MHz	dBm dBm		20.1 19.5	
NF	Noise Figure	f = DC - 1000 MHz f = 1000 - 2000 MHz	dB dB		2.6 3.1	
T <sub>D</sub>	Group Delay	f = 1000 MHz	pS		111	
V <sub>D</sub>	Device Voltage		V	2.3	2.6	2.9

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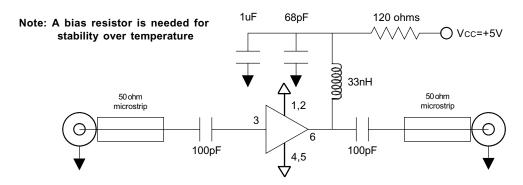


		Specificatio	n		Test
Parameter	Min	Тур.	Max.	Unit	Condition
Bandwidth					T= 25C
Frequency Range	DC		5000	MHz	
Device Bias					T= 25C
Operating Voltage		2.6		V	
Operating Current		20		mA	
500 MHz					T= 25C
Gain		20.3		dB	
Noise Figure		2.7		dB	
Output IP3		19.7		dBm	
Output P1dB		8.1		dBm	
Input Return Loss		14.5		dB	
Isolation		22.9		dB	
850 MHz					T= 25C
Gain		19.6		dB	
Noise Figure		2.6		dB	
Output IP3		20.1		dBm	
Output P1dB		9.2		dBm	
Input Return Loss		13.2		dB	
Isolation		22.9		dB	
1950 MHz					T= 25C
Gain		17.1		dB	
Noise Figure		3.1		dB	
Output IP3		19.5		dBm	
Output P1dB		6.6		dBm	
Input Return Loss		11.2		dB	
Isolation		22.8		dB	

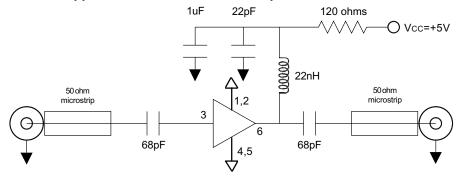


Pin #	Function	Description	Device Schematic
1	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.	
2	GND	Sames as Pin 1	
3	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.	
4	GND	Sames as Pin 1	
5	GND	Sames as Pin 1	
6	RF OUT	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.	

### Application Schematic for +5V Operation at 900 MHz

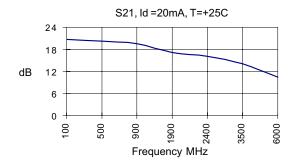


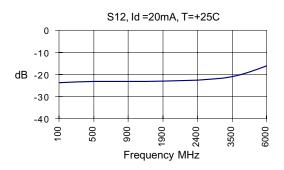
### Application Schematic for +5V Operation at 1900 MHz

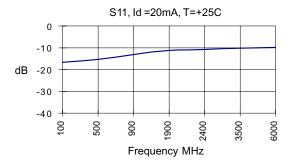


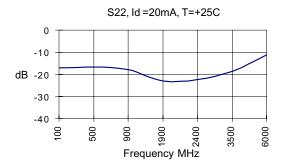


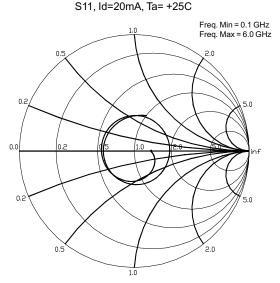


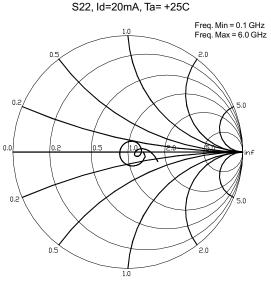






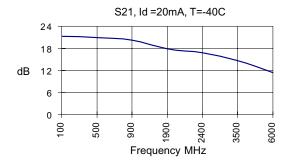


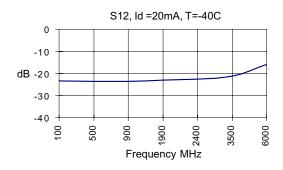


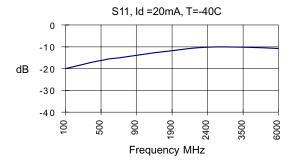


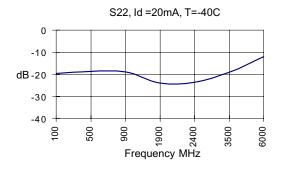


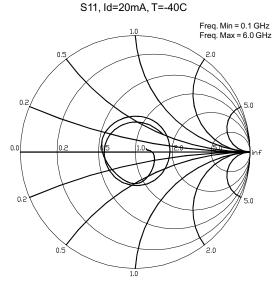


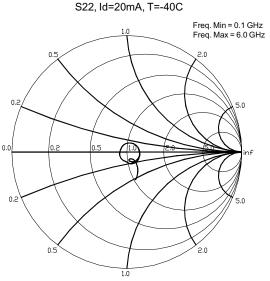






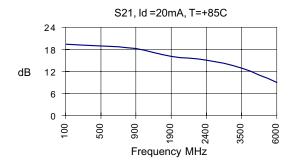


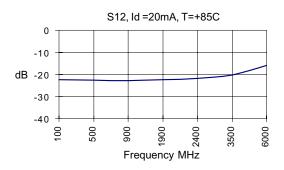


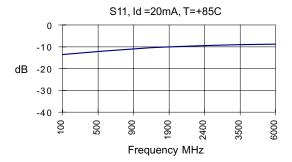


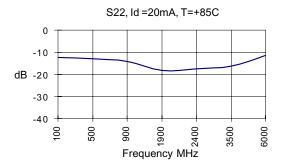


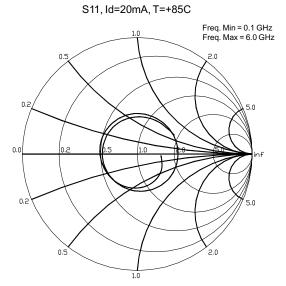


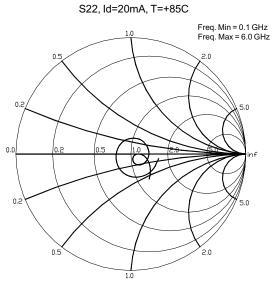
















#### **Absolute Maximum Ratings**

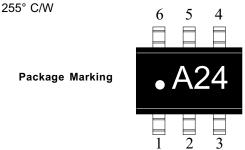
Parameter	Value	Unit
Supply Current	40	mA
Operating Temperature	-40 to +85	С
Maximum Input Power	+4	dBm
Storage Temperature Range	-40 to +85	С
Operating Junction Temperature	+125	С

#### Caution:



Operation of this device above any one of these parameters may cause permanent damage. Appropriate precautions in handling, packaging and testing devices must be observed.

Thermal Resistance (Lead-Junction):



#### **Part Number Ordering Information**

Part Number	Reel Size	Devices/Reel
SGA-2463-TR1	7"	3000

Recommended Bias Resistor Values					
Supply Voltage(Vs)	3V	5V	7.5V	9V	12V
Rbias (Ohms)	20	120	245	320	470

Pad Layout

Pin Designation				
1	GND			
2	GND			
3	RF in			
4	GND			
5	GND			
6	RF out			

Note: Pin 1 is on lower left when you can read package marking

#### **Package Dimensions**

