



# AWT6251

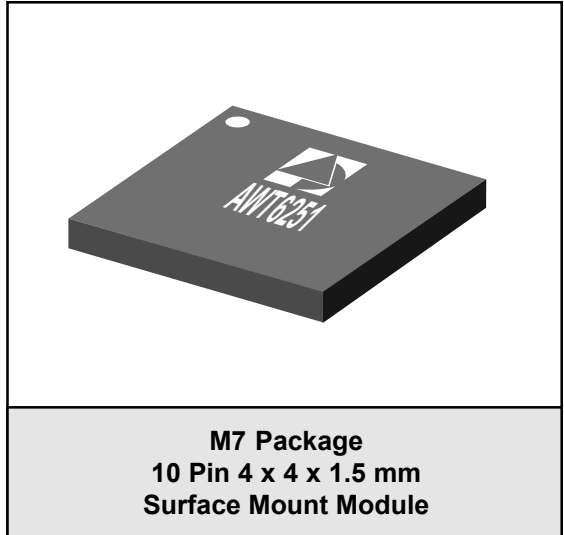
PCS/WCDMA 3.4V/27.5 dBm  
 Linear Power Amplifier Module  
 PRELIMINARY DATA SHEET - Rev 1.0

## FEATURES

- InGaP HBT Technology
- High Efficiency: 37%
- Low Quiescent Current: 50 mA
- Low Leakage Current in Shutdown Mode: <math><1 \mu\text{A}</math>
- $V_{REF} = +2.85 \text{ V}$  (+2.75 V min over temp)
- Optimized for a 50  $\Omega$  System
- Low Profile Miniature Surface Mount Package:  
 1.56mm Max

## APPLICATIONS

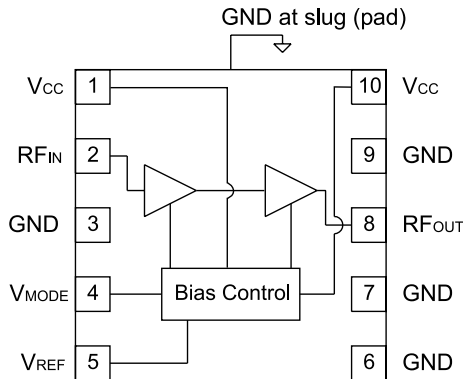
- Dual Mode 3GPP Wireless Handsets



## PRODUCT DESCRIPTION

The AWT6251 meets the increasing demands for higher output power in 3GPP handsets. The PA module is optimized for  $V_{REF} = +2.85 \text{ V}$ , a requirement for compatibility with the Qualcomm® 6000 chipset. The device is manufactured on an advanced InGaP HBT MMIC technology offering state-of-the-art reliability, temperature stability, and ruggedness. Selectable

bias modes that optimize efficiency for different output power levels, and a shutdown mode with low leakage current, increase handset talk and standby time. The self-contained 4mm x 4mm surface mount package incorporates matching networks optimized for output power, efficiency, and linearity in a 50  $\Omega$  system.



**Figure 1: Block Diagram**

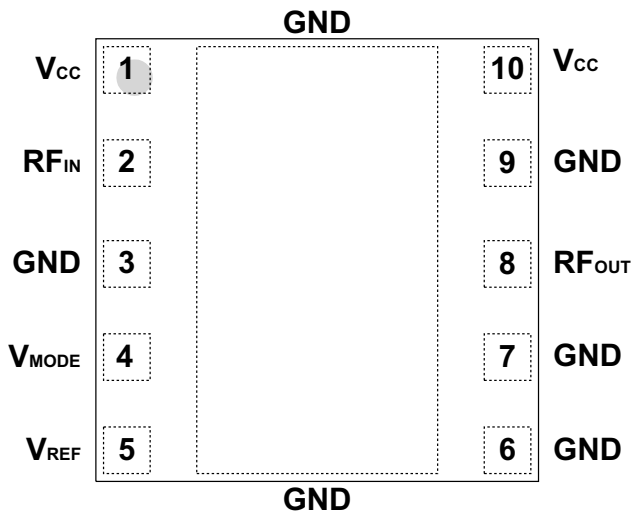


Figure 2: Pinout (X-ray Top View)

Table 1: Pin Description

PIN	NAME	DESCRIPTION
1	V <sub>CC</sub>	Supply Voltage
2	RF <sub>IN</sub>	RF Input
3	GND	Ground
4	V <sub>MODE</sub>	Mode Control Voltage
5	V <sub>REF</sub>	Reference Voltage
6	GND	Ground
7	GND	Ground
8	RF <sub>OUT</sub>	RF Output
9	GND	Ground
10	V <sub>CC</sub>	Supply Voltage

**ELECTRICAL CHARACTERISTICS****Table 2: Absolute Minimum and Maximum Ratings**

PARAMETER	MIN	MAX	UNIT
Supply Voltage ( $V_{CC}$ )	0	+5	V
Mode Control Voltage ( $V_{MODE}$ )	0	+3.5	V
Reference Voltage ( $V_{REF}$ )	0	+3.5	V
RF Input Power ( $P_{IN}$ )	-	+10	dBm
Storage Temperature ( $T_{STG}$ )	-40	+150	°C

Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

**Table 3: Operating Ranges**

PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS
Operating Frequency (f)	1850	-	1910	MHz	
Supply Voltage ( $V_{CC}$ )	+3.2	+3.4	+4.2	V	
Reference Voltage ( $V_{REF}$ )	+2.75 0	+2.85 -	+2.95 +0.5	V	PA "on" PA "shut down"
Mode Control Voltage ( $V_{MODE}$ )	+2.5 0	+2.8 -	+3.1 +0.5	V	Low Bias Mode High Bias Mode
RF Output Power ( $P_{OUT}$ )	+27.0 <sup>(1)</sup>	+27.5	-	dBm	
Case Temperature ( $T_C$ )	-10	-	+90	°C	

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

Notes:

(1) For operation at  $V_{CC} = +3.2$  V,  $P_{OUT}$  is derated by 0.5 dB.

Table 4: Electrical Specifications

(T<sub>C</sub> = +25 °C, V<sub>CC</sub> = +3.4 V, V<sub>REF</sub> = +2.85 V, 50 Ω system)

PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS
Gain	25 22 -	27 24.5 18	29 27 -	dB	P <sub>OUT</sub> = +27.5 dBm, V <sub>MODE</sub> = 0 V P <sub>OUT</sub> = +16 dBm, V <sub>MODE</sub> = +2.85 V P <sub>OUT</sub> = 0 dBm, V <sub>MODE</sub> = +2.85 V
ACLR1 at 5 MHz offset	- - -	-42.5 -41 -49	-38 -38 -38	dBc	P <sub>OUT</sub> = +27.5 dBm, V <sub>MODE</sub> = 0 V P <sub>OUT</sub> = +16 dBm, V <sub>MODE</sub> = +2.85 V P <sub>OUT</sub> = 0 dBm, V <sub>CC</sub> = 1.5 V, V <sub>MODE</sub> = +2.85 V
ACLR2 at 10 MHz offset	- - -	-61 -58 -55	-50 -50 -48	dBc	P <sub>OUT</sub> = +27.5 dBm, V <sub>MODE</sub> = 0 V P <sub>OUT</sub> = +16 dBm, V <sub>MODE</sub> = +2.85 V P <sub>OUT</sub> = 0 dBm, V <sub>CC</sub> = 1.5 V, V <sub>MODE</sub> = +2.85 V
Power-Added Efficiency	35 8 1.0	37 9 1.4	- - -	%	P <sub>OUT</sub> = +27.5 dBm, V <sub>MODE</sub> = 0 V P <sub>OUT</sub> = +16 dBm, V <sub>MODE</sub> = +2.85 V P <sub>OUT</sub> = 0 dBm, V <sub>CC</sub> = 1.5 V, V <sub>MODE</sub> = +2.85 V
Quiescent Current (I <sub>q</sub> )	-	50	65	mA	V <sub>MODE</sub> = +2.85 V
Reference Current	-	3.5	5	mA	through V <sub>REF</sub> pin
Mode Control Current	-	0.3	0.5	mA	through V <sub>MODE</sub> pin, V <sub>MODE</sub> = +2.85 V
Leakage Current	-	<1	5	μA	V <sub>CC</sub> = +4.2 V, V <sub>REF</sub> = 0 V, V <sub>MODE</sub> = 0 V
Noise in Receive Band	-	-137	-134	dBm/Hz	1930 MHz to 1990 MHz
Harmonics 2fo 3fo, 4fo	- - -	-45 -50	-30 -30	dBc	P <sub>OUT</sub> ≤ +27.5 dBm, V <sub>MODE</sub> = 0 V
Input Impedance	-	-	2:1	VSWR	
Spurious Output Level (all spurious outputs)	-	-	-70	dBc	P <sub>OUT</sub> ≤ +27.5 dBm In-band load VSWR < 8:1 Out-of-band load VSWR < 8:1 Applies over all voltage and temperature operating ranges
Load mismatch stress with no permanent degradation or failure	8:1	-	-	VSWR	V <sub>CC</sub> = +5.0 V, P <sub>IN</sub> = +5 dBm Applies over full operating temperature range

**APPLICATION INFORMATION**

To ensure proper performance, refer to all related Application Notes on the ANADIGICS web site: <http://www.anadigics.com>

**Shutdown Mode**

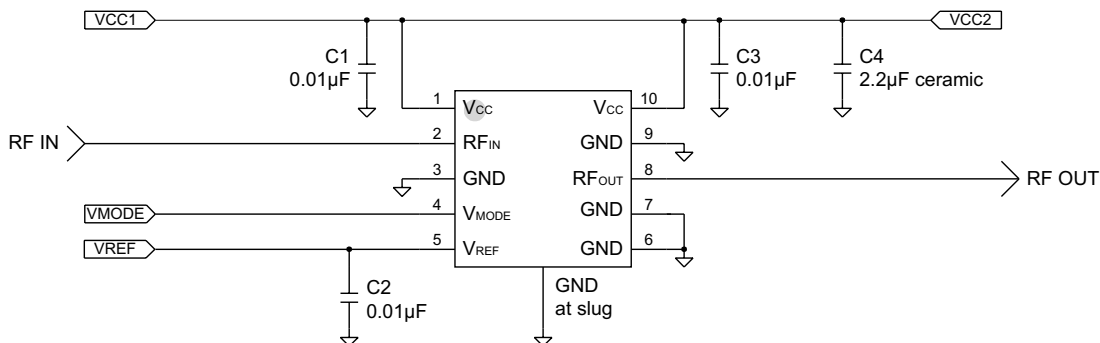
The power amplifier may be placed in a shutdown mode by applying logic low levels (see Operating Ranges table) to both the  $V_{REF}$  and  $V_{MODE}$  voltages.

**Bias Modes**

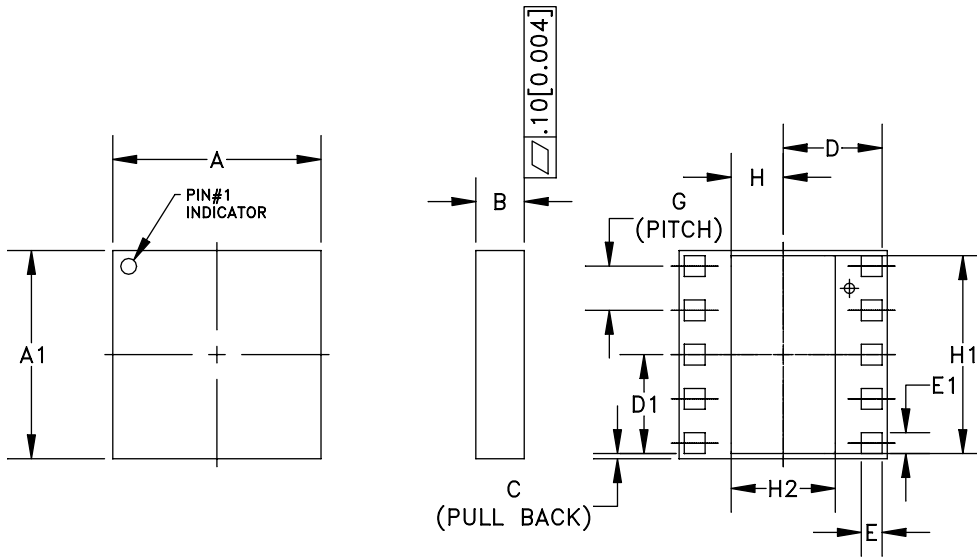
The power amplifier may be placed in either a Low Bias mode or a High Bias mode by applying the appropriate logic level (see Operating Ranges table) to the  $V_{MODE}$  voltage. The Bias Control table lists the recommended modes of operation for various applications.

**Table 5: Bias Control**

APPLICATION	$P_{OUT}$ LEVELS	BIAS MODE	$V_{REF}$	$V_{MODE}$
CDMA - low power	$\leq +16\text{dBm}$	Low	+2.85 V	+2.85 V
CDMA - high power	$> +16\text{ dBm}$	High	+2.85 V	0 V
Shutdown	-	Shutdown	0 V	0 V

**Figure 3: Application Circuit Schematic**

PACKAGE OUTLINE



S <sub>NO.</sub>	MILLIMETERS			INCHES			NOTE
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
A	3.88	4.00	4.12	0.152	0.157	0.162	-
A1	3.88	4.00	4.12	0.152	0.157	0.162	-
B	1.26	1.41	1.56	0.049	0.055	0.061	-
C	-	0.10	-	-	0.004	-	-
D	-	1.90	-	-	0.075	-	-
D1	-	1.90	-	-	0.075	-	-
E	0.35	0.40	0.45	0.013	0.015	0.017	-
E1	0.35	0.40	0.45	0.013	0.015	0.017	-
G	0.85 BSC			0.033 BSC			-
H	-	1.00	-	-	0.039	-	-
H1	-	3.80	-	-	0.149	-	-
H2	-	2.00	-	-	0.078	-	-

NOTES:

1. CONTROLLING DIMENSIONS: MILLIMETERS
2. UNLESS SPECIFIED TOLERANCE=±0.076[0.003].

Figure 4: M7 Package Outline - 10 Pin 4mm x 4mm Surface Mount Module

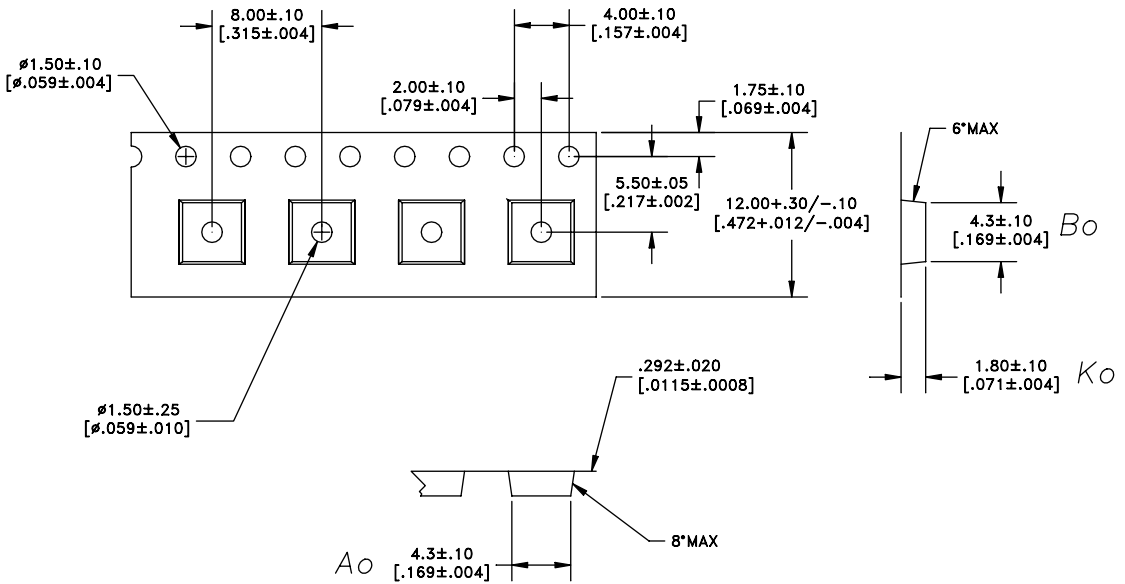


NOTES:

1. ANADIGICS LOGO SIZE: X=0.040±0.010 Y=0.048±0.010
2. PART # AWT6251
3. YEAR AND WORK WEEK: YYYY: YY = YEAR, WW = WORK WEEK
4. LOT - WAFER I.D.: LLLLL - SS = WAFER / LOT I.D.
5. PIN 1 INDICATOR: MOLD NOTCH -or- INK DOT
6. BOM # BBB
7. COUNTRY CODE: CCCCC
8. TYPE : ELITE  
SIZE : AS LARGE AS POSSIBLE  
LASER MARKED

Figure 5: Branding Specification

**COMPONENT PACKAGING**



DIMENSIONS ARE IN MILLIMETERS [INCHES]  
STANDARD TOLERANCES

**Figure 6: Tape & Reel Packaging**

**Table 6: Tape & Reel Dimensions**

PACKAGE TYPE	TAPE WIDTH	POCKET PITCH	REEL CAPACITY	MAX REEL DIA
4mm X 4mm	12mm	8mm	2500	13"

**AWT6251**

SUNSTAR微波光电 <http://www.rfoe.net/> TEL:0755-83396822 FAX:0755-83376182 E-MAIL:szss20@163.com

**ORDERING INFORMATION**

ORDER NUMBER	TEMPERATURE RANGE	PACKAGE DESCRIPTION	COMPONENT PACKAGING
AWT6251M7P8	-30 °C to +110 °C	10 Pin 4mm x 4mm Surface Mount Module	Tape and Reel, 2500 pieces per Reel



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