

# DATA SHEET

## **BLV2044** UHF power transistor

Product specification  
Supersedes data of 1996 Feb 09

1996 Nov 14

# UHF power transistor

# BLV2044

### FEATURES

- Emitter ballasting resistors for optimum temperature profile
- Gold metallization ensures excellent reliability
- Internal input and output matching to achieve high power gain and collector efficiency for an easy design of wideband circuits.

### APPLICATIONS

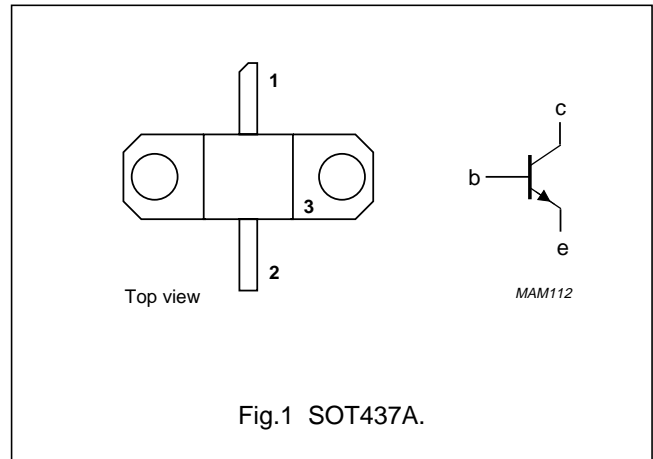
- Common emitter class-AB operation in base station transmitters in the 1800 to 2000 MHz frequency range.

### DESCRIPTION

NPN silicon planar transistor in a 2-lead SOT437A flange package with a ceramic cap. The emitter is connected to the flange.

### PINNING - SOT437A

PIN	SYMBOL	DESCRIPTION
1	c	collector
2	b	base
3	e	emitter, connected to flange



### QUICK REFERENCE DATA

RF performance at  $T_h = 25\text{ °C}$  in a common emitter test circuit.

MODE OF OPERATION	f (MHz)	$V_{CE}$ (V)	$P_L$ (W)	$G_p$ (dB)	$\eta_c$ (%)	$d_{im}$ (dBc)
CW, class-AB	1950	26	15	$\geq 8$	$\geq 40$	–
CW, class-AB	1990	26	15	$\geq 8$	$\geq 40$	–
2-tone, class-AB	$f_1 = 1950; f_2 = 1950.1$	26	15 (PEP)	typ. 8.5	typ. 35	typ. –30

### WARNING

#### Product and environmental safety - toxic materials

This product contains beryllium oxide. The product is entirely safe provided that the BeO disc is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with the general or domestic waste.

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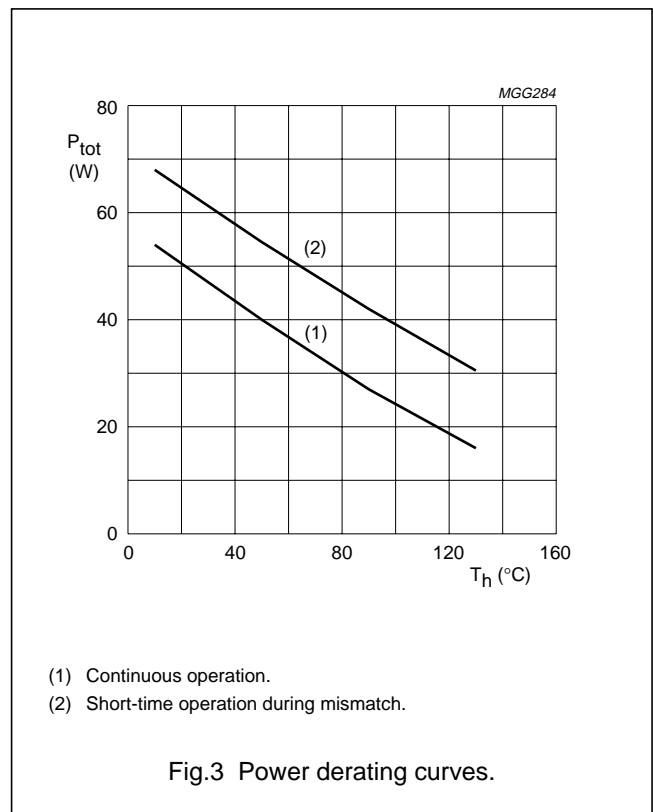
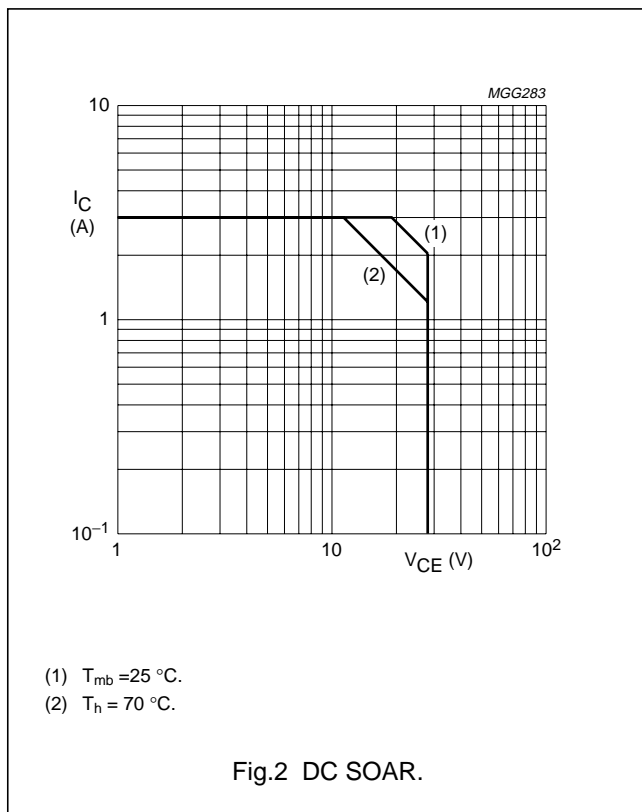
## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter	–	60	V
$V_{CEO}$	collector-emitter voltage	open base	–	28	V
$V_{EBO}$	emitter-base voltage	open collector	–	2.5	V
$I_C$	collector current (DC)		–	3	A
$I_{C(AV)}$	average collector current		–	3	A
$P_{tot}$	total power dissipation	$T_{mb} = 25\text{ °C}$	–	57	W
$T_{stg}$	storage temperature		–65	+150	°C
$T_j$	operating junction temperature		–	200	°C

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-mb}$	thermal resistance from junction to mounting base	$P_{tot} = 57\text{ W}; T_{mb} = 25\text{ °C}$	3.07	K/W
$R_{th\ mb-h}$	thermal resistance from mounting base to heatsink		0.4	K/W



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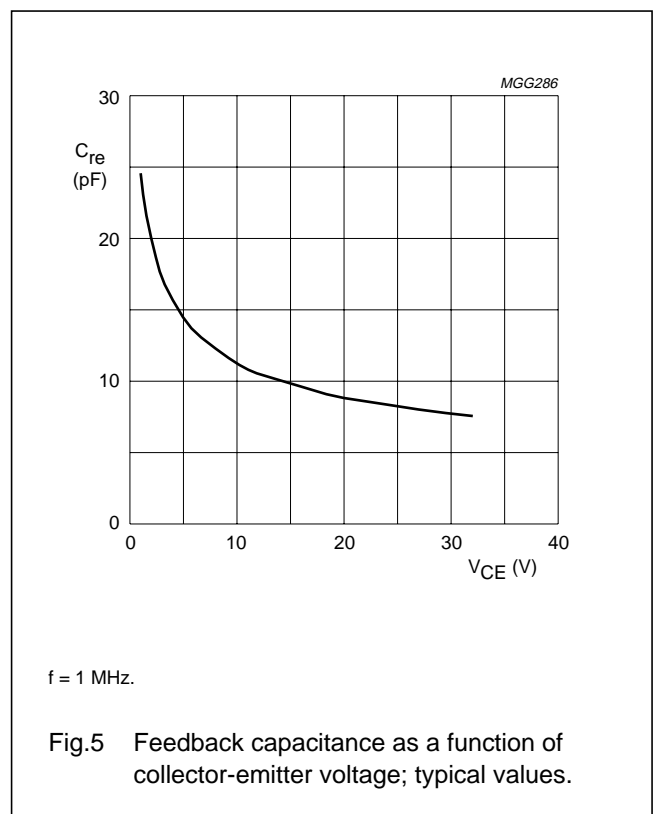
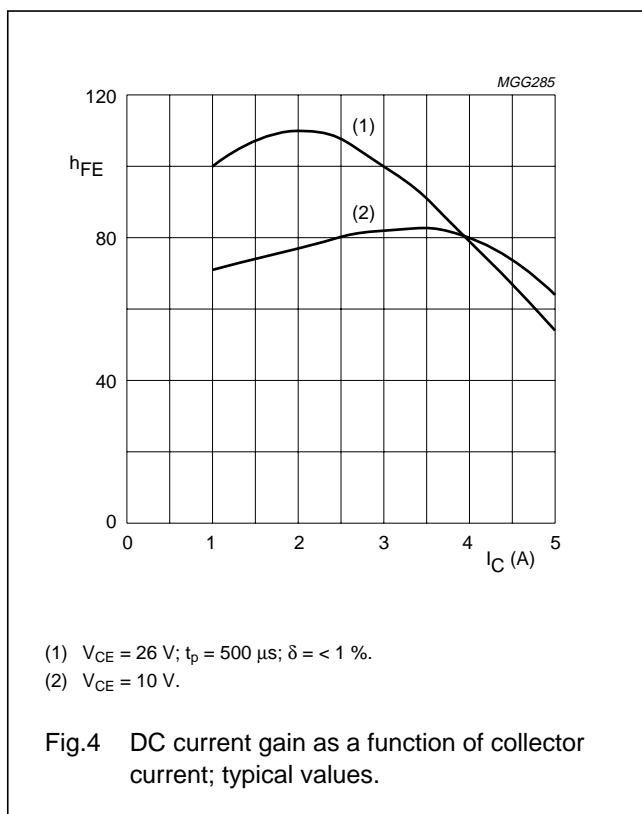
**CHARACTERISTICS**

$T_j = 25\text{ }^\circ\text{C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{(BR)CBO}$	collector-base breakdown voltage	open emitter; $I_C = 20\text{ mA}$	60	–	–	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	open base; $I_C = 10\text{ mA}$	28	–	–	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	open collector; $I_E = 0.5\text{ mA}$	2.5	–	–	V
$I_{CES}$	collector leakage current	$V_{CE} = 12.5\text{ V}$ ; $V_{BE} = 0$	–	–	4	mA
$h_{FE}$	DC current gain	$V_{CE} = 26\text{ V}$ ; $I_C = 1\text{ A}$	45	100	120	
$C_c$	collector capacitance	$V_{CB} = 26\text{ V}$ ; $I_E = i_e = 0$ ; $f = 1\text{ MHz}$ ; note 1	–	16	–	pF
$C_{re}$	feedback capacitance	$V_{CE} = 26\text{ V}$ ; $I_C = 0$ ; $f = 1\text{ MHz}$	–	8	–	pF

**Note**

1. Capacitance of die only.



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## APPLICATION INFORMATION

RF performance at  $T_h = 25\text{ }^\circ\text{C}$  in a common emitter test circuit.

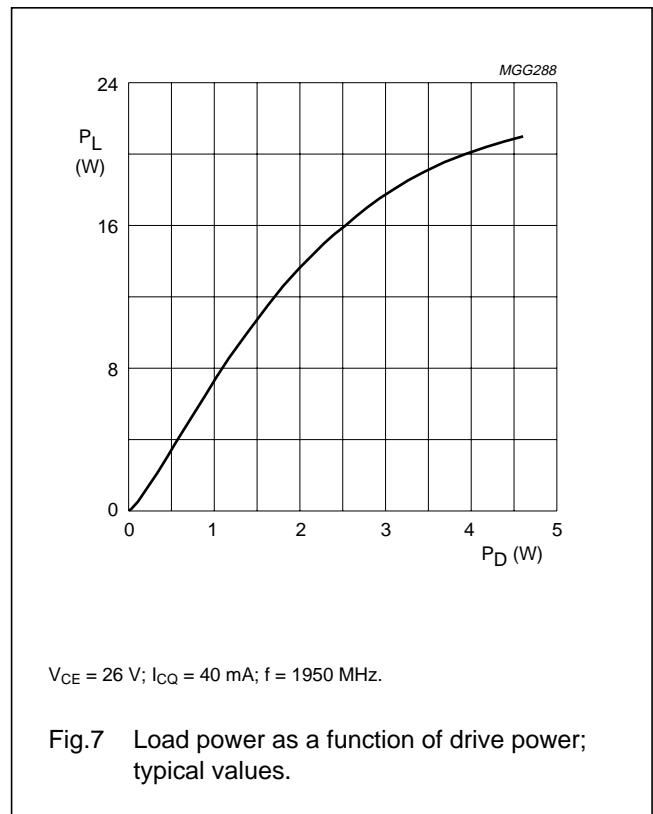
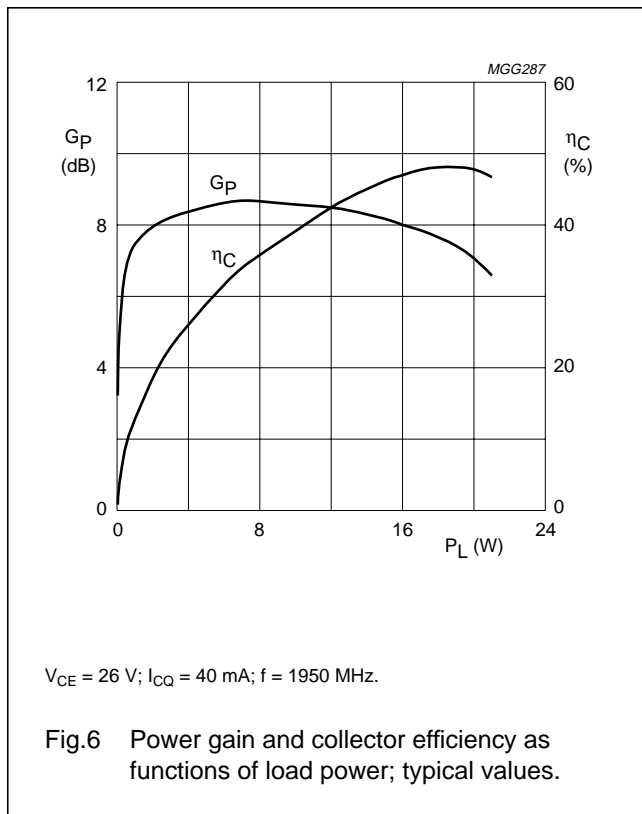
MODE OF OPERATION	f (MHz)	V <sub>CE</sub> (V)	I <sub>CQ</sub> (mA)	P <sub>L</sub> (W)	G <sub>p</sub> (dB)	η <sub>c</sub> (%)	d <sub>im</sub> (dBc)
CW, class-AB	1950	26	40	15	≥8 typ. 8.5	≥40 typ. 45	–
CW, class-AB (note 1)	1990	26	40	15	≥8	≥40	–
2-tone, class-AB	f <sub>1</sub> = 1950, f <sub>2</sub> = 1950.1	26	40	15 (PEP)	typ. 8.5	typ. 35	typ. –30

### Note

1. See application note BLV2044.

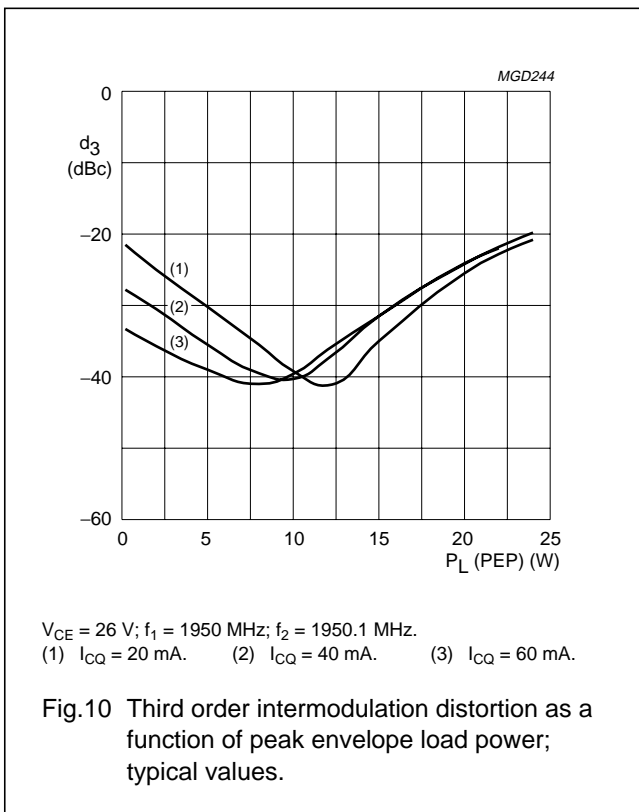
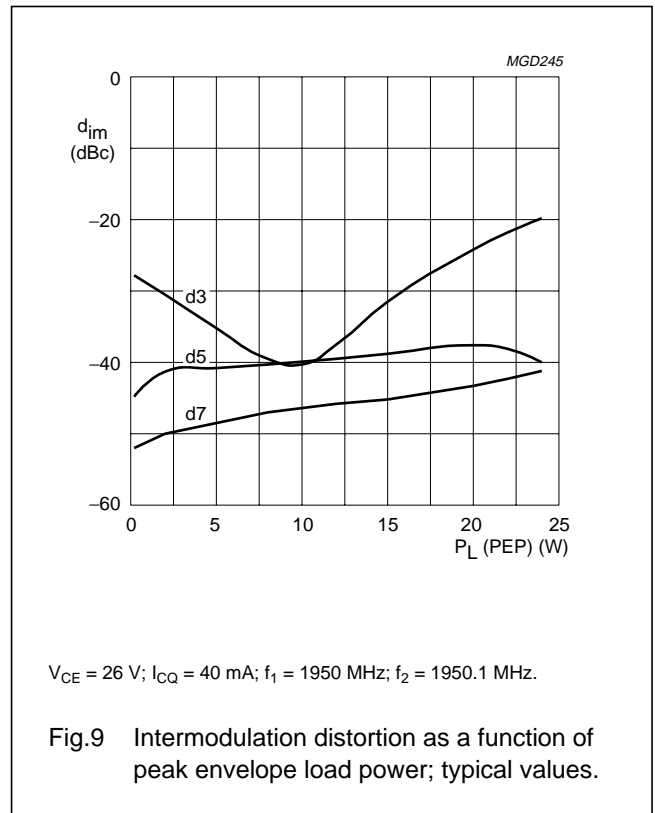
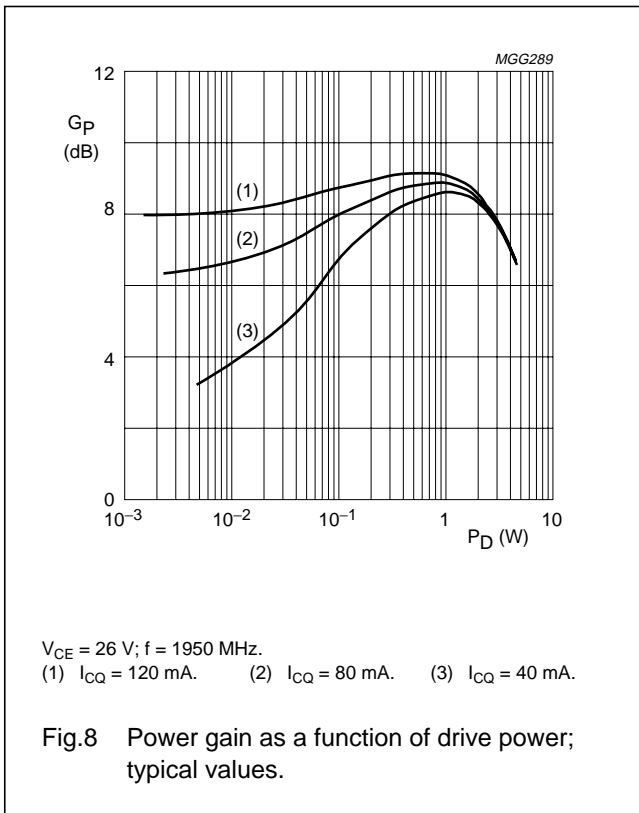
### Ruggedness in class-AB operation

The BLV2044 is capable of withstanding a load mismatch corresponding to VSWR = 5 : 1 through all phases under the following conditions: f = 1950 MHz; V<sub>CE</sub> = 26 V; I<sub>CQ</sub> = 40 mA; P<sub>L</sub> = 15 W; T<sub>mb</sub> = 25 °C.



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## Test circuit information

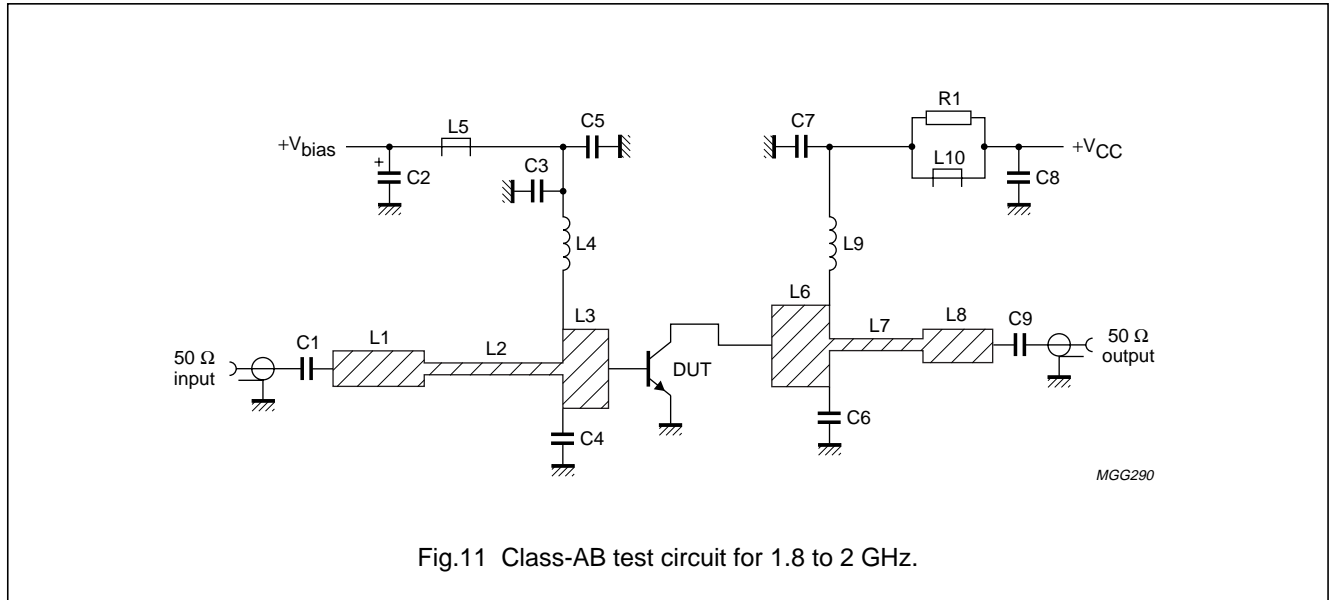


Fig.11 Class-AB test circuit for 1.8 to 2 GHz.

## List of components (see Figs 11 and 12)

COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE NO.
C1, C9	multilayer ceramic chip capacitor; note 1	30 pF		
C2	tantalum SMD capacitor	10 $\mu$ F; 35 V		
C3	multilayer ceramic chip capacitor	22 nF		2222 629 08223
C4	multilayer ceramic chip capacitor; note 1	1.1 pF		
C5, C7	multilayer ceramic chip capacitor; note 2	20 pF		
C6	multilayer ceramic chip capacitor; note 1	1.2 pF		
C8	multilayer ceramic chip capacitor	100 nF		2222 852 47104
L1	stripline; note 3	31 $\Omega$	length 7.8 mm width 2 mm	
L2	stripline; note 3	40 $\Omega$	length 8.8 mm width 1.4 mm	
L3	stripline; note 3	10 $\Omega$	length 8 mm width 8 mm	
L4	5 turns enamelled 1 mm copper wire	38 nH	length 8 mm int. dia. 3 mm	
L5, L10	grade 4S2 ferroxcube chip-bead			4330 030 36301
L6	stripline; note 3	12 $\Omega$	length 5 mm width 7 mm	
L7	stripline; note 3	40 $\Omega$	length 6.7 mm width 1.4 mm	

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COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE NO.
L8	stripline; note 3	23 $\Omega$	length 6.4 mm width 3 mm	
L9	2 turns enamelled 1 mm copper wire	9 nH	length 4 mm int. dia. 3 mm	
R1	metal film resistor	10 $\Omega$ ; 0.4 W		2311 153 51009

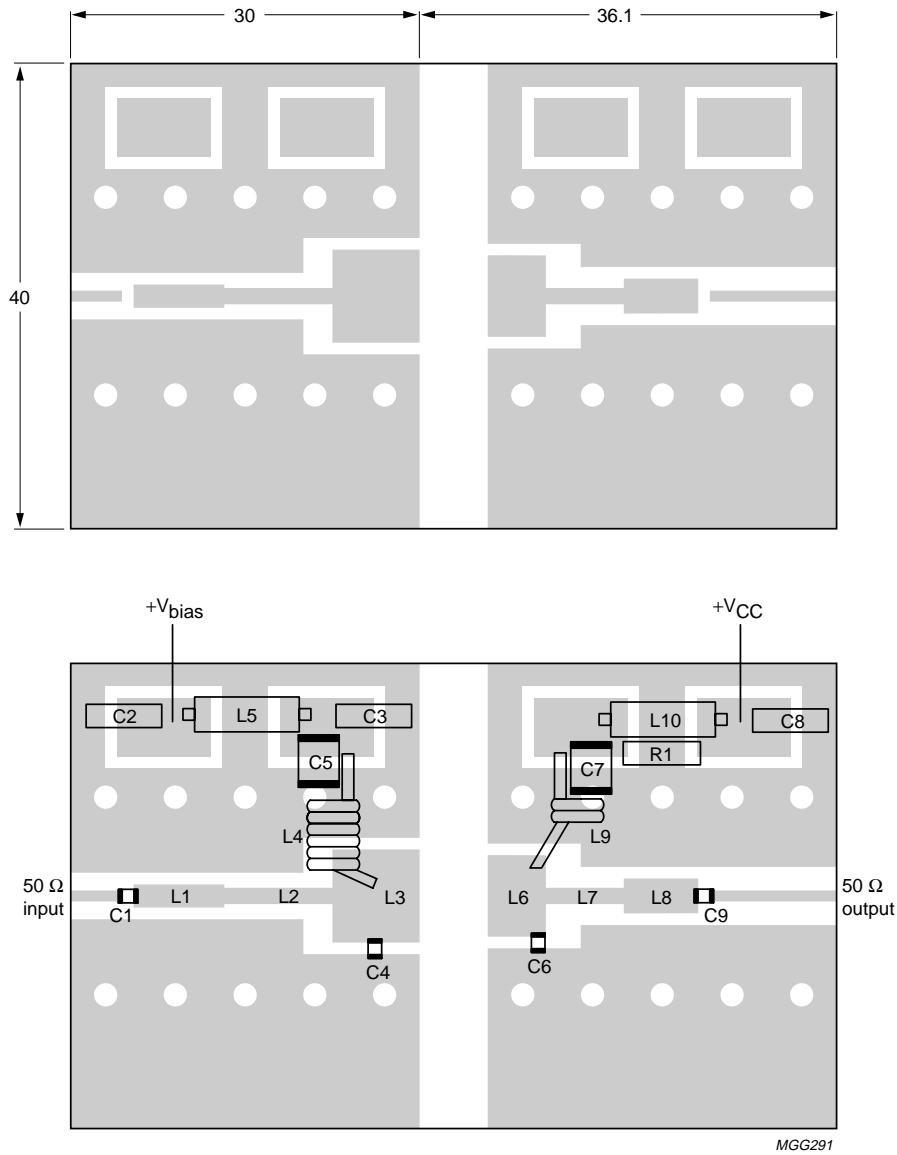
**Notes**

1. American Technical Ceramics type 100A or capacitor of the same quality.
2. American Technical Ceramics type 100B or capacitor of the same quality.
3. The striplines are on a double copper-clad printed-circuit board with epoxy fibre-glass dielectric ( $\epsilon_r = 6.15$ ); thickness 0.64 mm.



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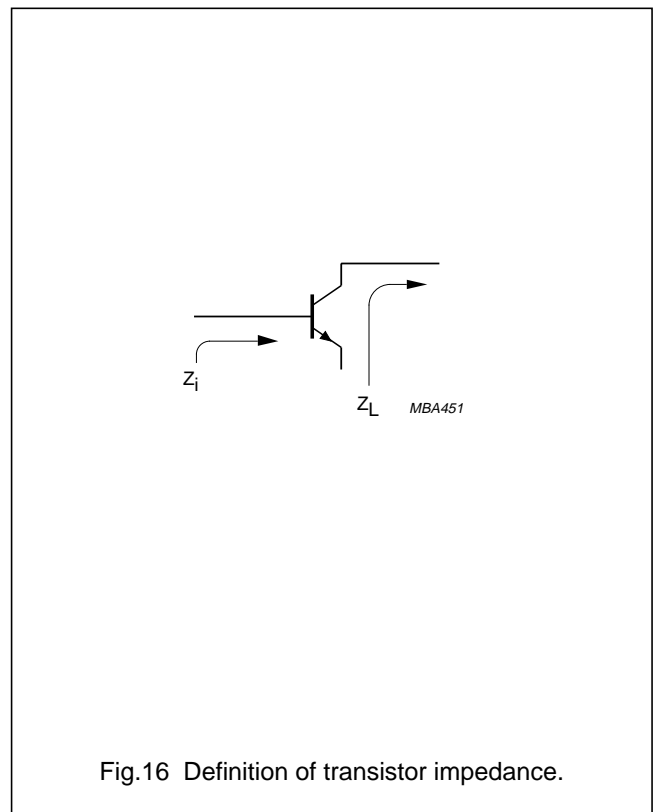
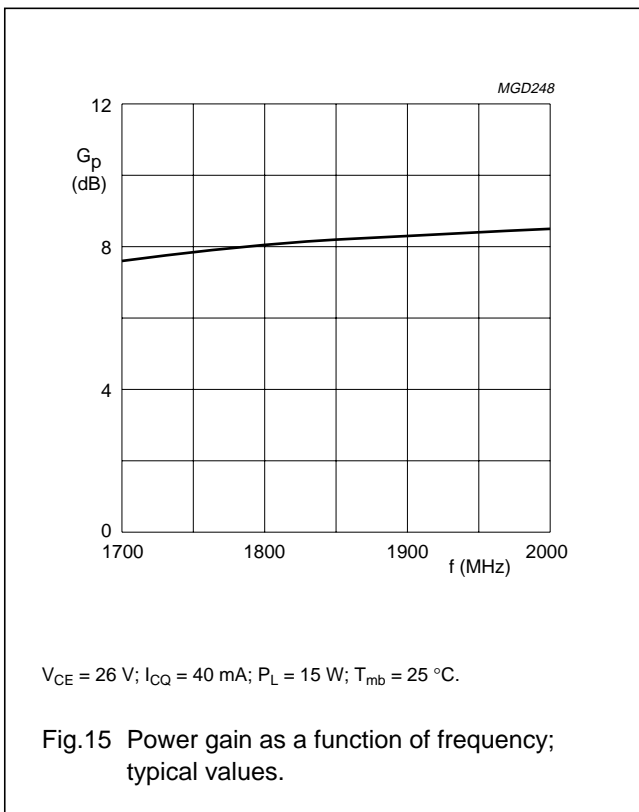
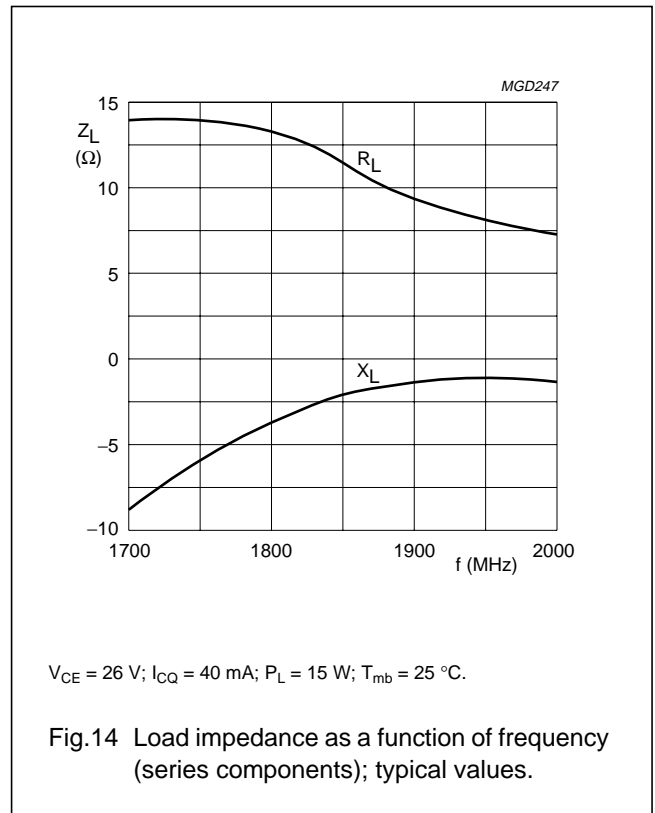
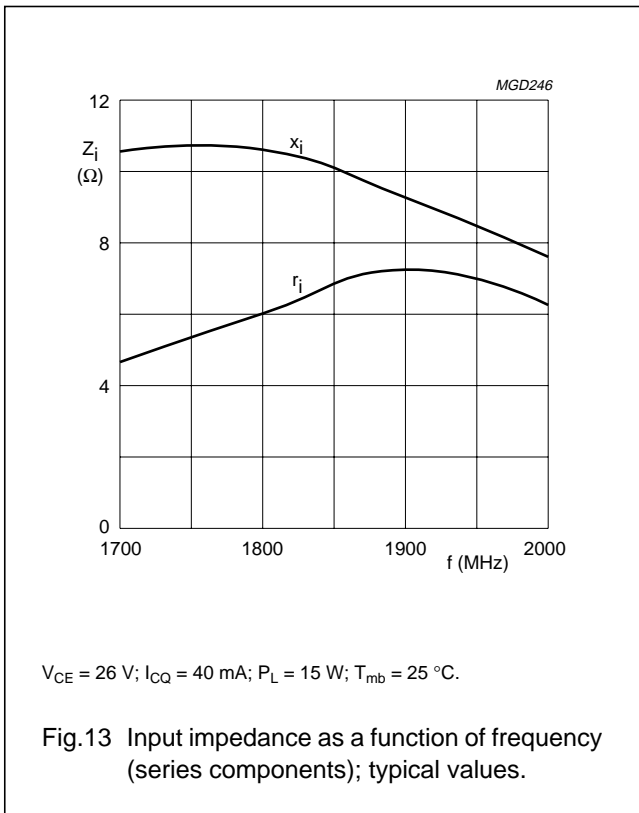
Dimensions in mm.

The components are situated on one side of the copper-clad epoxy fibre-glass board, the other side is not etched and serves as a ground plane. Earth connections from the component side to the ground plane are made by through metallization.

Fig.12 Component layout and printed-circuit board for 1.8 to 2 GHz class-AB test circuit.

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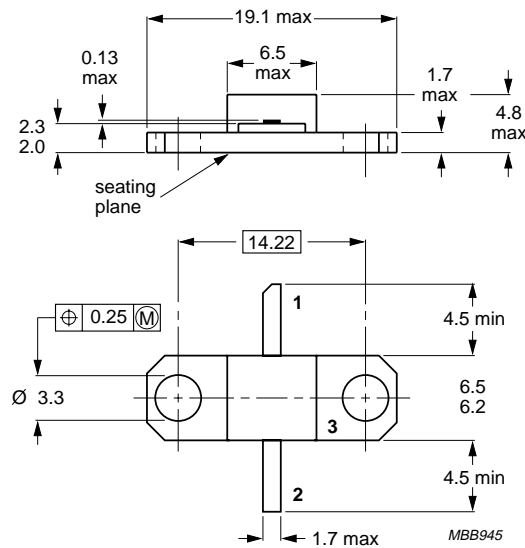
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PACKAGE OUTLINE



Dimensions in mm.  
 Recommended screw: M3.  
 Torque on screws: max. 0.5 Nm.

Fig.17 SOT437A.

DEFINITIONS

<b>Data Sheet Status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	

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Tel. +43 1 60 101, Fax. +43 1 60 101 1210

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