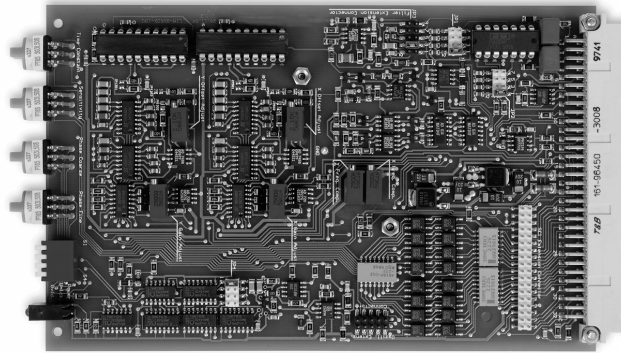


# Datasheet

# LIA-BV-150-H

## Single-Board Lock-In-Amplifier



Picture shows Lock-in Amplifier card with optional Mounting Kit LIA- MK- 2 (to be ordered separately)

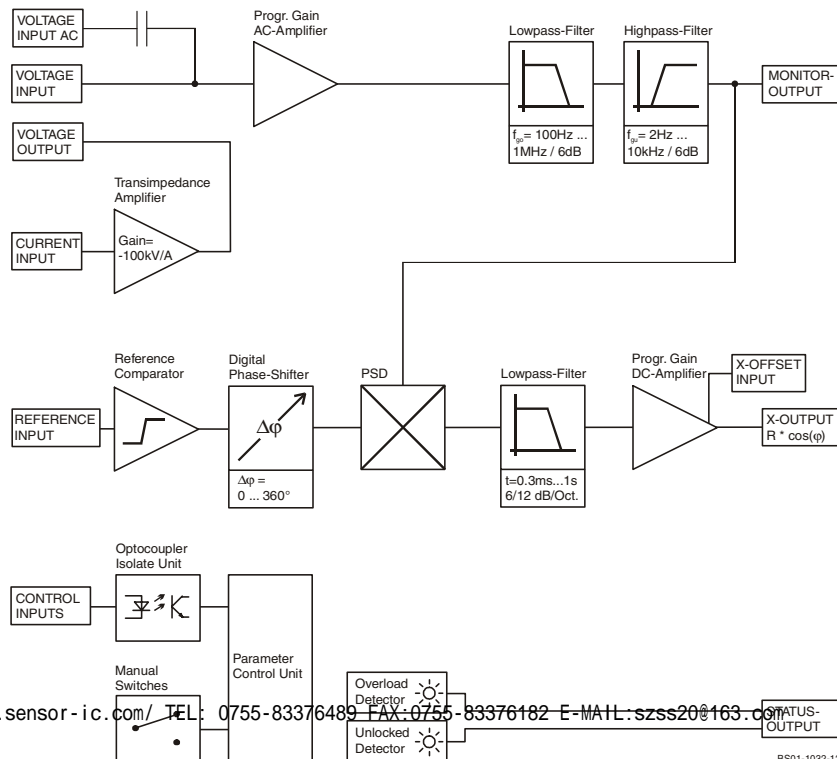
### Features

- **Single Phase Detection with X Output**
- **Working Frequency 50 Hz ... 120 kHz**
- **Digital Phase Shifter 0 ... 360°**
- **Current and Voltage Input**
- **Parameter Control by local Switches and opto-isolated digital Inputs**
- **Optional Mounting Kit and Reference Oscillator Modules available**

### Applications

- **Spectroscopy**
- **Luminescence, Fluorescence, Phosphorescence Measurements**
- **Light Scattering Measurements**
- **Opto-electronical Quality Control**
- **Integration in Industrial and Scientific Measurement-Systems**
- **Multi-Channel-Systems at moderate Costs**

### Block Diagram



**Datasheet****LIA-BV-150-H****Single-Board  
Lock-In-Amplifier**

Specifications	Test Conditions	$V_s = \pm 15\text{ V}$ , $T_a = 25^\circ\text{C}$																
Voltage Input	Voltage Input Characteristic Voltage Input Range Voltage Input Coupling Voltage Input Impedance Voltage Input Noise Voltage Input CMRR Voltage Input Gain Drift	True Differential Instrumentation-Amplifier 3 $\mu\text{V}$ ... 1V in 1-3-10 steps (for Full Scale Output) AC or DC (selectable at Connector) 1 $\text{M}\Omega$ // 4 pF 12 $\text{nV}/\sqrt{\text{Hz}}$ 110 dB @ 1 kHz, 100 dB @ 10 kHz 100 ppm/K																
Current Input	Current Input Characteristic Current Input Range Current Input Noise Current Input Source- Capacit. Current Input Gain Error vs. Source Capacitance	Transimpedance-Amplifier, -100 kV/A (inverting) 30 pA ... 10 $\mu\text{A}$ in 1-3-10 steps (for Full Scale Output) 0.4 $\text{pA}/\sqrt{\text{Hz}}$ 10 pF – 500 pF (recommended)  <table border="1"> <thead> <tr> <th><math>C_s</math></th> <th><math>f &lt; 20\text{ kHz}</math></th> <th><math>f = 50\text{ kHz}</math></th> <th><math>f = 100\text{ kHz}</math></th> </tr> </thead> <tbody> <tr> <td>10 pF</td> <td>&lt; 1 %</td> <td>1 %</td> <td>4 %</td> </tr> <tr> <td>100 pF</td> <td>&lt; 1 %</td> <td>1 %</td> <td>3 %</td> </tr> <tr> <td>500 pF</td> <td>&lt; 1 %</td> <td>4 %</td> <td>3 %</td> </tr> </tbody> </table>	$C_s$	$f < 20\text{ kHz}$	$f = 50\text{ kHz}$	$f = 100\text{ kHz}$	10 pF	< 1 %	1 %	4 %	100 pF	< 1 %	1 %	3 %	500 pF	< 1 %	4 %	3 %
$C_s$	$f < 20\text{ kHz}$	$f = 50\text{ kHz}$	$f = 100\text{ kHz}$															
10 pF	< 1 %	1 %	4 %															
100 pF	< 1 %	1 %	3 %															
500 pF	< 1 %	4 %	3 %															
Signal Filter	Signal Filter Lowpass (-3 dB BW) Signal Filter Highpass (-3 dB BW) Signal Filter Cutoff accuracy Max. Dynamic Reserve	1 MHz, 100 kHz, 10 kHz, 1 kHz, 100 Hz; 6 dB/Oct. selectable per jumper 2 Hz, 10 Hz, 100 Hz, 1 kHz, 10 kHz; 6 dB/Oct. selectable per jumper $\pm 20\%$ 80 dB																
Signal Monitor Output	Signal Monitor Output Gain Signal Monitor Output Voltage Signal Monitor Output Impedance Signal Monitor Output Current	1 ... 3333 (depends on Gain-Setting) $\pm 8\text{ V}$ max. 100 $\Omega$ $\pm 10\text{ mA}$ max.																
	Note	When using Current Input with low Input Ranges, the Monitor Output may be disabled by opening the soldering jumper at the Board (near JP1) to prevent from recoupling.																
Demodulator	Demodulator Dynamic Reserve	15 dB @ Ultra Stable Setting 35 dB @ Low Drift Setting 55 dB @ High Dynamic Setting																
Reference Input	Reference Input Voltage Range Reference Input Impedance Reference Acquisition Time	$\pm 100\text{ mV}$ ... $\pm 5\text{ V}$ @ bip. Mode (0 V Comparator Threshold) - 5 V / +10 V @ TTL Mode (+2 V Comparator Threshold) 1 $\text{M}\Omega$ max. 2 s @ Fast Setting max. 4 s @ Slow Setting																
Phase Shifter	Phase Shifter Type Phase Shifter Range Phase Shifter Resolution Phase Shifter Drift Phase Shifter Accuracy	Digital, Working Frequency 50 Hz ... 120 kHz 0 ... + 360 $^\circ$ 1.4 $^\circ$ @ $f < 60\text{ kHz}$ , 2.8 $^\circ$ @ $f > 60\text{ kHz}$ < 100 ppm/K < 0.3 $^\circ$																
Time Constants	Time Constant Range Time Const. Filter Characteristic	300 $\mu\text{s}$ ... 1 s in 1-3-10 steps 6 dB/Oct. or 12 dB/Oct. switchable																

SOPHISTICATED TOOLS FOR SIGNAL RECOVERY



# Datasheet

# LIA-BV-150-H

## Single-Board Lock-In-Amplifier

Specifications (continued) Output	Output Channels X = In Phase Output Voltage Range ± 10 V (@ 2 kΩ Load) Output Current ± 5 mA max. Output Impedance 50 Ω Output DC-Stability 5 ppm/K @ Ultra Stable Setting 50 ppm/K @ Low Drift Setting 500 ppm/K @ High Dynamic Setting Output Basic Accuracy 2 % @ sinusoidal input signal Output Voltage Offset Range ± 100 % Full Scale by ± 10 V Control Voltage Output Voltage Offset Control-Voltage Impedance > 2 kΩ																																							
Status Indicator LED	Functions Amplifier Overload Status Reference PLL Unlocked Status																																							
Digital Control	Control Input Voltage Low: - 0.8 V ... + 0.8 V, High: + 1.8 V ... + 12 V Control Input Current 0 mA @ 0V, 1.5 mA @ + 5 V, 4.5 mA @ + 12V typ. Digital Status Output Voltage Active: + 4.5 V typ., Non Active: 0 V typ. Digital Status Output Current 10 mA max.																																							
Power Supply	Supply Voltage ± 15 Vdc ... ± 18 Vdc Supply Current - 60 mA, + 120 mA																																							
Case	Board 19" Euro-Card, (100 mm x 160 mm Board) Weight 100 gr. (0.22 lbs)																																							
Temperature Range	Storage Temperature - 40 ... + 100 °C Operating Temperature 0 ... + 60 °C																																							
Absolute Maximum Ratings	Signal Input AC Voltage 50 Vpp Signal Input DC Voltage ± 70 V Reference Input Voltage ± 15 V Control Input Voltage - 5 V, + 15 V Power Supply Voltage ± 22 V																																							
Switch Settings	<table border="0"> <tr> <td>4 Dip Switch - Presettings</td> <td>Switch OFF</td> <td>ON</td> </tr> <tr> <td></td> <td>S1 Low Drift &amp; High Dynamic</td> <td>Ultra Stable &amp; Low Drift</td> </tr> <tr> <td></td> <td>S2 1-f Mode</td> <td>2-f Mode</td> </tr> <tr> <td></td> <td>S3 Fast PLL-Locking</td> <td>Slow PLL-Locking</td> </tr> <tr> <td></td> <td>S4 Reference-Input-Threshold = 0 V</td> <td>Reference-Input-Threshold = +2 V</td> </tr> </table> <table border="0"> <tr> <td rowspan="2">Sensitivity Setting, Output DC-Gain Modes</td> <td colspan="4">3 Output DC-Gain Modes are selectable:</td> </tr> <tr> <td>Mode</td> <td>DC-Gain</td> <td>Dyn. Reserve</td> <td>DC-Stability</td> </tr> <tr> <td></td> <td>Ultra Stable</td> <td>10</td> <td>Low</td> <td>High</td> </tr> <tr> <td></td> <td>Low Drift</td> <td>100</td> <td>Medium</td> <td>Medium</td> </tr> <tr> <td></td> <td>High Dynamic</td> <td>1000</td> <td>High</td> <td>Low</td> </tr> </table> <p>If only low dynamic reserve is required, select the higher DC-Stability settings. Use Dip switch S1 to preselect either the two upper or the two lower DC-Gain modes, then select best mode by Sensitivity switch settings 0-7 or 8-F.</p>	4 Dip Switch - Presettings	Switch OFF	ON		S1 Low Drift & High Dynamic	Ultra Stable & Low Drift		S2 1-f Mode	2-f Mode		S3 Fast PLL-Locking	Slow PLL-Locking		S4 Reference-Input-Threshold = 0 V	Reference-Input-Threshold = +2 V	Sensitivity Setting, Output DC-Gain Modes	3 Output DC-Gain Modes are selectable:				Mode	DC-Gain	Dyn. Reserve	DC-Stability		Ultra Stable	10	Low	High		Low Drift	100	Medium	Medium		High Dynamic	1000	High	Low
4 Dip Switch - Presettings	Switch OFF	ON																																						
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	Low Drift	100	Medium	Medium																																				
	High Dynamic	1000	High	Low																																				

# Datasheet

# LIA-BV-150-H

## Single-Board Lock-In-Amplifier

Switch Settings (continued)

S1 = ON: Sensitivity Setting for Full Scale (= 10 V Output)

Ultra Stable Mode

Setting	Voltage	Current
0	1 V	10 $\mu$ A
1	300 mV	3 $\mu$ A
2	100 mV	1 $\mu$ A
3	30 mV	300 nA
4	10 mV	100 nA
5	3 mV	30 nA
6	1 mV	10 nA
7	300 $\mu$ V	3 nA

Low Drift Mode

Setting	Voltage	Current
8	100 mV	1 $\mu$ A
9	30 mV	300 nA
A	10 mV	100 nA
B	3 mV	30 nA
C	1 mV	10 nA
D	300 $\mu$ V	3 nA
E	100 $\mu$ V	1 nA
F	30 $\mu$ V	300 pA

S1 = OFF: Sensitivity Setting for Full Scale (= 10 V Output)

Low Drift Mode

Setting	Voltage	Current
0	100 mV	1 $\mu$ A
1	30 mV	300 nA
2	10 mV	100 nA
3	3 mV	30 nA
4	1 mV	10 nA
5	300 $\mu$ V	3 nA
6	100 $\mu$ V	1 nA
7	30 $\mu$ V	300 pA

High Dynamic Mode

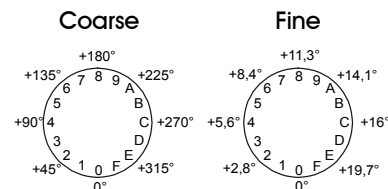
Setting	Voltage	Current
8	10 mV	100 nA
9	3 mV	30 nA
A	1 mV	10 nA
B	300 $\mu$ V	3 nA
C	100 $\mu$ V	1 nA
D	30 $\mu$ V	300 pA
E	10 $\mu$ V	100 pA
F	3 $\mu$ V	30 pA

Time Constant Setting

Setting	6 dB/Oct.	12 dB/Oct.	Time Constant
0	8		300 $\mu$ s
1	9		1 ms
2	A		3 ms
3	B		10 ms
4	C		30 ms
5	D		100 ms
6	E		300 ms
7	F		1 s

Phase Shift Setting

Phase shift is adjusted by 2 phase switches with 8 Bit resolution. Values 0 ... 255 (Hex 00 ... FF) correspond to phase shift setting 0 ... +360 °. One step with switch marked "Coarse" changes phase shift by 22.5 °. The "Fine"-switch changes phase shift by 1.4 ° - steps:



If Frequency Range  $f > 60$  kHz or 2-f Mode is selected, the resolution of digital phase control changes to 2.8 ° and the phase shift range doubles to 0 ... + 720 °.

Jumper Settings

Input Signal Filter

Set Cut-Off Frequency of Input Lowpass Filter

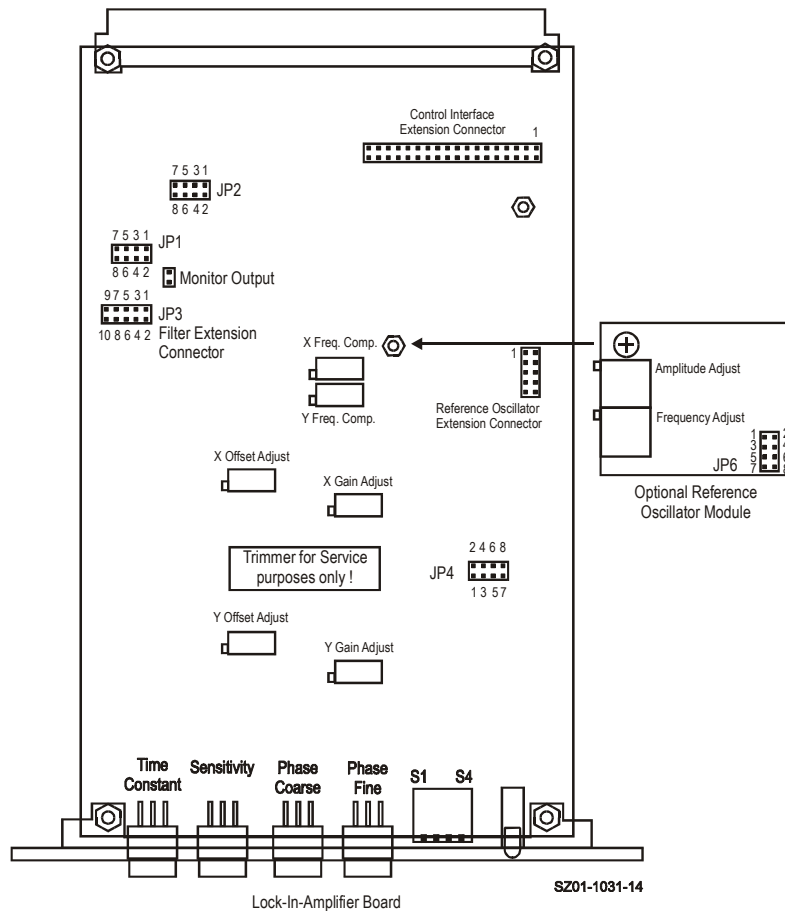
# Datasheet

# LIA-BV-150-H

## Single-Board Lock-In-Amplifier

Setting	with JP1 + JP2 (always same position) and Highpass Filter with JP3:			
	JP3	Highpass -3 dB Cut-Off	JP1, JP2	Lowpass -3 dB Cut-Off
	3 - 4	2 Hz	1 - 2	100 Hz
	1 - 3	10 Hz	3 - 4	1 kHz
	2 - 4	100 Hz	5 - 6	10 kHz
	3 - 5	1 kHz	7 - 8	100 kHz
	4 - 6	10 kHz	none	1 MHz *
	* (At Sensitivity Settings 6,7 & E,F max. 200 kHz)			
Frequency Range Selection	JP4	Frequency Range		
	1 - 2	f < 60 kHz		
	3 - 4 & 5 - 6	f > 60 kHz		
	7, 8	test pins, do not use		
	(if 2-f mode is used, position is always 1-2)			

Jumper Position Diagram



**Datasheet****LIA-BV-150-H****Single-Board  
Lock-In-Amplifier**

Connector	Connector Type	Euro-Card DIN 41612 Connector, 64 pin male, (a+c)	
	Input	Pin C2:	Voltage Input, Non Inverting, DC-Coupled
		Pin C3:	Voltage Input, Non Inverting, AC-Coupled
		Pin C4:	Voltage Input, Inverting, AC-Coupled
		Pin C5:	Voltage Input, Inverting, DC-Coupled
		Pin C7:	Current Input
		Pin C6:	Current Amplifier Voltage Output
		Pin A2- A6:	Input GND
	Monitor Output	Pin C9:	Monitor Output
		Pin A9:	Monitor GND
	Output	Pin C14:	X-Signal Output
		Pin C15:	Output GND
	Offset Input	Pin A10:	X-Offset Input
		Pin A13:	Offset GND
	Status Output	Pin C10:	Unlocked Status Output
		Pin C11:	Overload Status Output
		Pin C17:	Status Output GND (=Power Supply GND)
	Power Supply	Pin A16+C16:	Power Supply – 15V
		Pin A18+C18:	Power Supply + 15V
		Pin A17+C17:	Power Supply GND
	Remote Control Inputs (Opto-Isolated)	Pin C19:	Time Constant (TC0)
		Pin A19:	Time Constant (TC1)
		Pin C20:	Time Constant (TC2)
		Pin A20:	Time Constant Slope (TCSL)
		Pin A22:	Sensitivity (SEN0)
		Pin C21:	Sensitivity (SEN1)
		Pin A21:	Sensitivity (SEN2)
		Pin C22:	Dynamic Mode (DYN0)
		Pin A28:	Phase Shift (PH0)
		Pin C28:	Phase Shift (PH1)
		Pin A27:	Phase Shift (PH2)
		Pin C27:	Phase Shift (PH3)
		Pin A26:	Phase Shift (PH4)
		Pin C26:	Phase Shift (PH5)
		Pin A25:	Phase Shift (PH6)
		Pin C25:	Phase Shift (PH7)
		Pin C24:	Disable Local Switch Control
		Pin A23+A24:	Remote Control GND (Common Optocoupler Cathode)
	Reference Input	Pin A32:	Reference Input
		Pin A31:	Reference Input Ground
	Reference Output (Connected only if optional Oscillator Module is installed)	Pin A30:	Reference Output
		Pin A17:	Refer. Output GND (=Power Supply GND)
		Pin A29:	Reference Synchronization Input
	Standard Control Interface (Connected only if optional Control Interface Module (future product) is installed)	Pin C29:	Interface 0
		Pin C30:	Interface 1
		Pin C31:	Interface 2
		Pin C32:	Interface 3

## Single-Board Lock-In-Amplifier

## Remote Control Operation

## General

Remote Control Input Bits are opto-isolated and connected by logical OR to local switch setting.

The 4 hexadecimal switches are 4 Bit-coded as shown in the following table:

Switch Code	MSB			LSB
	Bit 3	Bit 2	Bit 1	Bit 0
0	Low	Low	Low	Low
1	Low	Low	Low	High
2	Low	Low	High	Low
3	Low	Low	High	High
4	Low	High	Low	Low
5	Low	High	Low	High
6	Low	High	High	Low
7	Low	High	High	High
8	High	Low	Low	Low
9	High	Low	Low	High
A	High	Low	High	Low
B	High	Low	High	High
C	High	High	Low	Low
D	High	High	Low	High
E	High	High	High	Low
F	High	High	High	High

For remote control a switch setting, set the local switch to "0" and select the wanted setting via the 4-Bit-code at the corresponding digital inputs.

## Disable Local Switches

By forcing Input Bit "Disable Local Switch Control" (Pin C24) to "High", the LIA is set to exclusively remote control operation and the manual switches are out of function.

Sensitivity Switch -  
Corresponding Inputs

Bit	Corresponding Control Port Input	
Bit 0	SEN0	(Pin A22)
Bit 1	SEN1	(Pin C21)
Bit 2	SEN2	(Pin A21)
Bit 3	DYNO	(Pin C22)

Time Constant Switch -  
Corresponding Inputs

Bit	Corresponding Control Port Input	
Bit 0	TC0	(Pin C19)
Bit 1	TC1	(Pin A19)
Bit 2	TC2	(Pin C20)
Bit 3	TCSL	(Pin A20)

Phase Switch Coarse -  
Corresponding Inputs

Bit	Corresponding Control Port Input	
Bit 0	PH4	(Pin A26)
Bit 1	PH5	(Pin C26)
Bit 2	PH6	(Pin A25)
Bit 3	PH7	(Pin C25)

Phase Switch Fine -  
Corresponding Inputs

Bit	Corresponding Control Port Input	
Bit 0	PH0	(Pin A28)
Bit 1	PH1	(Pin C28)
Bit 2	PH2	(Pin A27)
Bit 3	PH3	(Pin C27)

# Datasheet

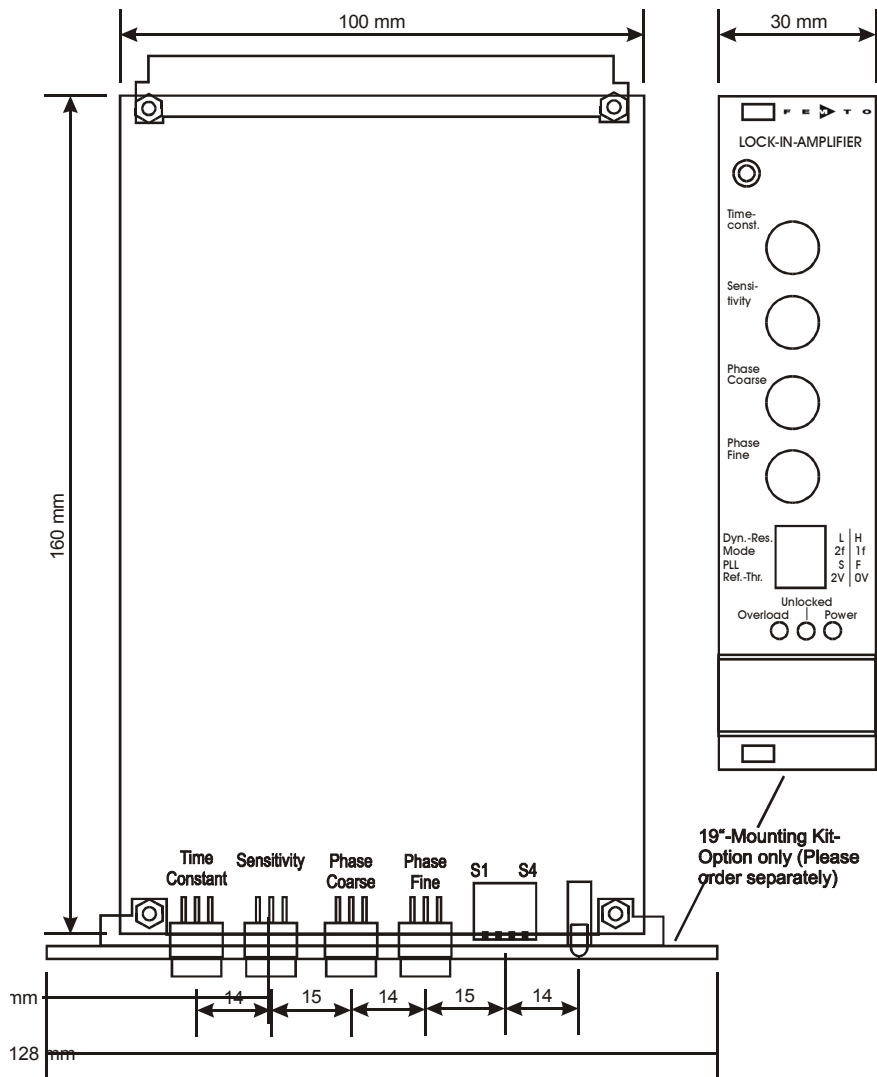
# LIA-BV-150-H

## Single-Board Lock-In-Amplifier

### Remote Control Example

For example, to select a switch setting code "6", you have to connect a "High"- level signal to the corresponding control input pins Bit 1 & Bit 2. Mixed operation, e.g. local phase settings and remote controlled sensitivity setting, is also possible when "Disable Local Switch Control" (Pin C24) is not active ("Low" or just not connected).

### Dimensions





**Datasheet****LIA-BV-150-H****Single-Board  
Lock-In-Amplifier**

Optional Extensions

Mounting Kit

Model No.: MK-LIA-2

- 19" – Frontpanel, printed
- EMI – shielding Board-Backplane

Reference Oscillator Module

Model No.: SOM-1

- Frequency Range 5 Hz ... 130 kHz, User adjustable
- Output Voltage 0 ... 2 Vrms, User adjustable
- 100 ppm/K Amplitude Accuracy

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**SOPHISTICATED TOOLS FOR SIGNAL RECOVERY**



**Datasheet**

**LUCI-10**

**USB to D-Sub Control Interface  
for FEMTO Amplifiers**



<p>Features</p>	<ul style="list-style-type: none"> <li>• <b>Compact Digital I/O Interface for USB Remote Control of FEMTO Amplifiers</b></li> <li>• <b>Supports Opto-Isolation of Amplifier Signal Path from PC USB Port</b></li> <li>• <b>16 Digital Outputs, 3 Opto-Isolated Digital Inputs</b></li> <li>• <b>Bus-Powered Operation</b></li> <li>• <b>System Driver, Application Software and VI's for use with LabVIEW™ Included</b></li> </ul>
<p>Applications</p>	<ul style="list-style-type: none"> <li>• <b>Remote Control of FEMTO® Amplifiers and Photoreceivers Directly from a PC</b></li> </ul>
<p>Block Diagram</p>	<p>The block diagram illustrates the system architecture. On the left, a 'Windows PC' is connected to the 'LUCI-10' device via a 'USB Type A' cable. The LUCI-10 is powered by '+ 5 V, Bus Powered' from the PC. Inside the LUCI-10, a 'Microcontroller' contains a 'USB Controller' and 'Digital I/O' blocks. The USB Controller handles communication with the PC. The Digital I/O block includes 'Digital Out 16 Bit' and 'Digital In 3 Bit' sections. The Digital Out section is connected to an 'LED' and a 'D-Sub 25 Pin Male' connector. The Digital In section is connected to an 'Opto-Isolation' block. The D-Sub connector is connected to a 'FEMTO Amplifier' via another 'Opto-Isolation' block. The amplifier provides 'Amplifier Control Bits' to the LUCI-10 and receives 'Amplifier Status Bits' from the LUCI-10. The diagram is labeled 'BS-LUCI-10_R1'.</p>

<p>Hardware Specifications</p>	<table border="0"> <tr> <td data-bbox="259 1617 470 1648">General Characteristics</td> <td data-bbox="535 1617 665 1648">Bus Interface</td> <td data-bbox="844 1617 1039 1648">USB 2.0 (full-speed)</td> </tr> <tr> <td></td> <td data-bbox="535 1648 730 1680">Digital I/O Channels</td> <td data-bbox="844 1648 1088 1701">16 output lines 3 opto-isolated input lines</td> </tr> <tr> <td></td> <td data-bbox="535 1701 617 1732">Supply</td> <td data-bbox="844 1701 1282 1764">PC USB port, + 5 V, typ. 100 mA, bus-powered (no auxiliary power supply required)</td> </tr> <tr> <td></td> <td data-bbox="535 1764 649 1795">Connectors</td> <td data-bbox="844 1764 958 1795">USB type A</td> </tr> <tr> <td></td> <td data-bbox="535 1795 600 1827">Cable</td> <td data-bbox="844 1795 1039 1848">D-Sub, 25 pin, male AWG 28, length 1.8 m</td> </tr> <tr> <td data-bbox="259 1869 324 1900">Output</td> <td data-bbox="535 1869 730 1900">Number of Channels</td> <td data-bbox="844 1869 1347 1932">16 output lines, supporting opto-isolation inside FEMTO amplifiers and photoreceivers</td> </tr> <tr> <td></td> <td data-bbox="535 1932 747 1963">Output Voltage Range</td> <td data-bbox="844 1932 1364 1984">LOW bit: 0 ... + 0.5 V (@ 0 ... 2 mA output current) HIGH bit: + 4 ... + 5.5 V (@ 0 ... 2 mA output current)</td> </tr> <tr> <td></td> <td data-bbox="535 1984 665 2047">Max. Current Writing Rate</td> <td data-bbox="844 1984 1153 2047">6 mA per channel max. 800 operations per second</td> </tr> </table>	General Characteristics	Bus Interface	USB 2.0 (full-speed)		Digital I/O Channels	16 output lines 3 opto-isolated input lines		Supply	PC USB port, + 5 V, typ. 100 mA, bus-powered (no auxiliary power supply required)		Connectors	USB type A		Cable	D-Sub, 25 pin, male AWG 28, length 1.8 m	Output	Number of Channels	16 output lines, supporting opto-isolation inside FEMTO amplifiers and photoreceivers		Output Voltage Range	LOW bit: 0 ... + 0.5 V (@ 0 ... 2 mA output current) HIGH bit: + 4 ... + 5.5 V (@ 0 ... 2 mA output current)		Max. Current Writing Rate	6 mA per channel max. 800 operations per second
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	Max. Current Writing Rate	6 mA per channel max. 800 operations per second																							



## USB to D-Sub Control Interface for FEMTO Amplifiers

### Software Specifications

Software  
(included on CD)

Device Driver	dynamic link library (DLL) for integration in Microsoft Windows® operating system for use with C/C++, LabWindows™ /CVI™ or LabVIEW™
Application Software	GUI (graphical user interface) programs for simple remote control of FEMTO amplifiers and photoreceivers provided as executable programs and LabVIEW projects
LabVIEW Programs	sample programs to control and test the LUCI-10 hardware (including front panel and block diagram)
LabVIEW Library	special VI toolkit for integration in LabVIEW development environment

**Note:** A National Instruments LabVIEW™ license is not included in this software package. For use of the GUI application programs the LabVIEW Run-Time Engine is required. If not detected on the host PC during the installation process the LabVIEW Run-Time Engine will be installed automatically from the CD.

### System Requirements

Operating System	Microsoft Windows XP with Service Pack 2, or higher
Processor	Intel Pentium III or AMD Athlon, or better
System Memory	512 MB of RAM, or more
Hard Disk Space	about 200 MB
Interface Port	USB 1.1 or USB 2.0
Supported FEMTO Modules	any standard FEMTO amplifier or photoreceiver with 25 pin D-Sub socket, except model HLVA-100

### Optional Requirements

For development of own application programs an additional development environment like LabVIEW Version 8 (or higher) or C/C++ is required.

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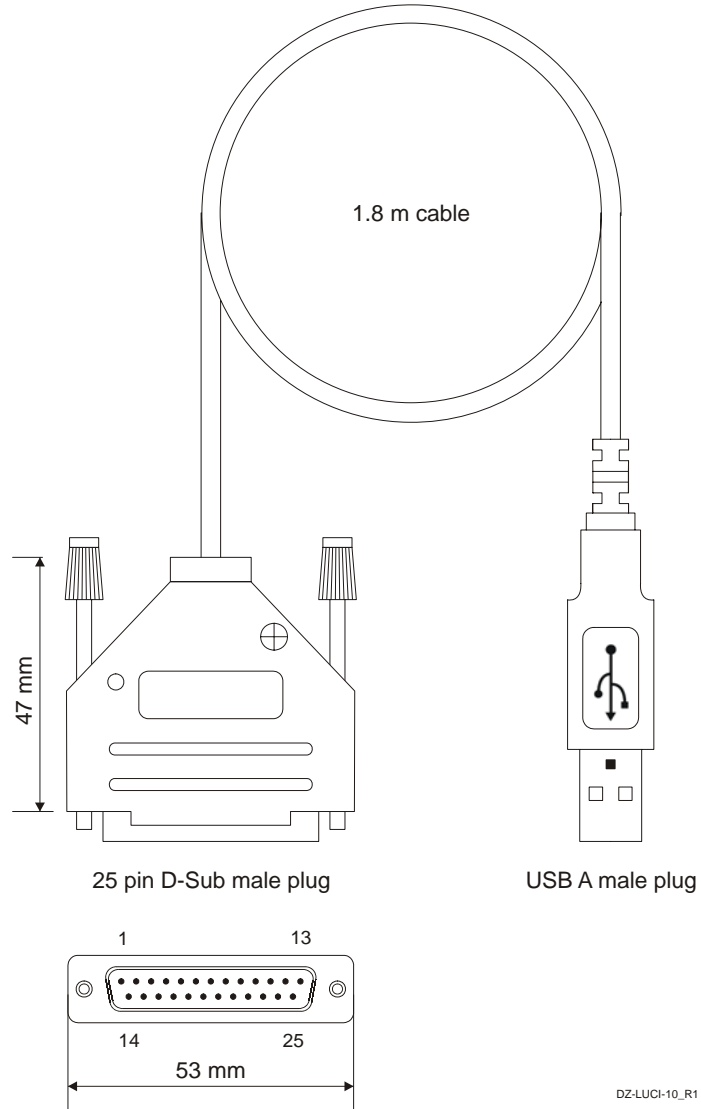
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## USB to D-Sub Control Interface for FEMTO Amplifiers

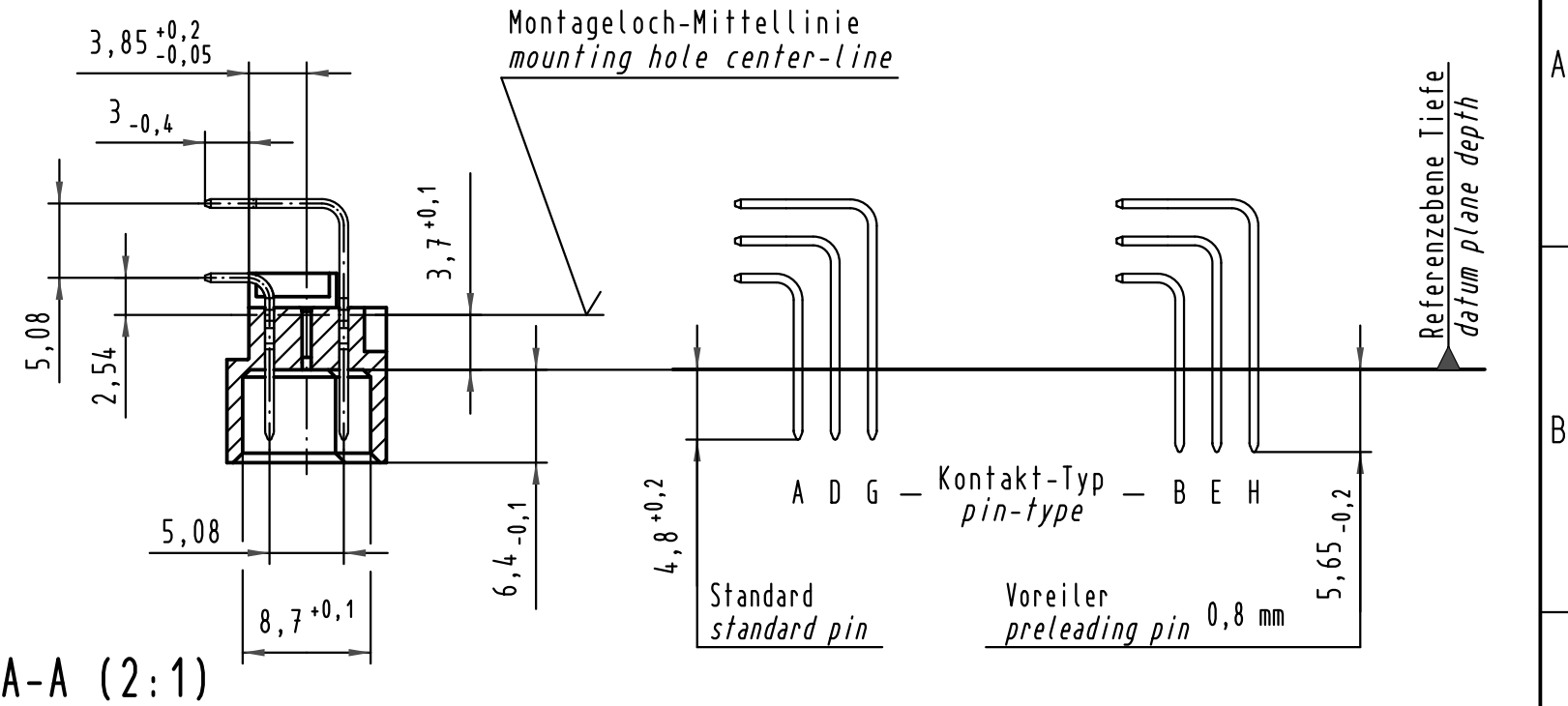
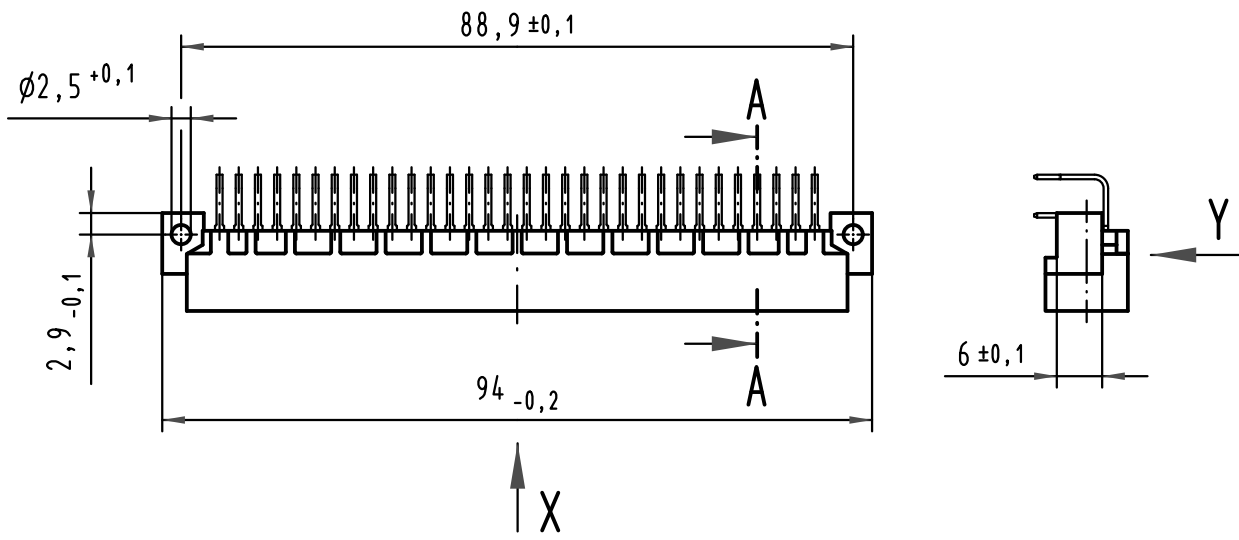
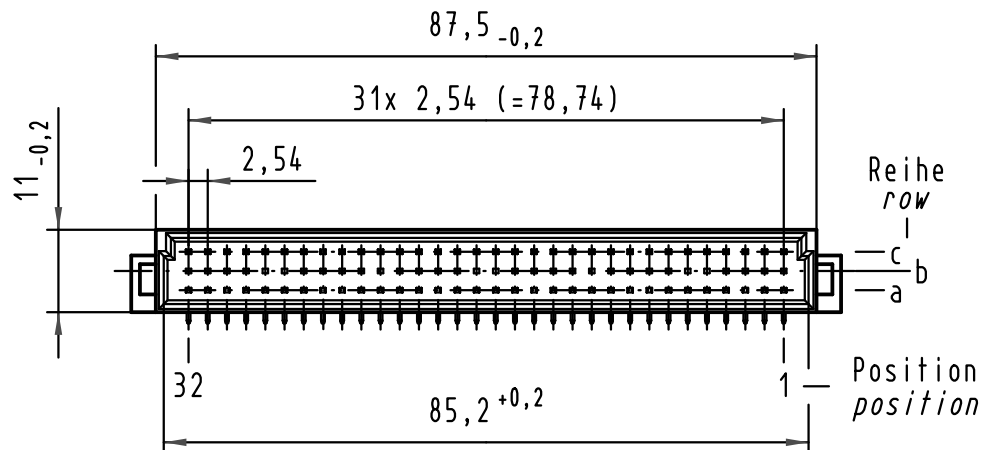
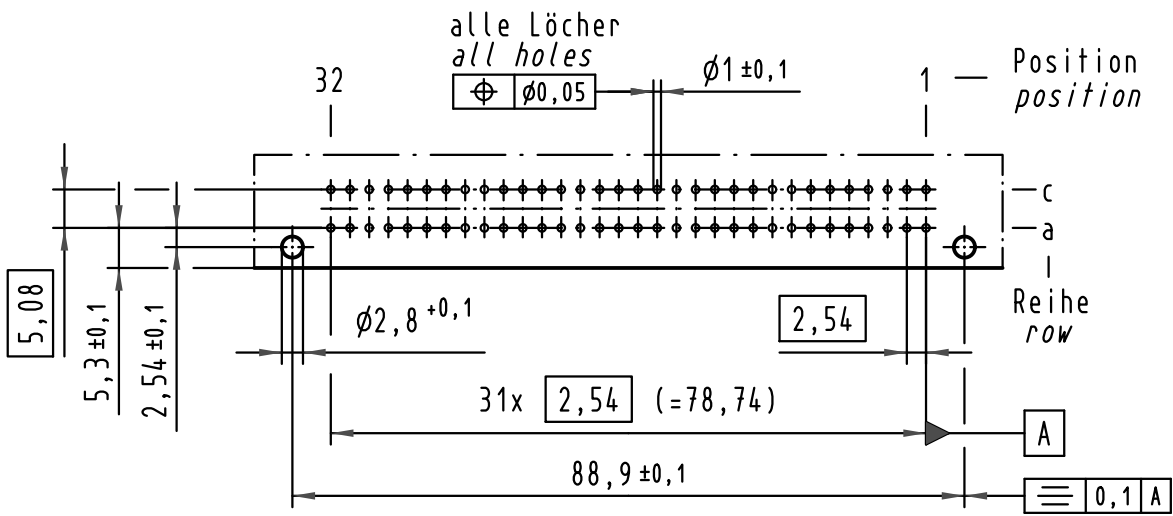
Dimensions



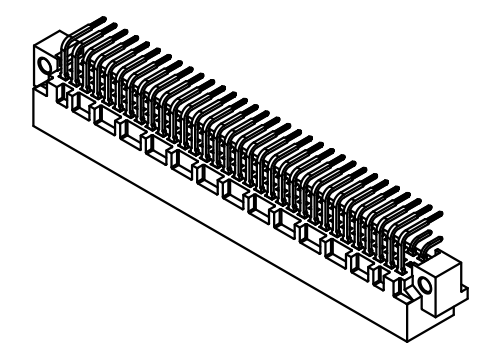
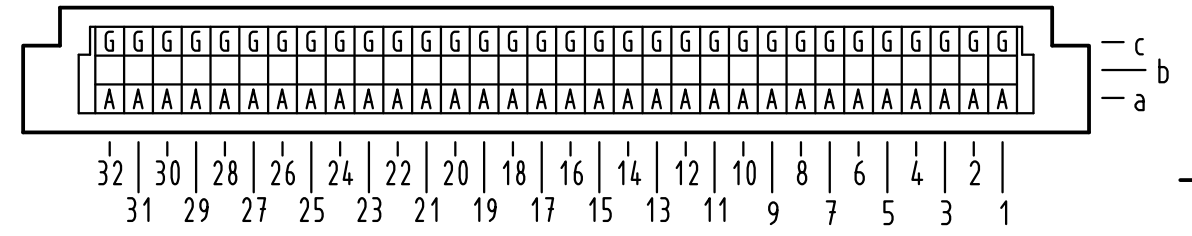
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**Y**  
(Lochbild)  
(board drillings)




**X** Kontaktbelegung  
contact loading



① weitere Informationen siehe: DS 09 03 120 02 01  
further information see: DS 09 03 120 02 01

09 03 164 7921	3
09 03 164 6921	2
09 03 164 2921	1
Bestell-Nr. part-no.	Anforderungsstufe performance level ①

All Dimensions in mm Original Size DIN A 3		Techn. Character.			Nicht tolerierte Maßel/Free size tolerances IEC 60603-2	
Mod.	Dat.	Name	Dat.	Name	Maßstab/Scale	Bauform C Messerleiste Einlöt, 64 pol. type C male connector solder, 64 pol.
36054	11.06.10	Hage.	Detail.	15.05.00	Rie.	
36042	21.05.10	Hage.	Insp.	15.05.00	Pa.	
35345	15.10.08	Hage.	Stand.			HARTING Electronics GmbH & Co. KG D-32339 ESPELKAMP 
34650	12.10.07	Hage.				
27477						
TB 09 03 164 x921						Blatt/page

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