

Product Description

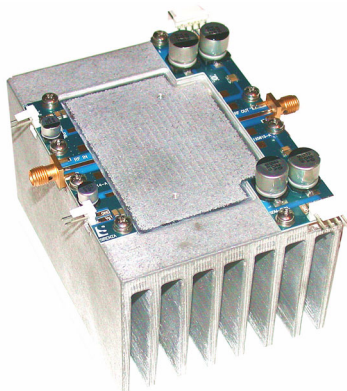
The SDM-EVAL connectorized fixture supports test and measurement of the SDM series of LDMOS modules available from Sirenza. The fixture includes convenient SMA female RF connectors along with a DC header to facilitate amplifier characterization, or rapid system prototyping. The test circuit is mounted on an aluminum heatsink that requires forced air cooling to ensure proper device operating temperatures.

Basic Bill of Materials

Component	Description	Manufacturer
PCB's	Input & Output halves, Rogers 4350, $\epsilon_r=3.5$ Thickness=30mils	Rogers
J1, J2	MTA Post Header, 2 Pin, Rectangle, Polarized, Surface Mount	AMP
J3, J4	MTA Post Header, 5 Pin, Rectangle, Polarized, Surface Mount	AMP
J5, J6	Connector, SMA END 0.037"	Johnson
C1, C2	Capacitor, Lytic 22F, 35V	Panasonic
C3, C4 C11, C12	Cap, 0.1 μ F, 100V, 10%, 1206	Johanson
C5, C6 C13, C14	Cap, 1000pF, 100V, 10%, 1206	Johanson
C7, C8, C9, C10	Capacitor, Lytic 220uF, 50V	Panasonic
Mounting Screws	4-40 X 0.250"	Various

SDM-EVAL

Test Fixture for Sirenza SDM Module Series



Product Features

- 50 Ohm SMA (F) Interface
- Integrated Aluminum Heatsink
- Supports "B" packages

Applications

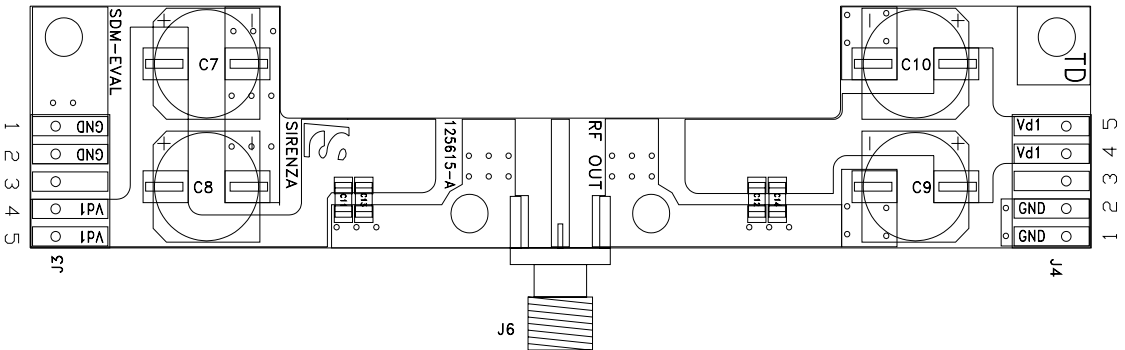
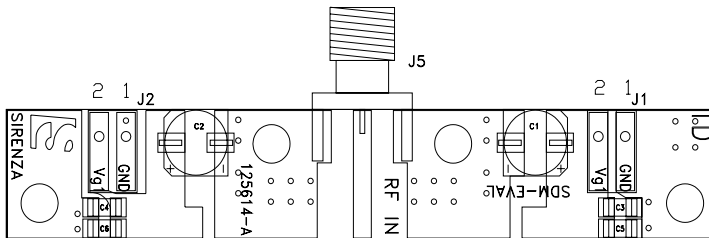
- Product Test and Evaluation
- Rapid System Prototyping
- Product Qualification

SDM-EVAL Test Fixture

Pin Descriptions

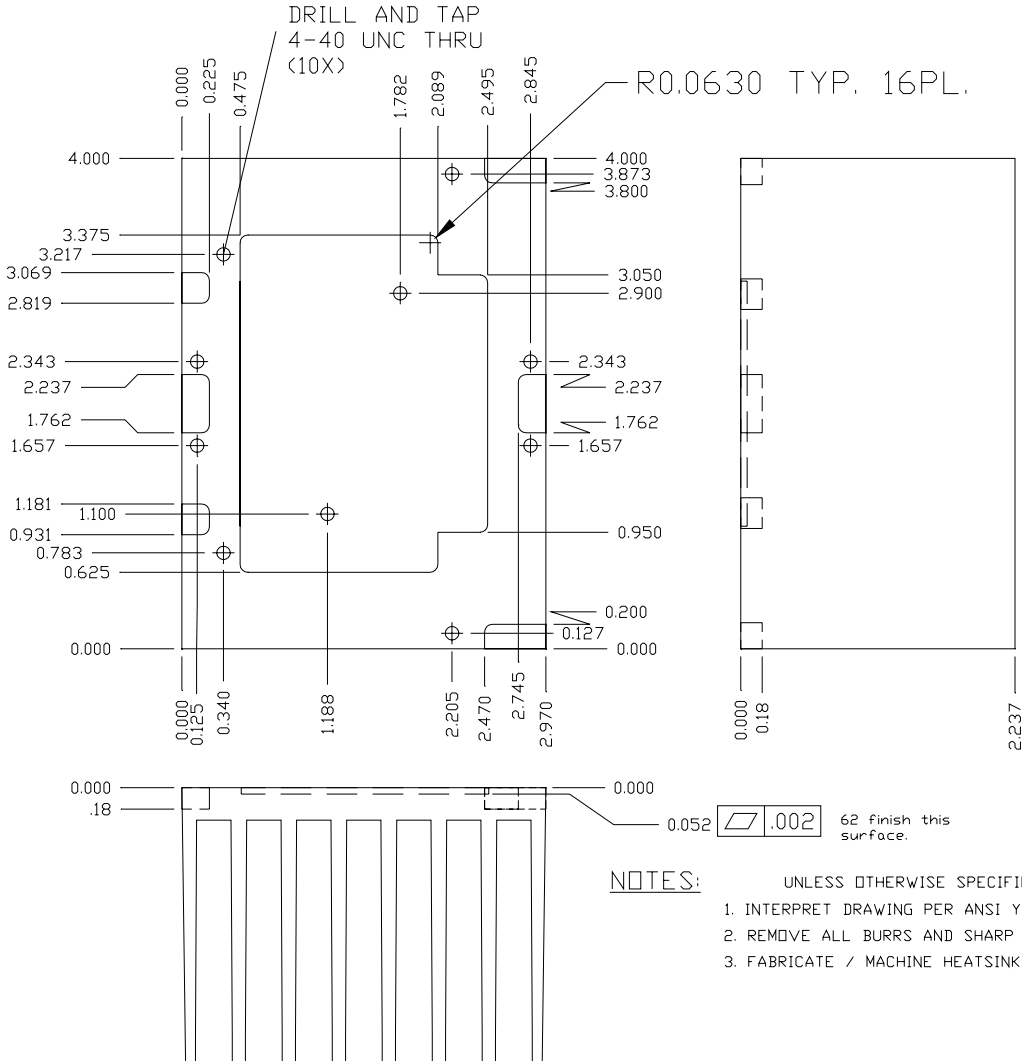
Connector	Pin #	Function	Description
J1	1	Gnd	DC ground for D package module. Also connected to RF ground.
J1	2	V _{G1}	Gate control. Sets the quiecent bias current for the top half of the SDM Module. Typical values 4.0 – 4.5Vdc.
J2	1	Gnd	DC ground for D package module. Also connected to RF ground.
J2	2	V _{G2}	Gate control. Sets the quiecent bias current for the bottom half of the SDM Module. Typical values 4.0 – 4.5Vdc.
J3	1	Gnd	DC ground for D package module. Also connected to RF ground.
J3	2	Gnd	DC ground for D package module. Also connected to RF ground.
J3	3	Gnd	DC ground for D package module. Also connected to RF ground.
J3	4	V _{D1}	Drain voltage for the top half of the SDM module. Nominally +28Vdc.
J3	5	V _{D1}	Drain voltage for the top half of the SDM module. Nominally +28Vdc.
J4	1	Gnd	DC ground for D package module. Also connected to RF ground.
J4	2	Gnd	DC ground for D package module. Also connected to RF ground.
J4	3	Gnd	DC ground for D package module. Also connected to RF ground.
J4	4	V _{D2}	Drain voltage for the bottom half of the SDM module. Nominally +28Vdc.
J4	5	V _{D2}	Drain voltage for the bottom half of the SDM module. Nominally +28Vdc.
J5	Coax	RF in	RF input to test fixture (50 Ohm system)
J6	Coax	RF out	RF output to test fixture (50 Ohm system)

Test Board Layouts



SDM-EVAL Test Fixture

Heatsink Drawing



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