

## Product Description

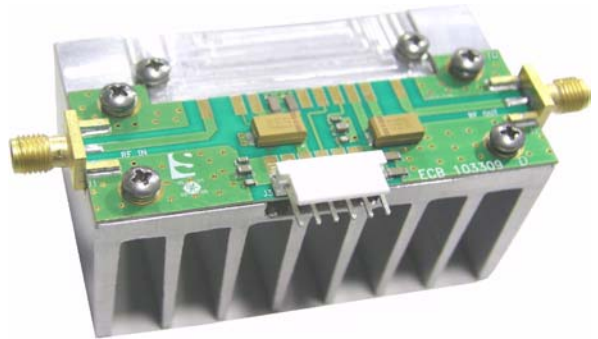
The XD010-EVAL connectorized fixture supports test and measurement of the XD series of LDMOS modules available from Sirenza. The fixture includes convenient SMA female RF connectors along with a DC header to facilitate amplifier characterization, or rapid system prototyping. The test circuit is mounted on an aluminum heatsink that requires forced air cooling to ensure proper device operating temperatures.

## Basic Bill of Materials

Component	Description	Manufacturer
PCB	Rogers 4350, $\epsilon_r=3.5$ Thickness=30mils	Rogers
J1, J2	SMA, RF, Panel Mount Tab W / Flange	Johnson
J3	MTA Post Header, 6 Pin, Rectan- gle, Polarized, Surface Mount	AMP
C1, C10	Cap, 10 $\mu$ F, 35V, 10%, Tant, Elect, D	Kemet
C2, C20	Cap, 0.1 $\mu$ F, 100V, 10%, 1206	Johanson
C3, C30	Cap, 1000pF, 100V, 10%, 1206	Johanson
C25, C26	Cap, 68pF, 250V, 5%, 0603	ATC
C21, C22	Cap, 0.1 $\mu$ F, 100V, 10%, 0805	Panasonic
C23, C24	Cap, 1000pF, 100V, 10%, 0603	AVX
Mounting Screws	4-40 X 0.250"	Various

## XD010-EVAL

### Test Fixture for Sirenza XD Module Series



## Product Features

- 50 Ohm SMA (F) Interface
- Integrated Aluminum Heatsink
- Supports D2, D4, and D5 packages

## Applications

- Product Test and Evaluation
- Rapid System Prototyping
- Product Qualification

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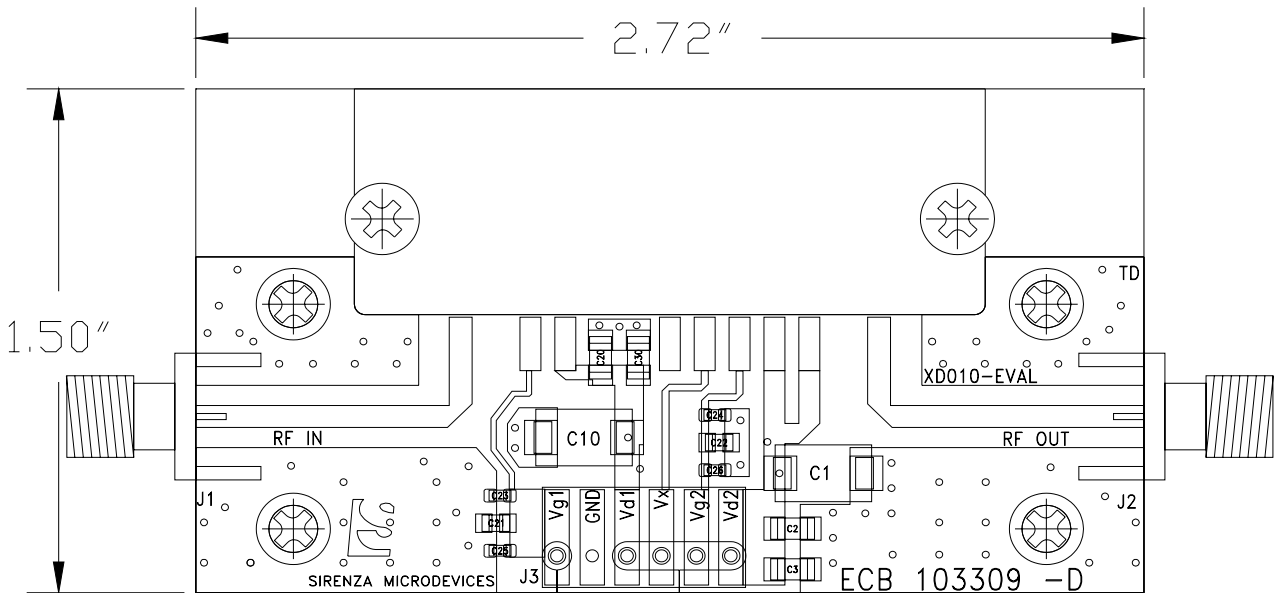
EDS-103395 Rev C

## XD010-EVAL Test Fixture

### J3 - Pin Description

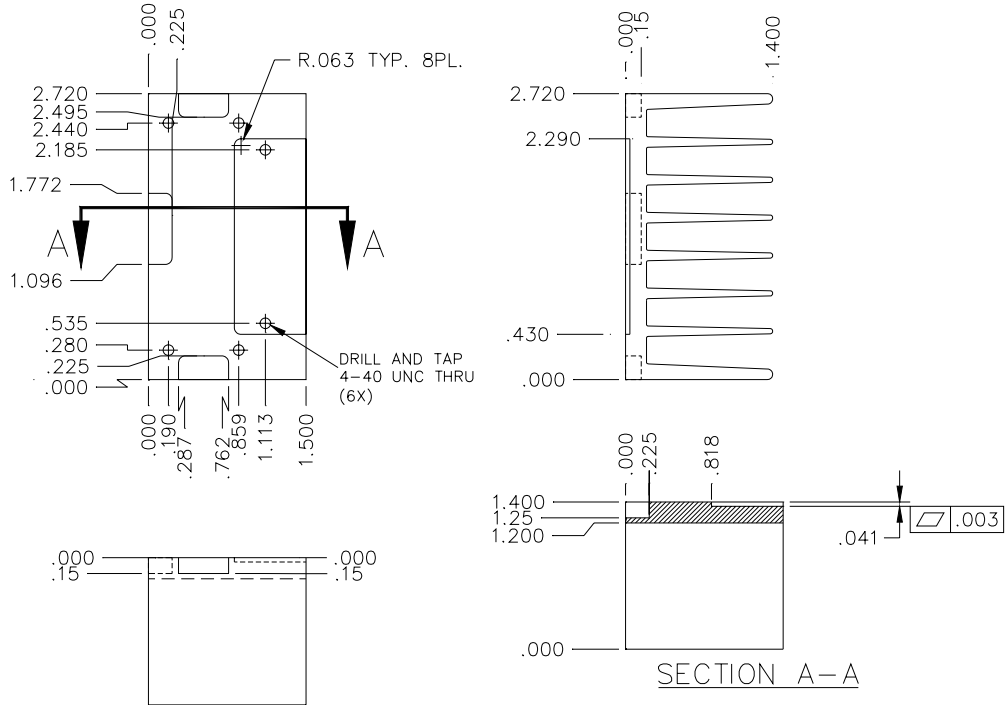
Pin #	Function	Description
1	V <sub>G1</sub>	Gate control for stage 1. Sets the quiecent bias current on Pin 3 of the XD Module. Typical values 4.0 – 4.4Vdc. This pin is not used with D2 and D4 modules.
2	Gnd	DC ground for D package module. Also connected to RF ground.
3	V <sub>D1</sub>	Drain voltage for the first stage. Nominally +28Vdc.
4	V <sub>x</sub>	Not used.
5	V <sub>G2</sub>	Gate control for stage 2. Sets the quiecent bias current on Pin 5. Typical values 4.0 – 4.4Vdc. This pin is not used with D2 and D4 modules.
6	V <sub>D2</sub>	Drain voltage for the second stage. Nominally +28Vdc

### Test Board Layout



## XD010-EVAL Test Fixture

### Heatsink Diagram



Material = Aluminum,  
Wakefield extrusion  
# HS-2063

To receive Gerber files, DXF drawings, a detailed BOM, and assembly recommendations for the test board with fixture, contact applications support at [support@sirenza.com](mailto:support@sirenza.com).

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