

# Part Number: IB1214M150



## L-Band Radar Transistor

The high power pulsed radar transistor device part number IB1214M150 is designed for L-Band radar systems operating over the instantaneous bandwidth of 1.215-1.400 GHz. While operating in class C mode this common base device supplies a minimum of 150 watts of peak pulse power under the conditions of 100µs pulse width and 10% duty cycle. All devices are 100% screened for large signal RF parameters.



### Silicon Bipolar

- Ultra-high  $f_T$

### Class C Operation

- High Efficiency

### Common Base Configuration

- Single Power Supply

### Gold Metal

- Maximum Reliability

### Emitter Ballasting

- Optimum Thermal Distribution

### Internal Impedance Matching

- Ease of Use
- Ultra-low Loss Design

### BeO Package

- Unmatched Thermal Reliability

### RF Test Fixture

- Broadband
- Matched to 50Ω
- Long-term Correlation
- 100% Device RF Screening
- No External Tuning Allowed

## TYPICAL DATA TYPICAL DATA TYPICAL DATA TYPICAL DATA

Lot/SN	F (MHz)	Pi (W)	Ic pk (A)	RL (dB)	Nc (%)	G (dB)	Po (W)	deltaG (dB)	VSWR	
									1.5 1	2 1
3130-27	1215	18.9	5.93	24	63	9	150		s	p
	1300	18.7	6.14	32	61	9	150	0.4	s	p
	1400	20.8	6.07	29	62	8.6	150		s	p
3127-25	1215	19.2	5.87	15	64	8.9	150		s	p
	1300	18.1	5.84	26	64	9.2	150	1.1	s	p
	1400	23.2	6.45	35	58	8.1	150		s	p

Vcc = 40V, Pulse width = 100us, Duty factor = 10%.

F = Frequency, Pi = Input power, Ic pk = Collector current, RL = Input return loss

Nc = Collector efficiency, G = Power gain, Po = Output power

delta G = Gain flatness across frequency band, s = stable, p = pass

Part Number: **IB1214M150****Integra**  
TECHNOLOGIES, INC.**MAXIMUM RATINGS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Collector-Emitter Voltage	$V_{CES}$	--	75	V	$V_{BE}=0V$ .
BD	Emitter-Base Voltage	$V_{EBO}$	--	2	V	--
BD	Storage Temperature Range	$T_{STG}$	-55	+150	°C	--
BD	Operating Junction Temperature Range	$T_J$	-55	+200	°C	$V_{CC}=V1$ , $PW=PW1$ , $DF=DF1$ , $T_F=25\pm5^\circ C$ , $P_{OUT}=150W$ .
Note	Screen 'BD' = parameter qualified By Design.					

**THERMAL CHARACTERISTICS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Thermal Resistance	$R_{TH(JC)}$	--	0.31	°C/W	$V_{CC}=V1$ , $PW=PW1$ , $DF=DF1$ , $T_F=25\pm5^\circ C$ , $P_{OUT}=150W$ .
Note	Screen 'BD' = parameter qualified By Design.					

**PROCESSING SPECIFICATIONS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	DC Wafer Probe	--	--	--	--	Per Integra specification.
Q1	Wafer DC and RF Qualification	--	--	--	--	Per Integra specification.
LM	Wire Bond Strength	--	--	--	--	Line monitor per Integra specification.
100%	Pre-cap visual inspection	--	--	--	--	Per Integra specification.
100%	Gross leak test	--	--	--	--	MIL-STD-750D, Method 1071.6, Test Condition C.
Note	Screen 'Q1' = parameter is qualified by assembly and test of 3 pieces minimum per wafer.					
Note	Screen 'LM' = parameter is qualified by assembly line monitor.					

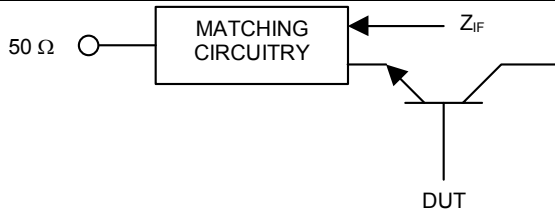
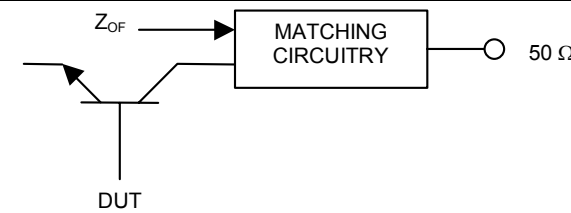
**DC ELECTRICAL CHARACTERISTICS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Collector-Emitter Breakdown Voltage	$BV_{CES}$	75	--	V	$I_C=25mA$ , $V_{BE}=0V$ , $T_F=25\pm5^\circ C$ .
100%	Zero Base Voltage Collector Leakage Current	$I_{CES}$	--	7.5	mA	$V_{CE}=40V$ , $V_{BE}=0V$ , $T_F=25\pm5^\circ C$ .
100%	DC Current Gain	$H_{FE}$	20	120	--	$V_{CE}=5V$ , $I_C=0.25A$ , $T_F=25\pm5^\circ C$ .

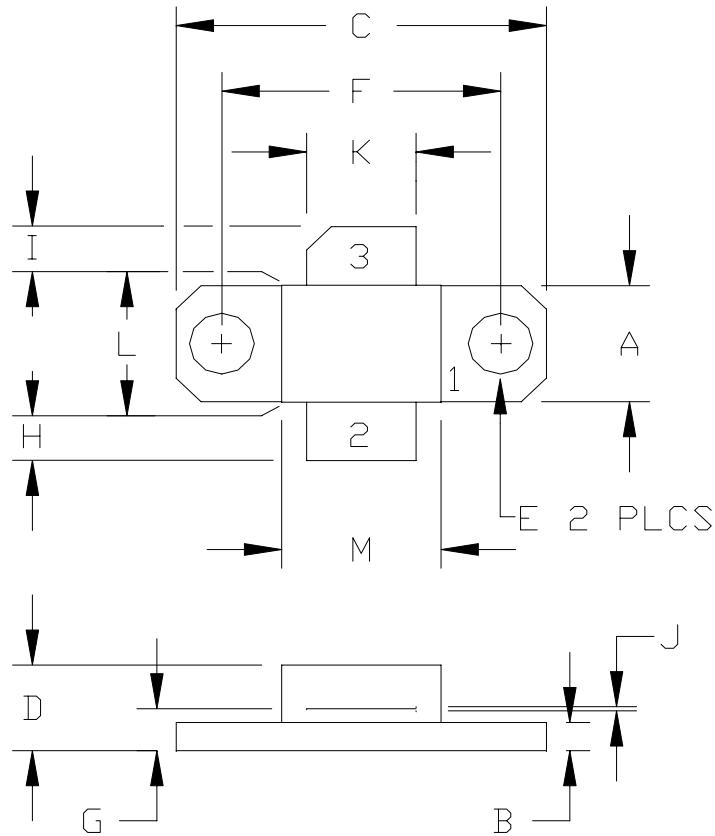
**RF ELECTRICAL CHARACTERISTICS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Input Return Loss	IRL	10	--	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, P_{OUT2}, P_{OUT3}, F=F1, F2, F3.$
100%	Input Power	$P_{IN}$	--	27.3	W	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, P_{OUT2}, P_{OUT3}, F=F1, F2, F3.$
100%	Power Gain	Gp	7.4	--	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, P_{OUT2}, P_{OUT3}, F=F1, F2, F3.$
100%	Collector Efficiency ( $P_O/I_C/V_{CC}$ )	$N_C$	50	--	%	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, P_{OUT2}, P_{OUT3}, F=F1, F2, F3.$
100%	Pulse Amplitude Droop	D	--	0.5	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, P_{OUT2}, P_{OUT3}, F=F1, F2, F3.$
100%	Stability into 1.5:1 VSWR with +0.75dB overdrive	VSWR-S	--	--	--	Rotate 1.5:1 output VSWR through 360° phase. No oscillatory or pulse break-up characteristics allowed on detected output pulse. $V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, P_{OUT2}, P_{OUT3}, F=F1, F2, F3.$
100%	2:1 Load Mismatch Tolerance	LMT	--	--	--	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, P_{OUT2}, P_{OUT3}, F=F1, F2, F3.$ Rotate 2:1 output VSWR through 360° phase. Post test $P_O =$ Pre test $P_O \pm 10W.$
Note	$V1 = 40V; PW1 = 100\mu s; DF1 = 10%; P_{OUT1} = P_{OUT2} = P_{OUT3} = 150W; F1 = 1.215 GHz, F2 = 1.300 GHz, F3 = 1.400 GHz.$					
Note	$T_F =$ Device flange temperature.					
Note	Screen 'BD' = parameter qualified By Design.					

**BROADBAND RF TEST FIXTURE IMPEDANCE CHARACTERISTICS**

Frequency (GHz)	$Z_{IF} (\Omega)$	$Z_{OF} (\Omega)$
1.215	1.8-j2.65	3.36-j1.96
1.300	1.82-j2.13	3.29-j1.08
1.400	1.87-j1.64	3.33-j0.14
Impedance Definition		

**PACKAGE DIMENSIONAL OUTLINE DRAWING**



DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.243	0.253	6.17	6.43
B	0.055	0.065	1.40	1.65
C	0.739	0.749	18.77	19.02
D	0.178	0.188	4.52	4.78
E	0.125	0.135	3.18	3.43
F	0.555	0.565	14.10	14.35
G	0.082	0.092	2.08	2.34
H	0.080	0.120	2.03	3.05
I	0.080	0.120	2.03	3.05
J	0.004	0.006	0.10	0.15
K	0.215	0.225	5.46	5.72
L	0.245	0.255	6.22	6.48
M	0.315	0.325	8.00	8.26

PIN SCHEDULE	
1	BASE
2	EMITTER
3	COLLECTOR

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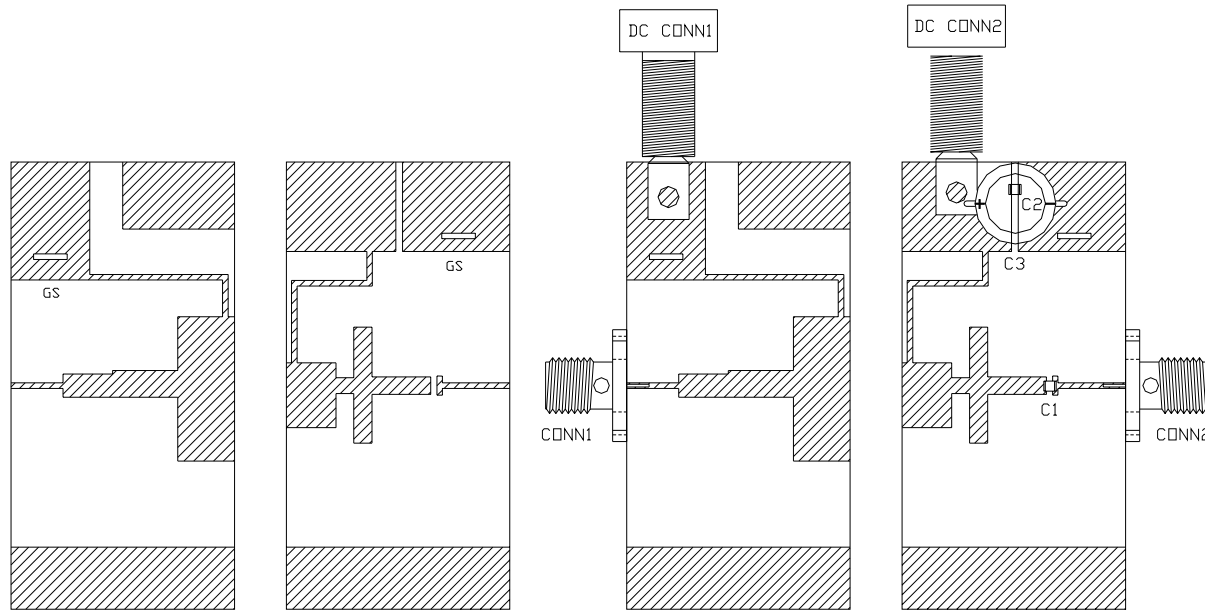
DOCUMENT NUMBER:  
IB1214M150

SHEET NAME:  
06-OUTLINE

REV:  
NC

REV:  
NC

**RF TEST FIXTURE**



COMPONENT	DESCRIPTION
DUT	TRANSISTOR IB1214M150, MOUNT HARD TO THE RIGHT
PC BOARD	ROGERS R03010 .025"
INPUT PC BOARD CARRIER	2 INCH BRASS -03 (1.0")
OUTPUT PC BOARD CARRIER	2 INCH BRASS -03 (1.0")
TRANSISTOR CARRIER	2 INCH COPPER -01 (P32)
TRANSISTOR CLAMP	NORYL CLAMP -01 (P32)
HEATSINK	2 INCH HEATSINK -10
CONN1, CONN2	SMA CONNECTOR, TYPE QS #2052-5636-02
C1, C2	CHIP CAPACITOR, TYPE ATC100A, 100pF 2 Plcs
C3	ELECTROLYTIC CAPACITOR, 68uF / 63V
C4 - NOT SHOWN	ELECTROLYTIC CAPACITOR, 4700uF / 50V
DC CONN1	BANANA JACK, BLACK
DC CONN2	BANANA JACK, RED
GS	GROUND SHIM, COPPER, TH=0.001"
NOTE	FIXTURE HARDWARE DRAWINGS AVAILABLE ON REQUEST

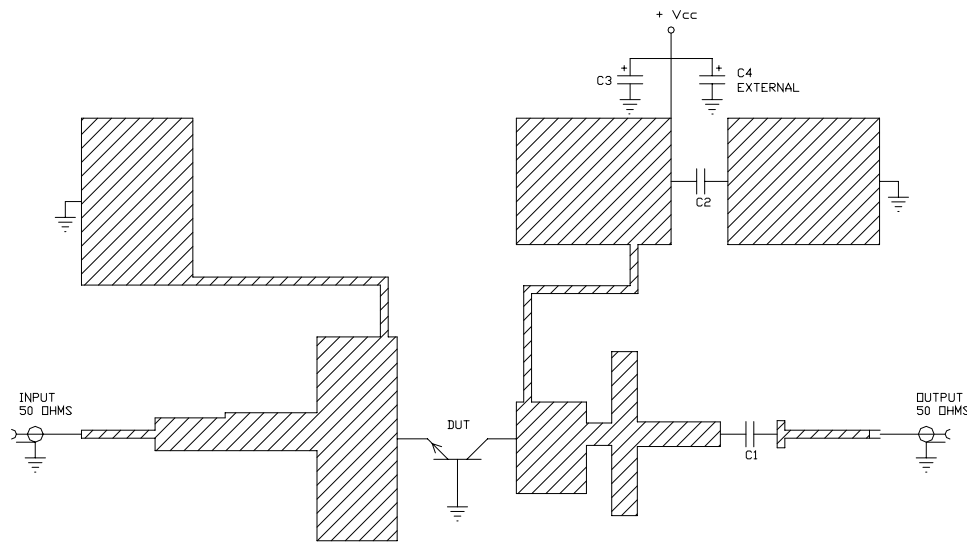
ASSEMBLY AND PARTS LIST

# Part Number: IB1214M150

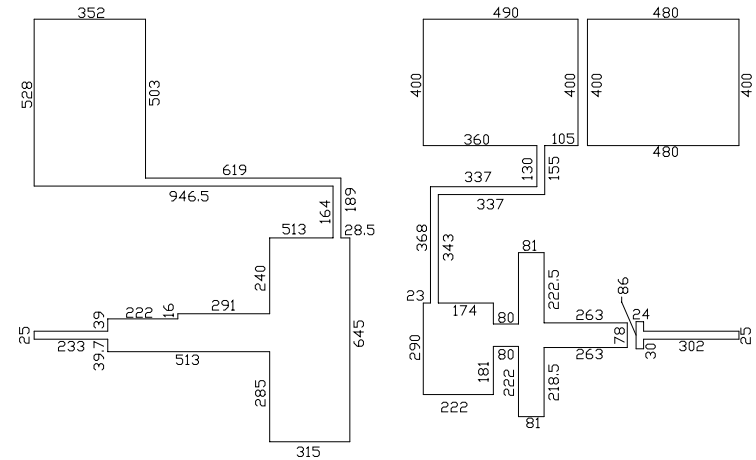


## RF TEST FIXTURE

TECHNOLOGIES, INC.



ELECTRICAL SCHEMATIC



CIRCUIT DIMENSIONS IN MILS

Part Number: **IB1214M150****Integra**  
**TECHNOLOGIES, INC.****DEFINITIONS**

<b>Data Sheet Status</b>	
Proposed Specification	This data sheet contains proposed specifications.
Preliminary Specification	This data sheet contains specifications based on preliminary measurements and data.
Product Specification	This data sheet contains final product specifications.
<b>Maximum Ratings</b>	
Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only and operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.	

**WARNING**

<b>Product and environmental safety - toxic materials</b>
This product contains beryllium oxide. The product is entirely safe provided that the BeO base is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with general or domestic waste.

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