

Part Number: IB2931M30



S-Band Radar Transistor

The high power pulsed radar transistor part number IB2931M30 is designed for S-Band ATC radar systems operating over the instantaneous bandwidth of 2.9-3.1 GHz. It is designed to serve as a driver transistor for part number IB2931M150. While operating in class C mode this common base device supplies a minimum of 30 watts of peak pulse power under the conditions of 100µs pulse width and 10% duty cycle. All devices are 100% screened for large signal RF parameters, including power gain compression. Excellent spectral stability into output mismatch over a broad input power range make it ideal for use in reliable high power solid state transmitters.



Silicon Bipolar

- Ultra-high f_T

Class C Operation

- High Efficiency

Common Base Configuration

- Single Power Supply

Gold Metal

- Maximum Reliability

Emitter Ballasting

- Optimum Thermal Distribution

Internal Impedance Matching

- Ease of Use
- Ultra-low Loss Design

BeO Package

- Unmatched Thermal Reliability

RF Test Fixture

- Broadband
- Matched to 50Ω
- Long-term Correlation
- 100% Device RF Screening
- No External Tuning Allowed

Insertion Phase Marking

- 5° Increment Marking

Patent Pending

- 6181200
- 6331931

SAMPLE DATA SAMPLE DATA SAMPLE DATA SAMPLE DATA

General Information	Freq (GHz)	PW (us)	Duty (%)	Vcc (V)	P _{IN} (W)	IRL (dB)	P _{OUT} (W)	G _p (dB)	OPC (dB)	OPF (dB)	I _c (A)	n _c (%)	Droop (dB)
Assbly Lot - SN : 502857-1	2.900	100	10	36.0	3.9	--	32	--	0.10	--	--	--	--
Test Fixture : 1306/1309	2.900	100	10	36.0	3.5	-25	32	9.6	--	0.14	1.91	46	-0.24
	2.900	100	10	36.0	4.4	--	--	--	--	--	--	--	--
	3.000	100	10	36.0	3.9	--	33	--	0.25	--	--	--	--
	3.000	100	10	36.0	3.5	-25	31	9.5	--	--	1.85	47	-0.14
	3.000	100	10	36.0	4.4	--	--	--	--	--	--	--	--
	3.100	100	10	36.0	3.9	--	33	--	0.15	--	--	--	--
	3.100	100	10	36.0	3.5	-26	32	9.6	--	--	1.99	45	-0.34
	3.100	100	10	36.0	4.4	--	--	--	--	--	--	--	--

MAXIMUM RATINGS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Collector-Emitter Voltage	V_{CES}	--	70	V	$V_{BE}=0V$.
BD	Emitter-Base Voltage	V_{EBO}	--	3.5	V	--
BD	Collector Current, Peak	I_C	--	2.9	A	$PW=PW1, DF=DF1$.
BD	Continuous Power Dissipation, Peak	P_D	--	90	W	$PW=PW1, DF=DF1, T_F=T1$.
BD	Storage Temperature Range	T_{STG}	-55	+150	°C	--
BD	Operating Junction Temperature Range	T_J	-55	+200	°C	--
Note	Screen 'BD' = parameter qualified By Design.					

THERMAL CHARACTERISTICS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Thermal Resistance	$R_{TH(JC)}$	--	1.10	°C/W	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=T1, P_{OUT}=30W$.
Note	Screen 'BD' = parameter qualified By Design.					

PROCESSING SPECIFICATIONS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	DC Wafer Probe	--	--	--	--	Per Integra specification.
Q1	Wafer DC and RF Qualification	--	--	--	--	Per Integra specification.
LM	Wire Bond Strength	--	--	--	--	Line monitor per Integra specification.
100%	Pre-cap visual inspection	--	--	--	--	Per Integra specification.
100%	Gross leak test	--	--	--	--	MIL-STD-750D, Method 1071.6, Test Condition C.
Note	Screen 'Q1' = parameter is qualified by assembly and test of 3 pieces minimum per wafer.					
Note	Screen 'LM' = parameter is qualified by assembly line monitor.					

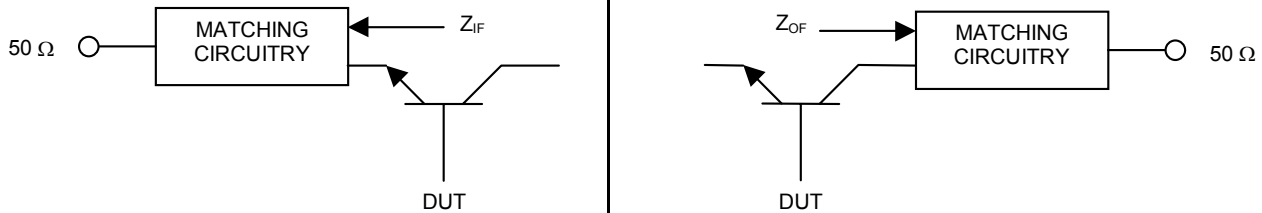
DC ELECTRICAL CHARACTERISTICS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Collector-Emitter Breakdown Voltage	BV_{CES}	70	--	V	$I_C=10mA, V_{BE}=0V, T_F=T1$.
100%	Zero Base Voltage Collector Leakage Current	I_{CES}	--	1.5	mA	$V_{CE}=30V, V_{BE}=0V, T_F=T1$.
100%	DC Current Gain	H_{FE}	10	100	--	$V_{CE}=5V, I_C=0.1A, T_F=T1$.

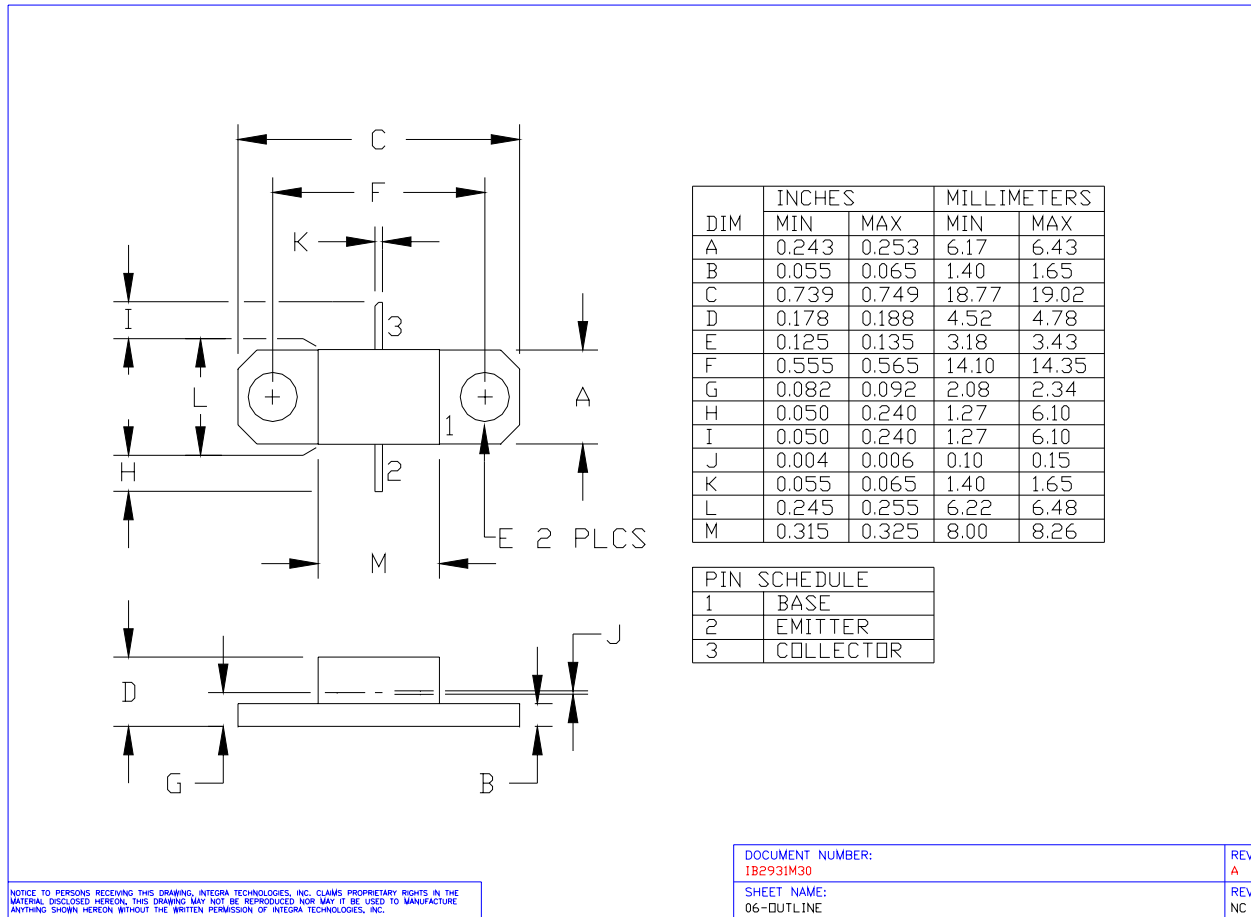
RF ELECTRICAL CHARACTERISTICS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Input Return Loss	IRL	7	--	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=T1, P_{IN}=P_{IN1}, P_{IN2}, P_{IN3}, F=F1, F2, F3.$
100%	Output Power	P_O	30	--	W	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=T1, P_{IN}=P_{IN1}, P_{IN2}, P_{IN3}, F=F1, F2, F3.$
100%	Collector Efficiency ($P_O/I_C/V_{CC}$)	N_C	38	--	%	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=T1, P_{IN}=P_{IN1}, P_{IN2}, P_{IN3}, F=F1, F2, F3.$
100%	Pulse Amplitude Droop	D	--	0.6	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=T1, P_{IN}=P_{IN1}, P_{IN2}, P_{IN3}, F=F1, F2, F3.$
100%	Output Power Flatness = $10 \cdot \text{LOG}(P_{O\text{MAX}}/P_{O\text{MIN}})$	OPF	--	0.75	dB	Calculate from P_O at each frequency F.
100%	Output Power Compression = $10 \cdot \text{LOG}(P_{OC}/P_O)$	OPC	+0.05	+0.45	dB	P_{OC} measured with P_{IN} increased by 0.5dB at $F=F1, F2, F3.$
100%	Insertion Phase	IP	-20	+20	Deg	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=T1, P_{IN}=P_{IN2}, F=F2,$ Mark in 5° increments.
100%	Stability into 1.5:1 VSWR with +1.0dB overdrive	VSWR-S	--	--	--	Repeat P_O with P_{IN} increased by 1.0dB. Rotate 1.5:1 output VSWR through 360° phase. No oscillatory or pulse break-up characteristics allowed on detected output pulse. All non-harmonically related signals must be at least -65 dBc.
100%	2:1 Load Mismatch Tolerance	LMT	--	--	--	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=T1, P_{IN}=P_{IN1}, P_{IN2}, F=F1, F2, F3.$ Rotate 2:1 output VSWR through 360° phase. Post test $P_O = \text{Pre test } P_O \pm 0.2\text{dB}.$
BD	Pulse Risetime	RT	--	150	ns	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=T1, P_{IN}=P_{IN1}, P_{IN2}, F=F1, F2, F3.$ Measure between 10 μ s and 90 μ s detected power points.
Note	F1 = 2.90 GHz, F2 = 3.00 GHz, F3 = 3.10 GHz. PW1=100 μ s, DF1=10%, V1=36v.					
Note	$P_{IN1} = 3.5\text{W}, P_{IN2} = 3.5\text{W}, P_{IN3} = 3.5\text{W}.$					
Note	T1 = Device flange temperature = 25°C \pm 5°.					
Note	Parts are binned and marked in 5 degree increments for Insertion Phase IP.					
Note	Screen 'BD' = parameter qualified By Design.					

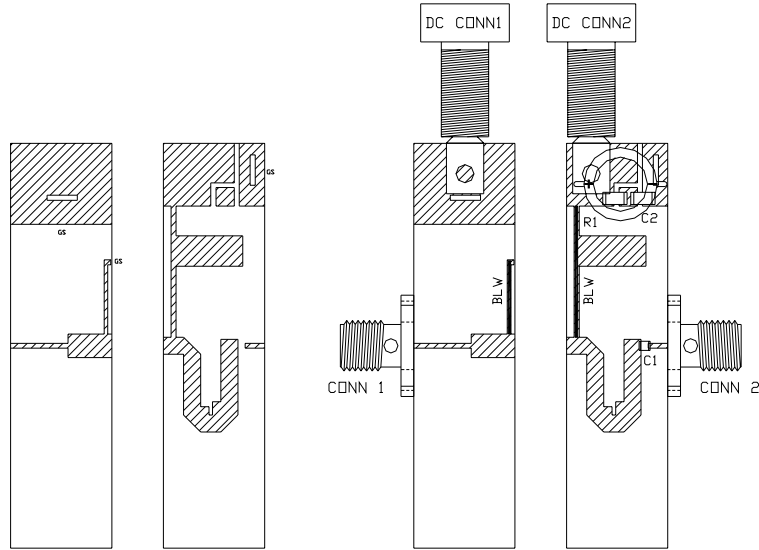
BROADBAND RF TEST FIXTURE IMPEDANCE CHARACTERISTICS

Frequency (GHz)	$Z_{IF} (\Omega)$	$Z_{OF} (\Omega)$
2.90	11.6 - j8.6	19.7 - j9.5
3.00	11.2 - j8.3	17.2 - j7.2
3.10	10.8 - j8.3	15.9 - j4.9
Impedance Definition		

PACKAGE DIMENSIONAL OUTLINE DRAWING

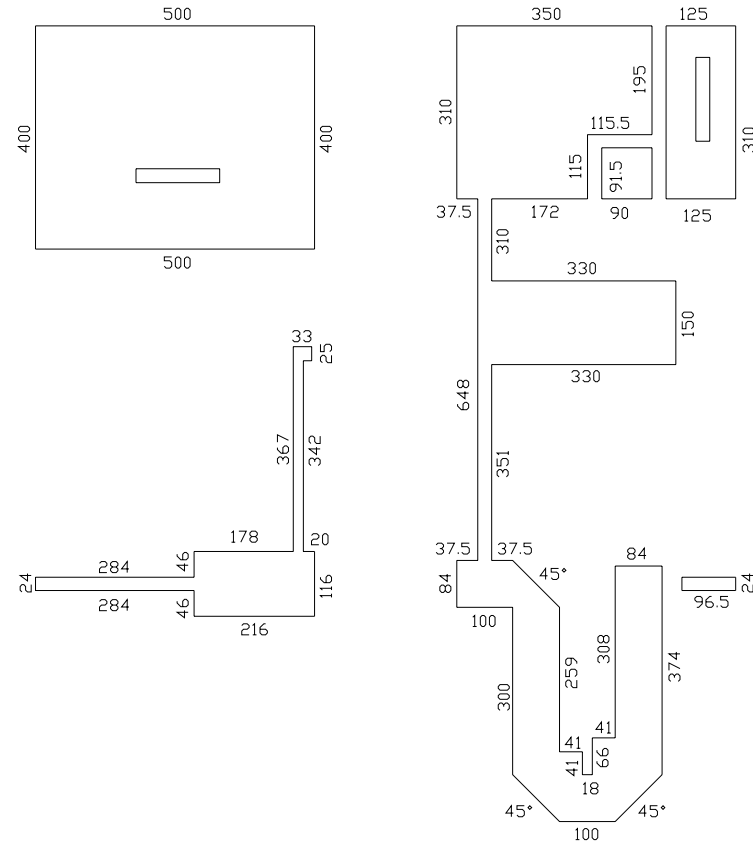


BROADBAND RF TEST FIXTURE



COMPONENT	DESCRIPTION
DUT	TRANSISTOR #IB2931M30 MOUNT HARD TO THE RIGHT
PC BOARD	ROGERS #R06010.2LM, TH=0.025" 2E/2E
C1	CHIP CAPACITOR, TYPE ATC100A, 39 pF
C2	SNUB CAPACITOR 1uF
C3	ELECTROLYTIC CAPACITOR, 68uF / 63V
C4 - NOT SHOWN	ELECTROLYTIC CAPACITOR, 2200uF / 63V
R1	SNUB RESISTOR 6.81 OHM
GS	GROUND SHIM, COPPER, TH=0.001"
CONN1, CONN2	SMA CONNECTOR, TYPE DS #2052-5636-02
INPUT PC BOARD CARRIER	2 INCH BRASS - 01
OUTPUT PC BOARD CARRIER	2 INCH BRASS - 01
TRANSISTOR CARRIER	2 INCH COPPER - 01
TRANSISTOR CLAMP	NDRYL CLAMP -01
HEATSINK	2 INCH HEATSINK - 09
DC CONN1	BANANA JACK, BLACK
DC CONN2	BANANA JACK, RED
BLW	BIAS LINE WIRE - COPPER - 0.022" DIA TYPICAL
NOTE	FIXTURE HARDWARE DRAWINGS AVAILABLE ON REQUEST

ASSEMBLY AND PARTS LIST



CIRCUIT DIMENSIONS IN MILS (1 MIL = 0.001")

DEFINITIONS**Data Sheet Status**

Proposed Specification	This data sheet contains proposed specifications.
Preliminary Specification	This data sheet contains specifications based on preliminary measurements and data.
Product Specification	This data sheet contains final product specifications.

Maximum Ratings

Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only and operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.

WARNING**Product and environmental safety - toxic materials**

This product contains beryllium oxide. The product is entirely safe provided that the BeO base is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with general or domestic waste.

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