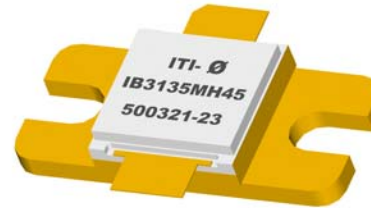


Part Number: IB3135MH45**Integra****TECHNOLOGIES, INC.****S-Band Radar Transistor**

The high power pulsed radar transistor device part number IB3135MH45 is designed for S-Band radar systems operating over the instantaneous bandwidth of 3.1-3.5 GHz. While operating in class C mode this common base device supplies a minimum of 45 watts of peak pulse power under the conditions of 100 μ s pulse width and 10% duty cycle over the frequency range of 3.1-3.5 GHz. All devices are 100% screened for large signal RF parameters, including power gain compression. Excellent spectral stability into output mismatch over a broad input power range make it ideal for use in reliable high power solid state transmitters. The test fixture includes a passive amplitude sloping network to insure that the device is not overdriven as the operating frequency decreases.

**Silicon Bipolar**

- Ultra-high f_T

Class C Operation

- High Efficiency

Common Base Configuration

- Single Power Supply

Gold Metal

- Maximum Reliability

Emitter Ballasting

- Optimum Thermal Distribution

Internal Impedance Matching

- Ease of Use
- Ultra-low Loss Design

BeO Package

- Unmatched Thermal Reliability
- Solder Seal Hermeticity

RF Test Fixture

- Broadband
- Matched to 50 Ω
- Long-term Correlation
- 100% Device RF Screening
- No External Tuning Allowed

Insertion Phase Marking

- 5° Increment Marking

Patents Issued

- US 6181200 B1
- US 6331931 B1

TYPICAL DATA		TYPICAL DATA		TYPICAL DATA		TYPICAL DATA	
--------------	--	--------------	--	--------------	--	--------------	--

Freq (GHz)	PW (us)	Duty (%)	Vcc (V)	P _{IN} (W)	IRL (dB)	P _{OUT} (W)	G _P (dB)	I _C (A)	n _C (%)	Droop (dB)
3.100	100	10	36.0	6.5	-15	66	10.1	4.17	44	-0.40
3.200	100	10	36.0	6.5	-16	62	9.8	4.15	41	-0.40
3.300	100	10	36.0	6.5	-18	59	9.6	3.94	42	-0.30
3.400	100	10	36.0	6.5	-18	57	9.4	4.01	39	-0.40
3.500	100	10	36.0	6.5	-15	52	9.1	3.73	39	-0.10

Part Number: **IB3135MH45****Integra**
TECHNOLOGIES, INC.**MAXIMUM RATINGS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Collector-Emitter Voltage	V_{CES}	--	70	V	$V_{BE}=0V$.
BD	Storage Temperature Range	T_{STG}	-65	+200	°C	--
BD	Operating Junction Temperature Range	T_J	-55	+200	°C	--
Note	Screen 'BD' = parameter qualified By Design.					

THERMAL CHARACTERISTICS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Thermal Resistance	$R_{TH(JC)}$	--	0.70	°C/W	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=45W, N_C=38\%$.
Note	GB = Guard Band. Screen 'BD' = parameter qualified By Design.					

PROCESSING SPECIFICATIONS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	DC Wafer Probe	--	--	--	--	Per Integra specification.
Q1	Wafer DC and RF Qualification	--	--	--	--	Per Integra specification.
LM	Wire Bond Strength	--	--	--	--	Line monitor per Integra specification.
100%	Pre-cap visual inspection	--	--	--	--	Per Integra specification.
100%	Gross leak test	--	--	--	--	MIL-STD-750D, Method 1071.6, Test Condition C.
Note	Screen 'Q1' = parameter is qualified by assembly and test of 3 pieces minimum per wafer.					
Note	Screen 'LM' = parameter is qualified by assembly line monitor.					

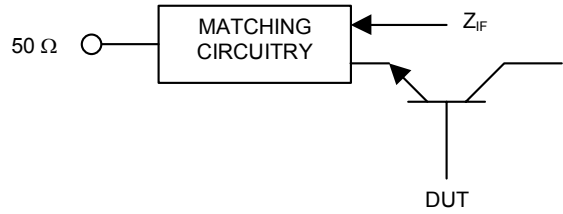
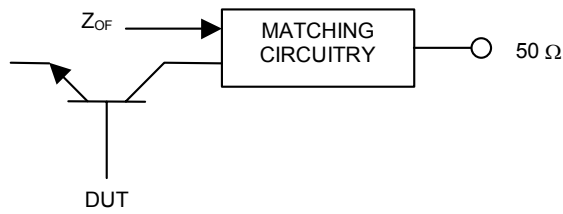
DC ELECTRICAL CHARACTERISTICS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Collector-Emitter Breakdown Voltage	BV_{CES}	70	--	V	$I_C=15mA, V_{BE}=0V, T_F=25\pm5^\circ C$.
100%	Zero Base Voltage Collector Leakage Current	I_{CES}	--	3.0	mA	$V_{CE}=30V, V_{BE}=0V, T_F=25\pm5^\circ C$.
100%	DC Current Gain	H_{FE}	10	120	--	$V_{CE}=5V, I_C=0.1A, T_F=25\pm5^\circ C$.

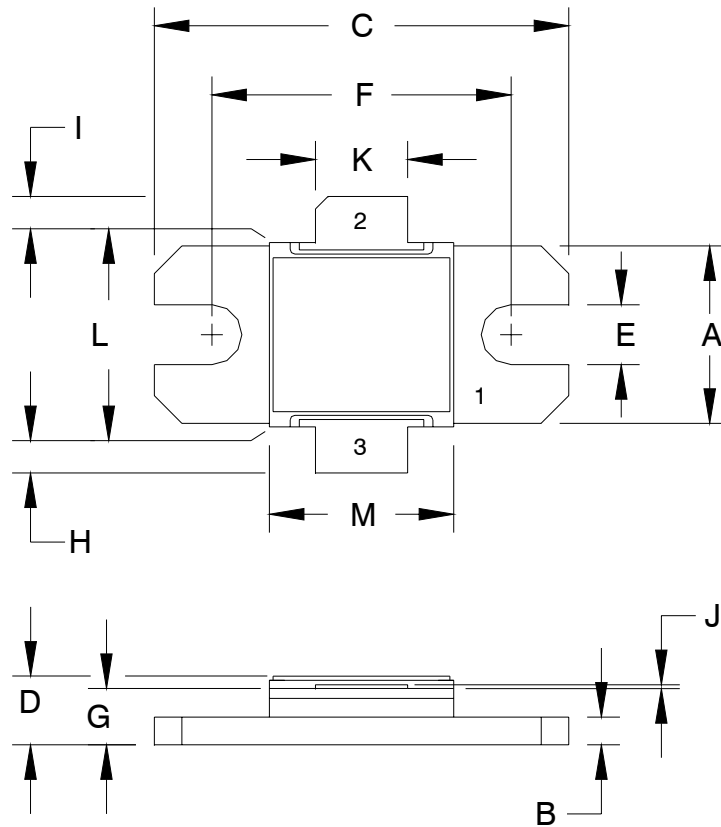
RF ELECTRICAL CHARACTERISTICS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Input Return Loss	IRL1	8	--	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1.$
100%	Input Return Loss	IRL2	9	--	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN2}, F=F2.$
100%	Input Return Loss	IRL3	10	--	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN3}, F=F3.$
100%	Output Power	P_{O1}	45	--	W	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1.$
100%	Output Power	P_{O2}	45	--	W	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN2}, F=F2.$
100%	Output Power	P_{O3}	45	--	W	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN3}, F=F3.$
100%	Collector Efficiency ($P_O/I_C/V_{CC}$)	N_{C1}	36	--	%	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1.$
100%	Collector Efficiency ($P_O/I_C/V_{CC}$)	N_{C2}	36	--	%	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN2}, F=F2.$
100%	Collector Efficiency ($P_O/I_C/V_{CC}$)	N_{C3}	36	--	%	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN3}, F=F3.$
100%	Pulse Amplitude Droop	D1	--	0.7	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1.$
100%	Pulse Amplitude Droop	D2	--	0.7	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN2}, F=F2.$
100%	Pulse Amplitude Droop	D3	--	0.7	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN3}, F=F3.$
100%	Output Power Compression = $10*\text{LOG}(P_{OC}/P_O)$	OPC1	+0.02	+0.48	dB	P_{OC} measured with P_{IN} increased by 0.5dB at $F=F1.$
100%	Output Power Compression = $10*\text{LOG}(P_{OC}/P_O)$	OPC2	+0.02	+0.48	dB	P_{OC} measured with P_{IN} increased by 0.5dB at $F=F2.$
100%	Output Power Compression = $10*\text{LOG}(P_{OC}/P_O)$	OPC3	+0.02	+0.48	dB	P_{OC} measured with P_{IN} increased by 0.5dB at $F=F3.$
100%	Output Power Flatness = $10*\text{LOG}(P_{OMAX}/P_{OMIN})$	OPF	--	1.0	dB	Calculate from P_O at each frequency $F.$
100%	Delta Insertion Phase Variation	d-IP	-30	+30	Deg	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN3}, F=F3,$ Mark in 5° increments.
100%	Stability into 1.5:1 VSWR with +0.75dB overdrive	VSWR-S	--	--	--	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, P_{IN2}, P_{IN3}, F=F1, F2, F3.$ Repeat P_O with P_{IN} increased by 0.75dB. Rotate 1.5:1 output VSWR through 360° phase. No oscillatory or pulse break-up characteristics allowed on detected output pulse. All non-harmonically related signals must be at least -65 dBc.
100%	2:1 Load Mismatch Tolerance	LMT	--	--	--	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, P_{IN2}, P_{IN3}, F=F1, F2, F3.$ Rotate 2:1 output VSWR through 360° phase. Post test $P_O =$ Pre test $P_O \pm 3W.$
Note	$V1 = 36V; PW1 = 100\mu s; DF1 = 10\%; P_{IN1} = P_{IN2} = P_{IN3} = 6.5W; F1 = 3.10$ GHz, $F2 = 3.20$ GHz, $F3 = 3.30$ GHz.					
Note	$T_F =$ Device flange temperature. Screen 'BD' = parameter qualified By Design.					
Note	Parts are binned and marked in 5 degree increments for Insertion Phase IP : ITI-1, -2, -3, -4, -5, -6, -7, -8, -9, -10, -11, -12.					

BROADBAND RF TEST FIXTURE IMPEDANCE CHARACTERISTICS

Frequency (GHz)	Z_{IF} (Ω)	Z_{OF} (Ω)
3.10	7.0 -j8.6	5.0 -j7.4
3.20	6.7 -j8.6	4.7 -j7.1
3.30	6.0 -j8.4	4.5 -j6.7
3.40	5.4 -j8.2	4.3 -j6.4
3.50	4.6 -j7.7	4.1 -j6.1
Impedance Definition		

PACKAGE DIMENSIONAL OUTLINE DRAWING



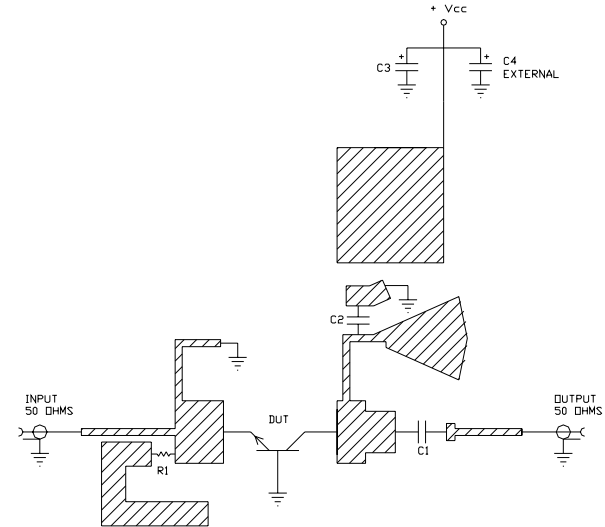
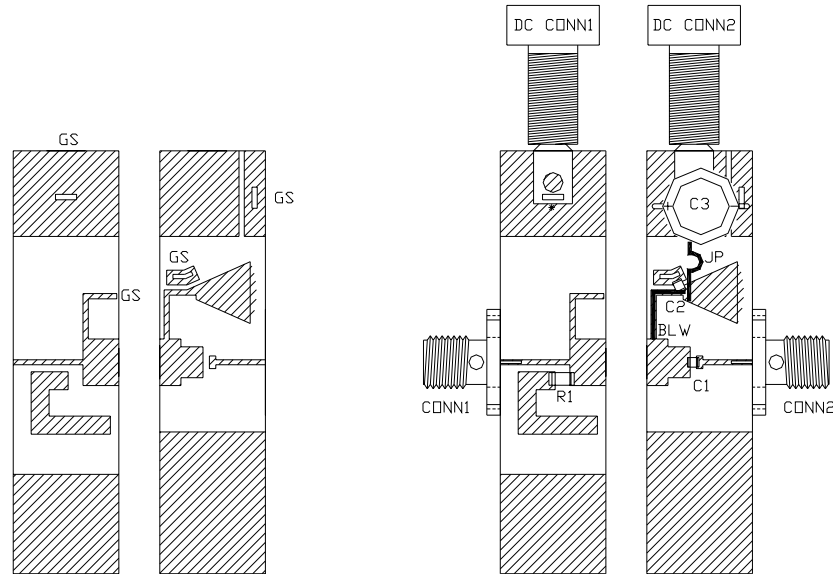
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.380	0.390	9.65	9.91
B	0.058	0.062	1.47	1.57
C	0.895	0.905	22.73	22.99
D	0.157	0.177	3.99	4.50
E	0.125	0.135	3.18	3.43
F	0.645	0.655	16.38	16.64
G	0.112	0.132	2.84	3.35
H	0.090	0.110	2.29	2.79
I	0.090	0.110	2.29	2.79
J	0.003	0.005	0.08	0.13
K	0.195	0.205	4.95	5.21
L	0.395	0.405	10.03	10.29
M	0.395	0.405	10.03	10.29

PIN	
1	BASE
2	COLLECTOR
3	EMITTER

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DOCUMENT NUMBER: IB3135MH45	REV: PR1
SHEET NAME: 06-OUTLINE	REV: NC

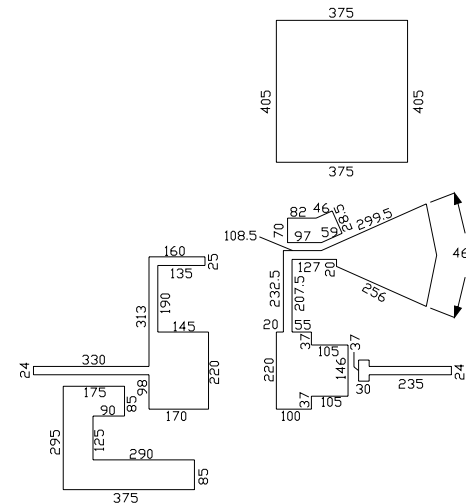
BROADBAND RF TEST FIXTURE



ELECTRICAL SCHEMATIC

COMPONENT	DESCRIPTION
DUT	TRANSISTOR #IB3135MH45, MOUNT HARD TO THE RIGHT
PC BOARD	ROGERS #6010.2LM, TH=0.025", 1 oz. Cu.
C1, C2	CHIP CAPACITOR, TYPE ATC100A, 39 pF
C3	ELECTROLYTIC CAPACITOR, 68uF / 63V
C4 - NOT SHOWN	ELECTROLYTIC CAPACITOR, 2200uF / 63V
GS	GROUND SHIM, COPPER, TH=0.001"
CONN1, CONN2	SMA CONNECTOR, TYPE QS #2052-5636-02
INPUT PC BOARD CARRIER	0.5 INCH BRASS - 01
OUTPUT PC BOARD CARRIER	0.5 INCH BRASS - 01
TRANSISTOR CARRIER	2 INCH COPPER - 02
TRANSISTOR CLAMP	NORYL CLAMP -02
HEATSINK	2 INCH HEATSINK - 09
DC CONN1	BANANA JACK, BLACK
DC CONN2	BANANA JACK, RED
BLW	BIAS LINE WIRE - COPPER - 0.022" DIA TYPICAL
JP	JUMPER WIRE
R1	300 ohm CHIP RESISTOR, MSI# WA57PS-3000J-NS62
NOTE	FIXTURE HARDWARE DRAWINGS AVAILABLE ON REQUEST

ASSEMBLY AND PARTS LIST



CIRCUIT DIMENSIONS IN MILS (1 MIL = 0.001")

Part Number: **IB3135MH45****Integra**
TECHNOLOGIES, INC.**DEFINITIONS**

Data Sheet Status	
Proposed Specification	This data sheet contains proposed specifications.
Preliminary Specification	This data sheet contains specifications based on preliminary measurements and data.
Product Specification	This data sheet contains final product specifications.
Maximum Ratings	
Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only and operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.	

WARNING

Product and environmental safety - toxic materials
This product contains beryllium oxide. The product is entirely safe provided that the BeO base is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with general or domestic waste.

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微波光电部专业代理经销高频、微波、光纤、光电元器件、组件、部件、模块、整机；电磁兼容元器件、材料、设备；微波 CAD、EDA 软件、开发测试仿真工具；微波、光纤仪器仪表。欢迎国外高科技微波、光纤厂商将优秀产品介绍到中国、共同开拓市场。长期大量现货专业批发高频、微波、卫星、光纤、电视、CATV 器件：晶振、VCO、连接器、PIN 开关、变容二极管、开关二极管、低噪晶体管、功率电阻及电容、放大器、功率管、MMIC、混频器、耦合器、功分器、振荡器、合成器、衰减器、滤波器、隔离器、环行器、移相器、调制解调器；光电子器件和组件：红外发射管、红外接收管、光电开关、光敏管、发光二极管和发光二极管组件、半导体激光二极管和激光器组件、光电探测器和光接收组件、光发射接收模块、光纤激光器和光放大器、光调制器、光开关、DWDM 用光发射和接收器件、用户接入系统光收发器件与模块、光纤连接器、光纤跳线/尾纤、光衰减器、光纤适配器、光隔离器、光耦合器、光环行器、光复用器/转换器；无线收发芯片和模组、蓝牙芯片和模组。

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