



# NJM2224

## ■ ABSOLUTE MAXIMUM RATINGS

( $T_a=25^\circ\text{C}$ )

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	$V^+$	8	V
Power Dissipation	$P_D$	300	mW
Operating Temperature Range	$T_{opr}$	-20 to +75	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	-40 to +125	$^\circ\text{C}$

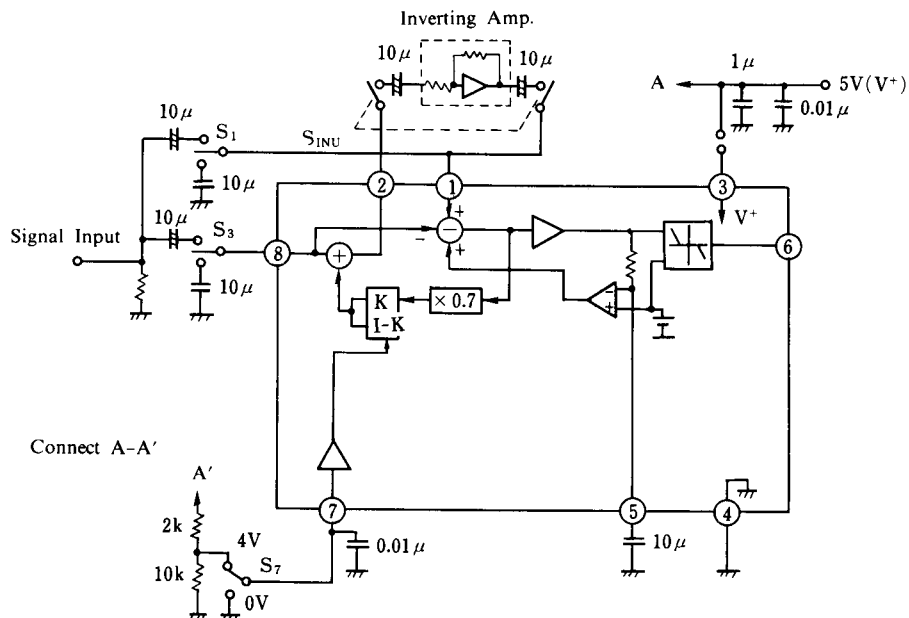
## ■ ELECTRICAL CHARACTERISTICS

( $V^+=5\text{V}$ ,  $f=100\text{kHz}$ ,  $T_a=25^\circ\text{C}$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	$I_{CC}$		-	6.9	10	mA
Voltage Gain (Pin8-Pin2)	$A_{U11}$ $A_{U12}$	$V_7=4\text{V}$ , $V_{IN}=100\text{mVp-p}$ $V_7=0\text{V}$ , $V_{IN}=100\text{mVp-p}$	-1 -11.5	0 -10	+1 -8.5	dB
Voltage Gain (Pin1-Pin2)	$A_{U21}$ $A_{U22}$	$V_7=4\text{V}$ , $V_{IN}=100\text{mVp-p}$ $V_7=0\text{V}$ , $V_{IN}=100\text{mVp-p}$	- -4.2	-45 -3.2	-38 -2.2	dB
Voltage Gain (Pin8-Pin6)	$A_{U31}$ $A_{U32}$	$V_7=4\text{V}$ , Pin2-1(Inv. Amp), $V_{IN}=20\text{mVp-p}$ $V_7=4\text{V}$ , Pin2-1(Inv. Amp), $V_{IN}=300\text{mVp-p}$	- -5	- -3	-20 -1	dB
Bandwidth (Pin8-Pin2)	$f_{B1}$	$V_7=4\text{V}$ , $V_{IN}=100\text{mVp-p}$	10	-	-	MHz
Bandwidth (Pin1-Pin2)	$f_{B2}$	$V_7=0\text{V}$ , $V_{IN}=100\text{mVp-p}$	10	-	-	MHz
Bandwidth (Pin8-Pin6)	$f_{B3}$	$V_7=4\text{V}$ , $V_{IN}=100\text{mVp-p}$	8	-	-	MHz
Pin6 DC Voltage	$V_{6-DC}$		-	1.3	-	V

NOTE: unless specified, all items are tested by Test Circuit.

## ■ TEST CIRCUIT



## ■ TERMINAL FUNCTION

PIN No.	PIN NAME	INSIDE EQUIVALENT CIRCUITS	PIN No.	PIN NAME	INSIDE EQUIVALENT CIRCUITS
1	CCD Input		5	Signal Bypass	
2	CCD Output		6	Output	
3	V <sup>+</sup>		7	Control Input	
4	GND		8	Input	

# NJM2224

## APPLICATION NOTE

The **NJM2224** is an integrated circuit of composing variable comb type filter which reduces noise mixed in chroma or luminance signal of VCR camera and others. The CCD delay device is suitable for outside delay element composing comb type filter, compared with a glass delay device. The basic circuit is the most excellent FB system with NULL.

Fig.1 is its basic block diagram and Fig.2 is actual block diagram of **NJM2224**.

Fig.3 is one of application examples.

The video noise reducer system is composed of three capacitors for connection and one capacitor for signal bypass and CCD delay device. The **NJM2224** is applicable to both of chroma and luminous signal using CCD delay device suitable for each signal. High level input to the Control of Reduce/Enhance terminal (Pin 7) makes enhance mode (increasing of high frequency part) and Low level input makes reduce mode (decreasing of high frequency part.) Its threshold level is about 2.25V at 5V supply voltage. Basic operating characteristics is shown in Fig.4.

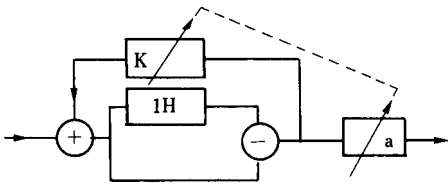


Fig.1 Basic Block Diagram

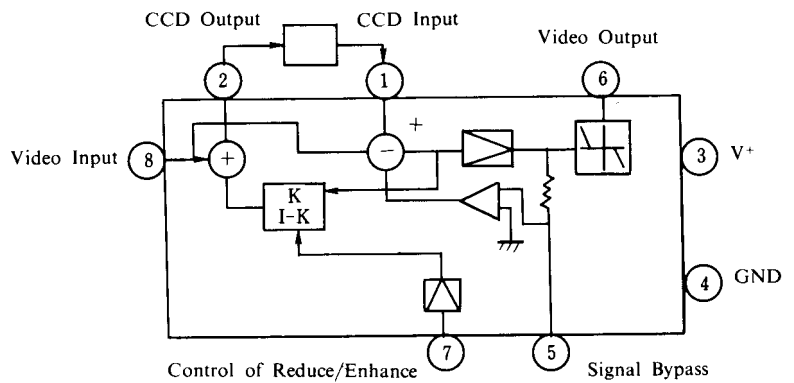


Fig.2 Block Diagram

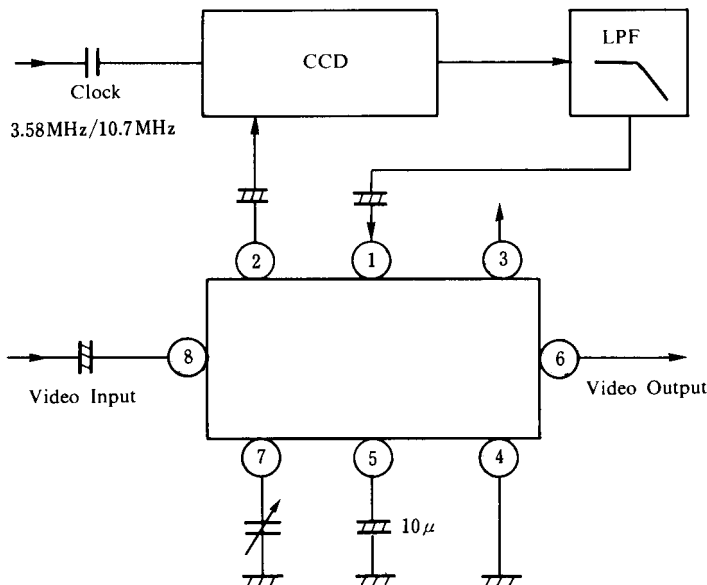


Fig.3 Application

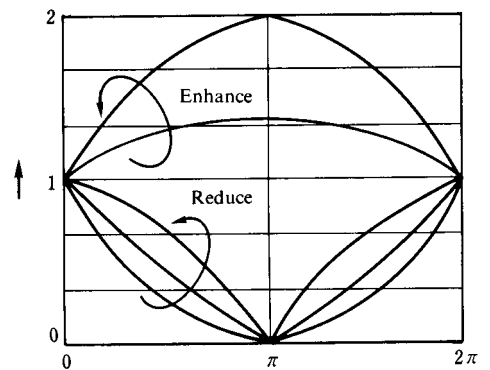


Fig.4 Basic Operating Characteristic

The comb type filter has frequency characteristics shown Fig.5, and is widely used to separate luminance and color signal in video-signal circuit. Using this comb type filter, the **NJM2224** reduces noise and enhances signal in video signal, automatically.

Fig.6 shows video signal wave form and its frequency component.

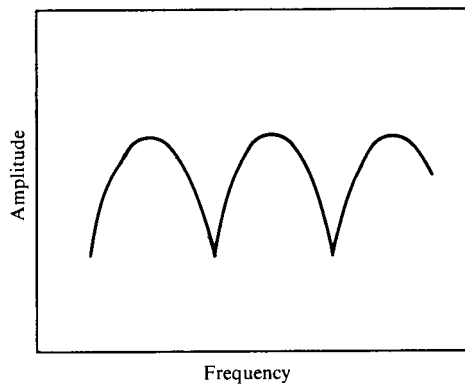


Fig.5 Comb Type Filter Frequency Characteristic

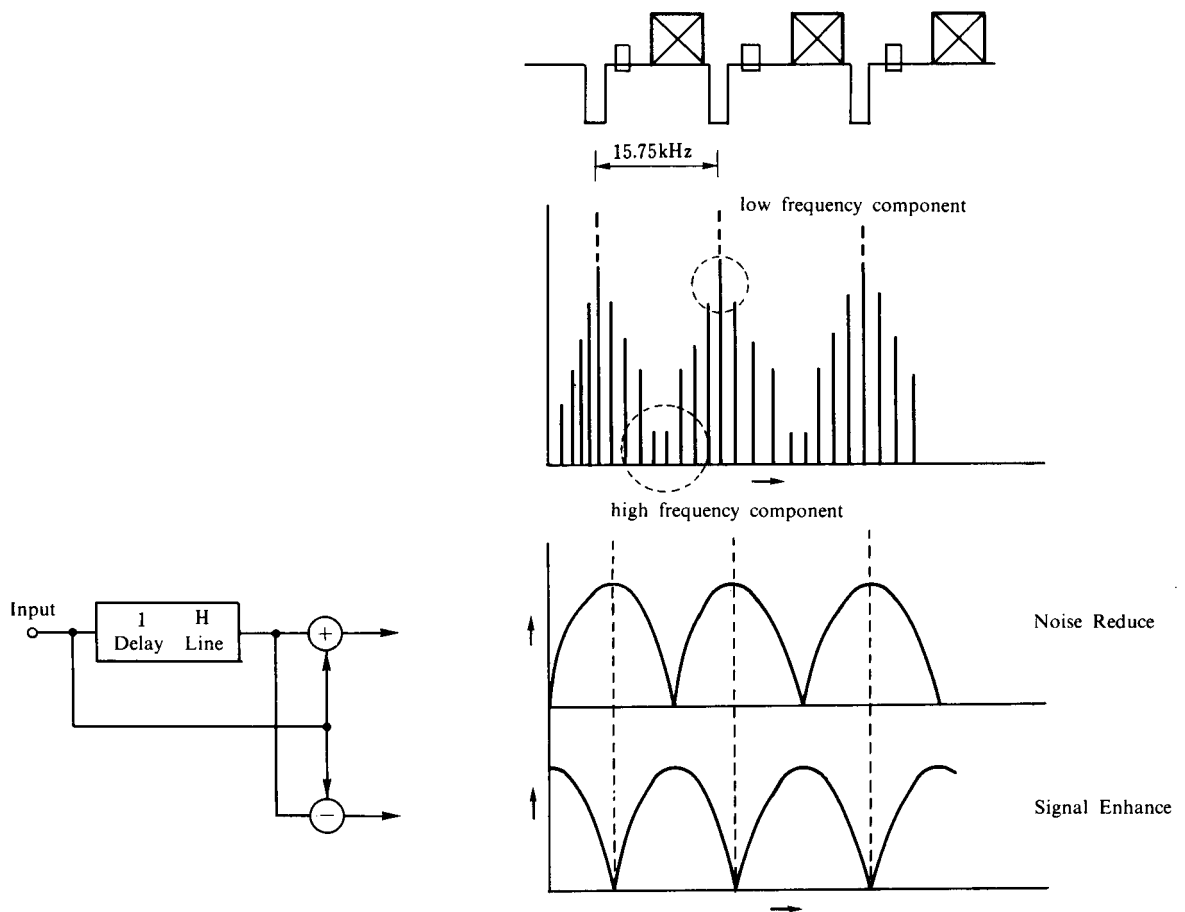
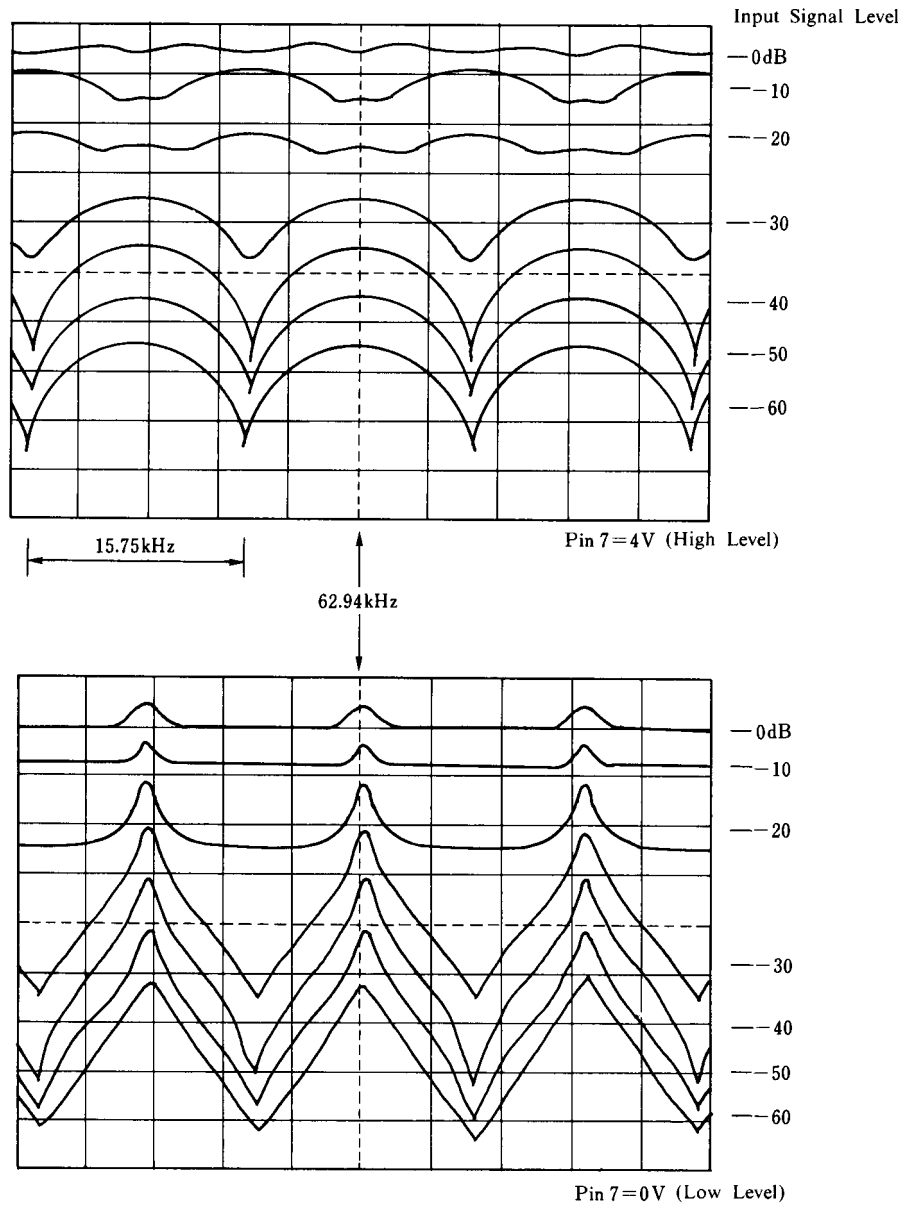


Fig.6 Video Signal Waveform & Frequency Component

# NJM2224

Fig.7 shows output characteristics determined by the input Level to Pin7. The phase difference of each signal makes the output distortion of type filter characteristics, because FB system adds and subtracts each signal. The NJM2224 phase difference is 2 degree at 4MHz. High dynamic range of video signal is realized by high supply voltage.

Fig. 7 Comb Type Filter Characteristics vs. Input Signal Level (800mV<sub>P-P</sub>=0dB)



[CAUTION]  
 The specifications on this databook are only given for information, without any guarantee as regards either mistakes or omissions. The application circuits in this databook are described only to show representative usages of the product and not intended for the guarantee or permission of any right including the industrial rights.