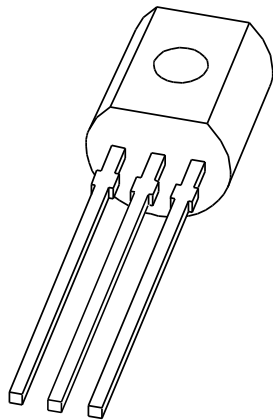


DATA SHEET



BF421; BF423 PNP high-voltage transistors

Product specification
Supersedes data of September 1994
File under Discrete Semiconductors, SC04

1996 Dec 09

PNP high-voltage transistors

BF421; BF423

FEATURES

- Low feedback capacitance.

APPLICATIONS

- Class-B video output stages in colour television and professional monitor equipment.

DESCRIPTION

PNP transistors in a TO-92 plastic package.
 NPN complements: BF420 and BF422.

PINNING

PIN	DESCRIPTION
1	base
2	collector
3	emitter

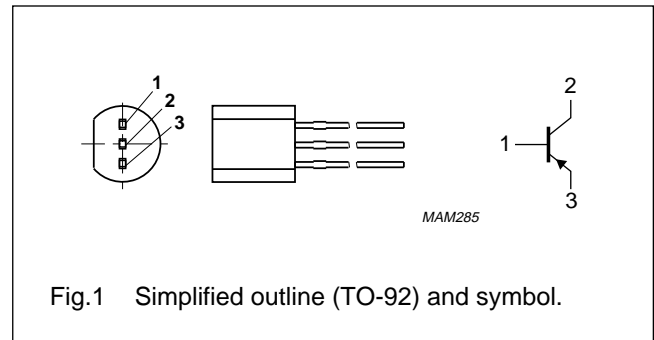


Fig.1 Simplified outline (TO-92) and symbol.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BF421		–	–300	V
	BF423		–	–250	V
V _{CEO}	collector-emitter voltage	open base			
	BF421		–	–300	V
	BF423		–	–250	V
I _{CM}	peak collector current		–	–100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	–	830	mW
h _{FE}	DC current gain	I _C = –25 mA; V _{CE} = –20 V	50	–	
C _{re}	feedback capacitance	I _C = i _c = 0; V _{CE} = –30 V; f = 1 MHz	–	1.6	pF
f _T	transition frequency	I _C = –10 mA; V _{CE} = –10 V; f = 100 MHz	60	–	MHz

PNP high-voltage transistors

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BF421		–	–300	V
	BF423		–	–250	V
V _{CEO}	collector-emitter voltage	open base			
	BF421		–	–300	V
	BF423		–	–250	V
V _{EBO}	emitter-base voltage	open collector	–	–5	V
I _C	collector current (DC)		–	–50	mA
I _{CM}	peak collector current		–	–100	mA
I _{BM}	peak base current		–	–50	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	–	830	mW
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	150	°C
T _{amb}	operating ambient temperature		–65	+150	°C

Note

1. Transistor mounted on a printed-circuit board.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	150	K/W

Note

1. Transistor mounted on a printed-circuit board.

CHARACTERISTICS

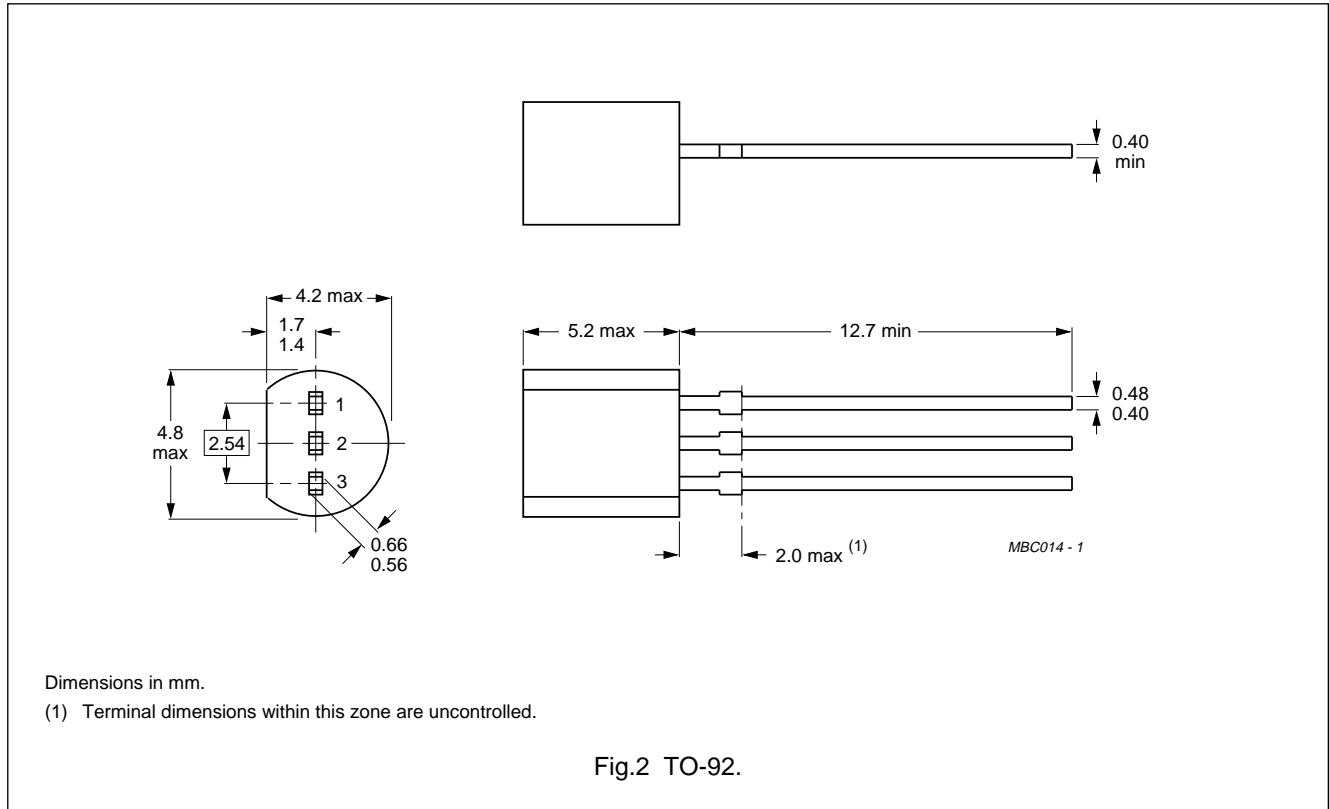
T_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = –200 V	–	–10	nA
		I _E = 0; V _{CB} = –200 V; T _j = 150 °C	–	–10	μA
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = –5 V	–	–50	nA
h _{FE}	DC current gain	I _C = –25 mA; V _{CE} = –20 V	50	–	
V _{CEsat}	collector-emitter saturation voltage	I _C = –30 mA; I _B = –5 mA	–	–0.6	V
C _{re}	feedback capacitance	I _C = I _c = 0; V _{CE} = –30 V; f = 1 MHz	–	1.6	pF
f _T	transition frequency	I _C = –10 mA; V _{CE} = –10 V; f = 100 MHz	60	–	MHz

PNP high-voltage transistors

BF421; BF423

PACKAGE OUTLINE



DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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PNP high-voltage transistors

BF421; BF423

NOTES

PNP high-voltage transistors

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NOTES

PNP high-voltage transistors

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