

DATA SHEET

BFQ255; BFQ255A PNP video transistors

Product specification
Supersedes data November 1995
File under Discrete Semiconductors, SC05

1997 Oct 02

PNP video transistors

BFQ255; BFQ255A

FEATURES

- High breakdown voltages
- Low output capacitance
- High gain bandwidth
- Good thermal stability
- Gold metallization ensures excellent reliability.

APPLICATIONS

- Buffer/driver in high-resolution colour graphics monitors.

DESCRIPTION

PNP video transistor in a SOT128B (TO-202) plastic package.
NPN complements: BFQ235 and BFQ235A.

PINNING

PIN	DESCRIPTION
1	emitter
2	collector
3	base

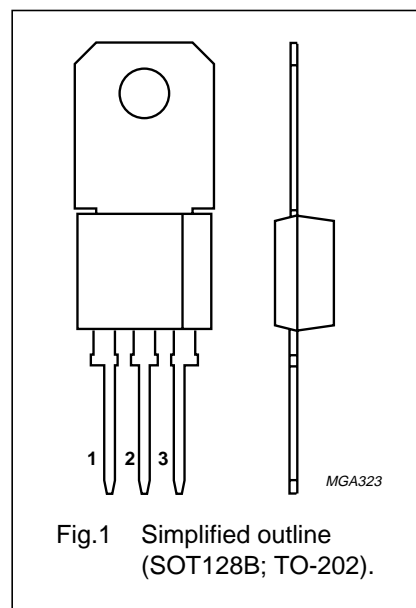


Fig.1 Simplified outline (SOT128B; TO-202).

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{CBO}	collector-base voltage BFQ255 BFQ255A	open emitter	–	–	–100	V
			–	–	–115	V
V _{CER}	collector-emitter voltage BFQ255 BFQ255A	R _{BE} = 100 Ω	–	–	–95	V
			–	–	–110	V
I _C	collector current (DC)		–	–	–300	mA
P _{tot}	total power dissipation	T _s ≤ 100 °C; note 1	–	–	3	W
h _{FE}	DC current gain	I _C = –50 mA; V _{CE} = –10 V; T _{amb} = 25 °C	20	30	–	
f _T	transition frequency BFQ255 BFQ255A	I _C = –50 mA; V _{CE} = –10 V; f = 100 MHz; T _{amb} = 25 °C	1	1.3	–	GHz
			0.8	1.2	–	GHz

Note

1. T_s is the temperature at the soldering point of the collector pin.

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	–	–100	V
	BFQ255 BFQ255A			–115	V
V _{CEO}	collector-emitter voltage	open base	–	–65	V
	BFQ255 BFQ255A			–95	V
V _{CER}	collector-emitter voltage	R _{BE} = 100 Ω	–	–95	V
	BFQ255 BFQ255A			–110	V
V _{EBO}	emitter-base voltage	open collector	–	–3	V
I _C	collector current (DC)		–	–300	mA
P _{tot}	total power dissipation	T _s ≤ 100 °C; note 1; see Fig.3	–	3	W
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	175	°C

Note

1. T_s is the temperature at the soldering point of the collector pin.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point	T _s ≤ 100 °C; note 1	25	K/W

Note

1. T_s is the temperature at the soldering point of the collector pin.

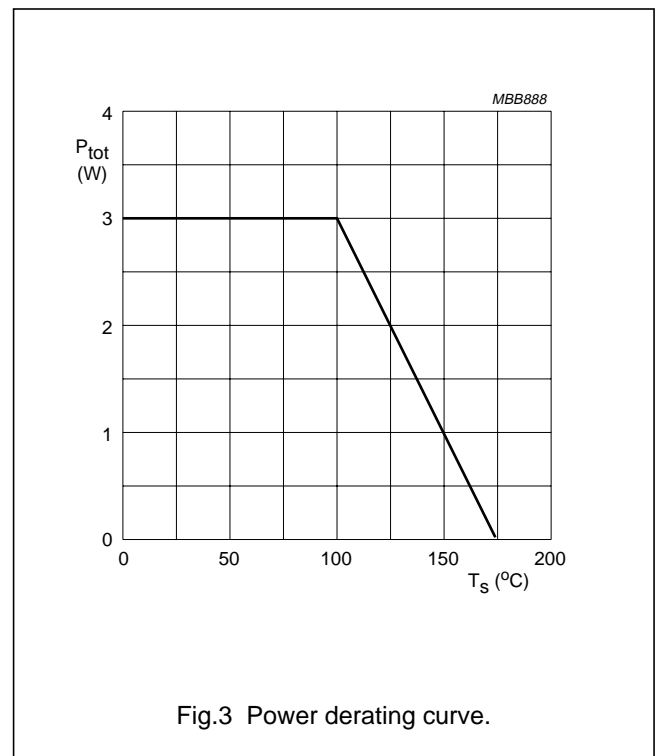
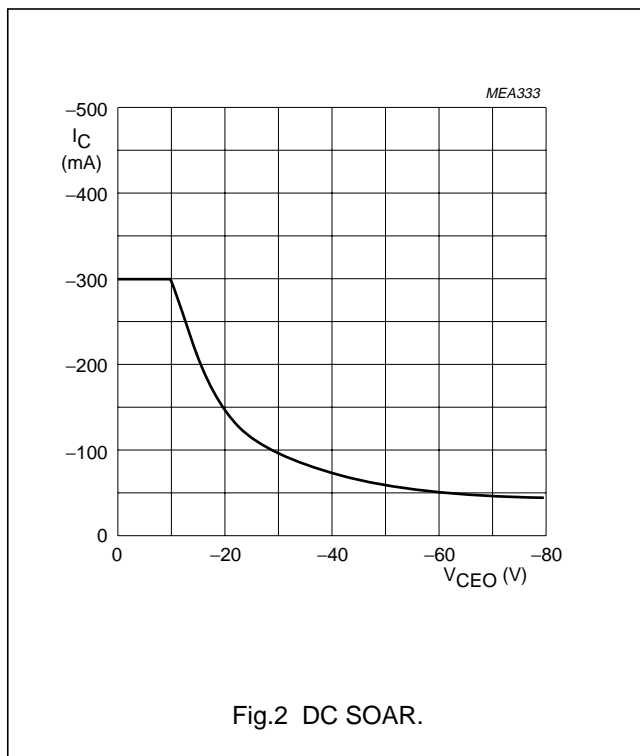
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CHARACTERISTICS

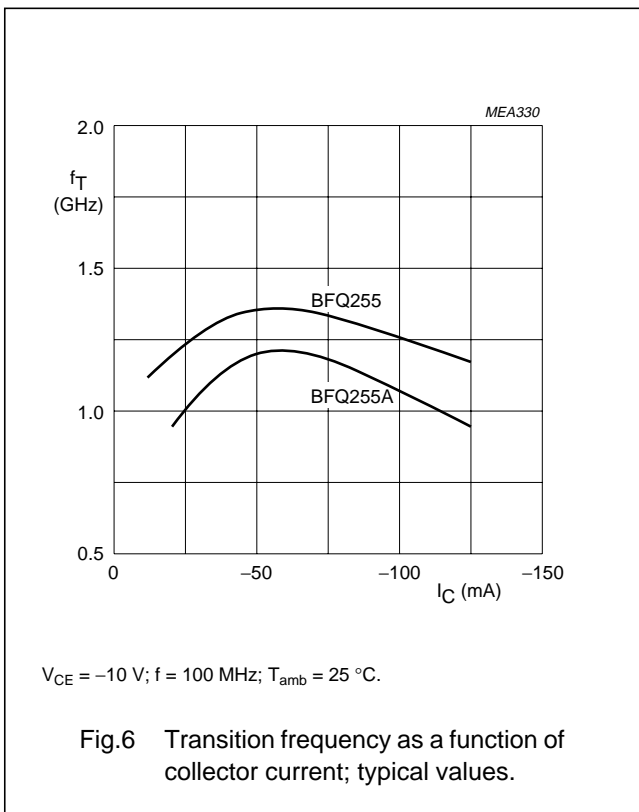
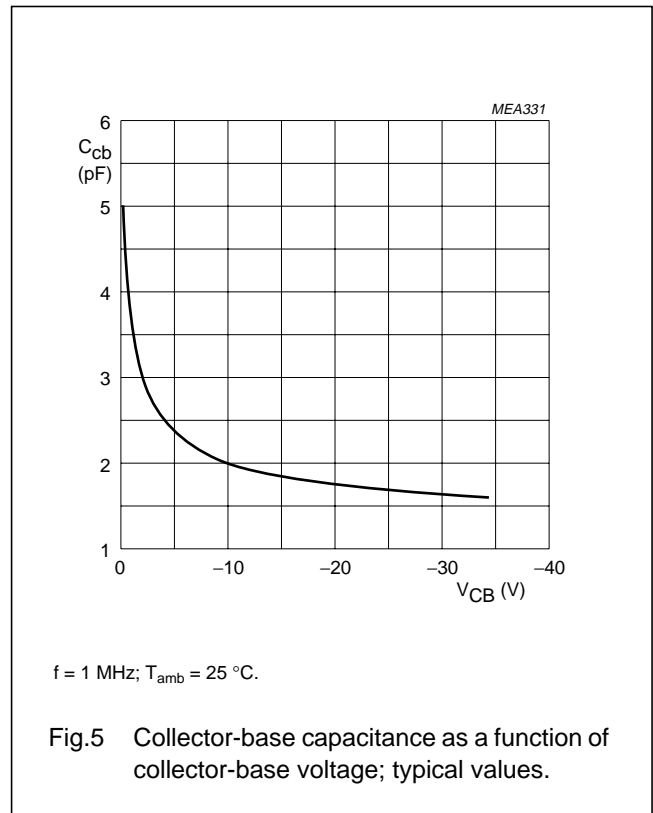
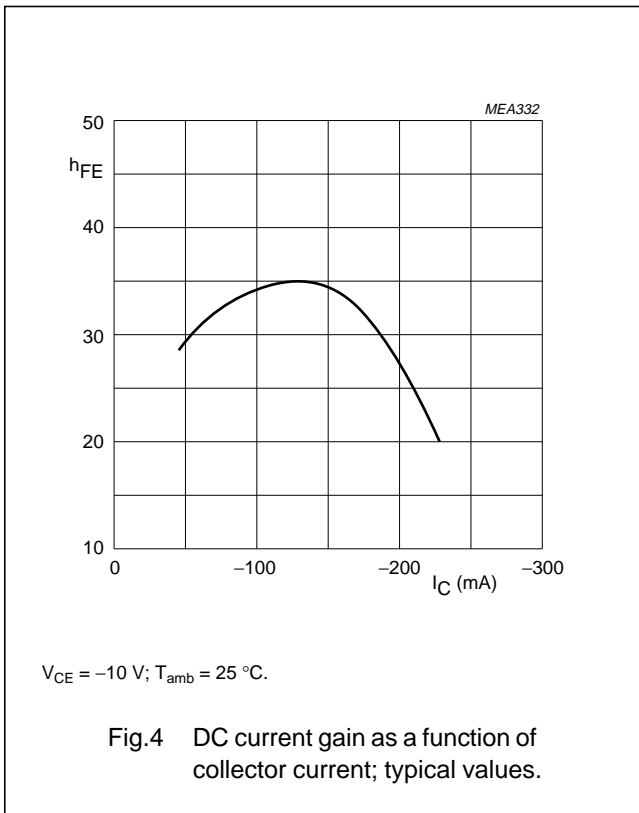
T_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{(BR)CBO}	collector-base breakdown voltage BFQ255 BFQ255A	I _C = -0.1 mA; I _E = 0	-100	-	-	V
			-115	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage BFQ255 BFQ255A	I _C = -10 mA; I _B = 0	-66	-	-	V
			-95	-	-	V
V _{(BR)CER}	collector-emitter breakdown voltage BFQ255 BFQ255A	I _C = -10 mA; R _{BE} = 100 Ω	-95	-	-	V
			-110	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = -0.1 mA; I _C = 0	-3	-	-	V
I _{CES}	collector-emitter cut-off current	I _B = 0; V _{CE} = -50 V	-	-	-100	μA
I _{CBO}	collector-base cut-off current	I _E = 0; V _{CB} = -50 V	-	-	-20	μA
h _{FE}	DC current gain	I _C = -50 mA; V _{CE} = -10 V; T _{amb} = 25 °C; see Fig.4	20	30	-	
C _{cb}	collector-base capacitance	I _C = 0; V _{CB} = -10 V; f = 1 MHz; see Fig.5	-	2	-	pF
f _T	transition frequency BFQ255 BFQ255A	I _C = -50 mA; V _{CE} = -10 V; f = 100 MHz; T _{amb} = 25 °C; see Fig.6	1	1.3	-	GHz
			0.8	1.2	-	GHz



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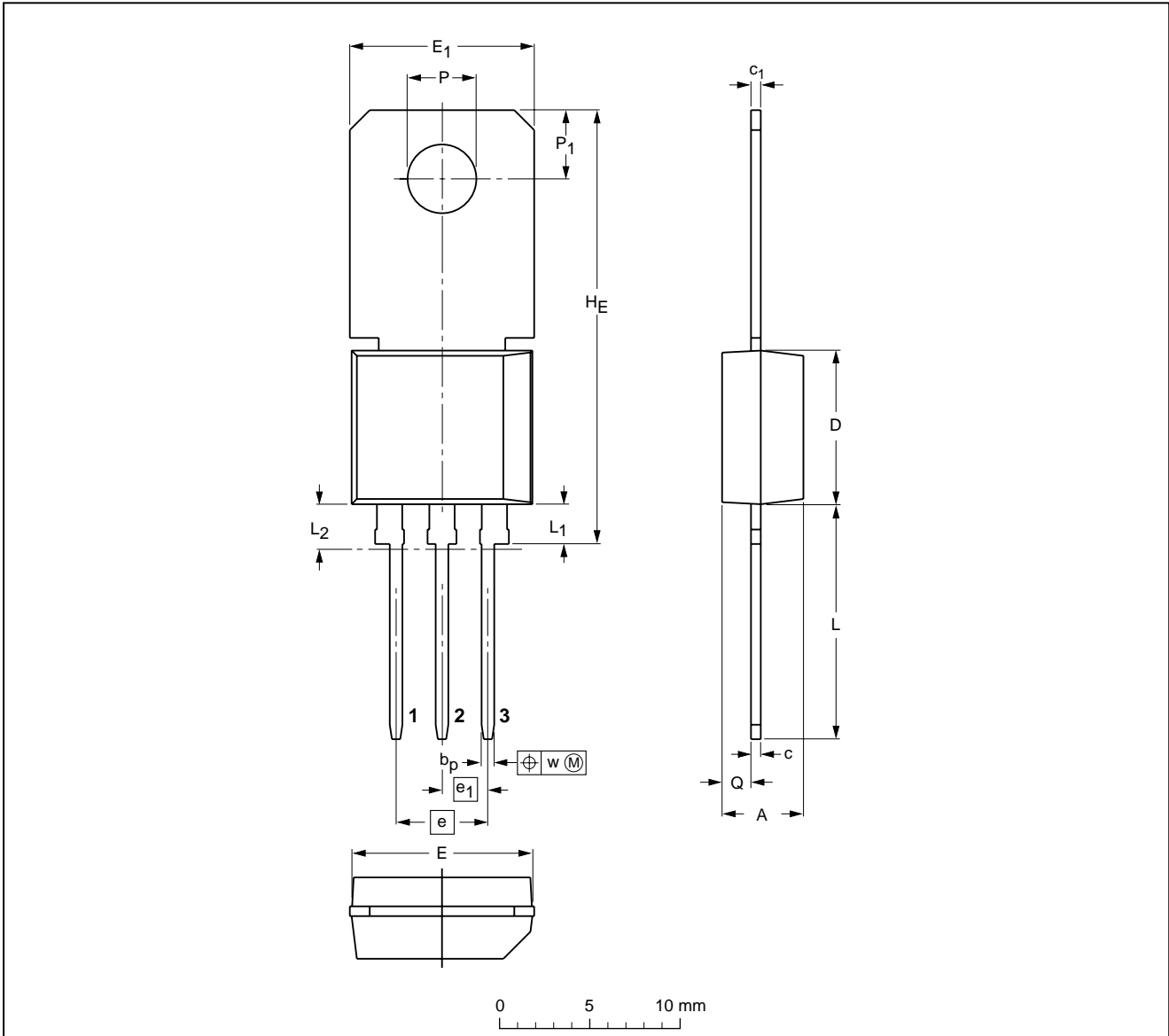
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PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; with cooling fin, mountable to heatsink, 1 mounting hole; 3 leads (in-line)

SOT128B



DIMENSIONS (mm are the original dimensions)

UNIT	A	Ap	c	c1	D	E	E1	e	e1	HE	L	L1	L2 ⁽¹⁾ max	P	P1	Q	w
mm	4.6 4.4	0.8 0.6	0.65 0.5	0.56 0.46	8.6 8.4	10.1 9.9	10.4 10.0	5.08	2.54	24.2 23.8	13.3 12.2	2.4 2.0	2.5	3.8 3.6	3.9 3.7	1.7 1.5	0.25

Note

1. Plastic flash allowed within this zone

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT128B		TO-202				97-02-28

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DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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