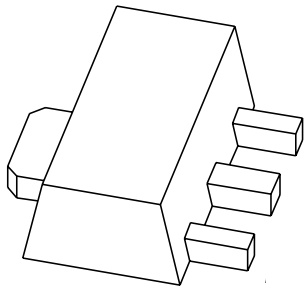


# DATA SHEET



## **PXTA92** PNP high-voltage transistor

Product specification  
Supersedes data of 1997 Jun 20

1999 Apr 29

## PNP high-voltage transistor

## PXTA92

## FEATURES

- Low current (max. 100 mA)
- High voltage (max. 300 V).

## APPLICATIONS

- Telephony and professional communication equipment.

## DESCRIPTION

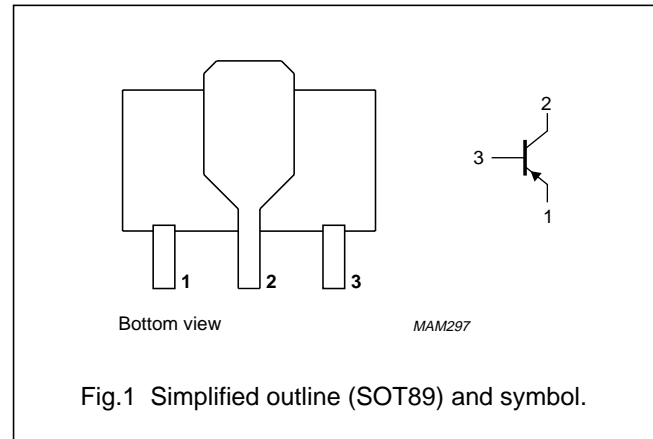
PNP high-voltage transistor in a SOT89 plastic package.  
NPN complement: PXTA42.

## MARKING

| TYPE NUMBER | MARKING CODE |
|-------------|--------------|
| PXTA92      | p2D          |
| PXTA93      | p2E          |

## PINNING

| PIN | DESCRIPTION |
|-----|-------------|
| 1   | emitter     |
| 2   | collector   |
| 3   | base        |



## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

| SYMBOL    | PARAMETER                     | CONDITIONS                           | MIN. | MAX. | UNIT |
|-----------|-------------------------------|--------------------------------------|------|------|------|
| $V_{CBO}$ | collector-base voltage        | open emitter                         | –    | –300 | V    |
| $V_{CEO}$ | collector-emitter voltage     | open base                            | –    | –300 | V    |
| $V_{EBO}$ | emitter-base voltage          | open collector                       | –    | –5   | V    |
| $I_C$     | collector current (DC)        |                                      | –    | –100 | mA   |
| $I_{CM}$  | peak collector current        |                                      | –    | –200 | mA   |
| $I_{BM}$  | peak base current             |                                      | –    | –100 | mA   |
| $P_{tot}$ | total power dissipation       | $T_{amb} \leq 25\text{ °C}$ ; note 1 | –    | 1.3  | W    |
| $T_{stg}$ | storage temperature           |                                      | –65  | +150 | °C   |
| $T_j$     | junction temperature          |                                      | –    | 150  | °C   |
| $T_{amb}$ | operating ambient temperature |                                      | –65  | +150 | °C   |

## Note

1. Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 6 cm<sup>2</sup>.  
For other mounting conditions, see “*Thermal considerations for SOT89 in the General Part of associated Handbook*”.

## PNP high-voltage transistor

PXTA92

## THERMAL CHARACTERISTICS

| SYMBOL        | PARAMETER   | CONDITIONS | VALUE | UNIT |
|---------------|---|------------|-------|------|
| $R_{th\ j-a}$ | thermal resistance from junction to ambient         | note 1     | 96    | K/W  |
| $R_{th\ j-s}$ | thermal resistance from junction to soldering point |            | 16    | K/W  |

## Note

- Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 6 cm<sup>2</sup>.  
For other mounting conditions, see "Thermal considerations for SOT89 in the General Part of associated Handbook".

## CHARACTERISTICS

$T_{amb} = 25\text{ }^{\circ}\text{C}$  unless otherwise specified.

| SYMBOL      | PARAMETER                            | CONDITIONS   | MIN.           | MAX.        | UNIT |
|-------------|--------------------------------------|--|----------------|-------------|------|
| $I_{CBO}$   | collector cut-off current            | $I_E = 0; V_{CB} = -200\text{ V}$  | –              | –250        | nA   |
| $I_{EBO}$   | emitter cut-off current              | $I_C = 0; V_{BE} = -3\text{ V}$  | –              | –100        | nA   |
| $h_{FE}$    | DC current gain                      | $V_{CE} = -10\text{ V}$ ; note 1<br>$I_C = -1\text{ mA}$<br>$I_C = -10\text{ mA}$<br>$I_C = -30\text{ mA}$ | 25<br>40<br>25 | –<br>–<br>– |      |
| $V_{CEsat}$ | collector-emitter saturation voltage | $I_C = -20\text{ mA}; I_B = -2\text{ mA}$  | –              | –500        | mV   |
| $V_{BEsat}$ | base-emitter saturation voltage      | $I_C = -20\text{ mA}; I_B = -2\text{ mA}$  |                | –900        | mV   |
| $C_c$       | collector capacitance                | $I_E = i_e = 0; V_{CB} = -20\text{ V};$<br>$f = 1\text{ MHz}$  | –              | 6           | pF   |
| $f_T$       | transition frequency                 | $I_C = -10\text{ mA}; V_{CE} = -20\text{ V};$<br>$f = 100\text{ MHz}$                                      | 50             | –           | MHz  |

## Note

- Pulse test:  $t_p \leq 300\text{ }\mu\text{s}$ ;  $\delta \leq 0.02$ .

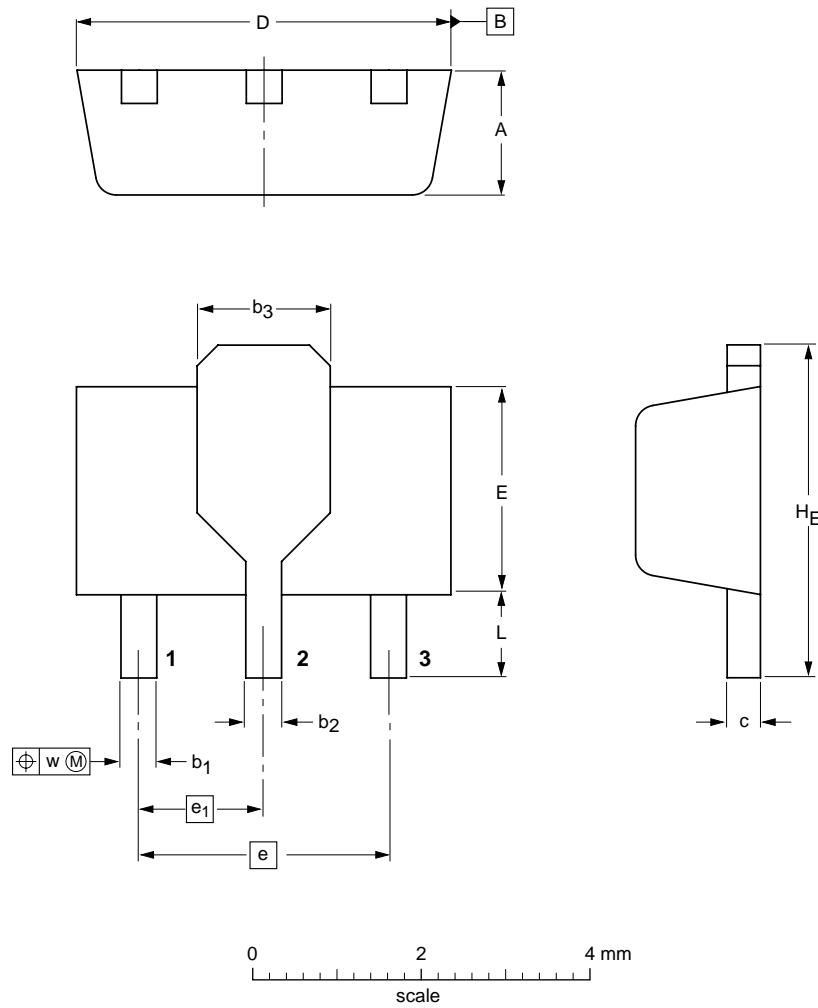
PNP high-voltage transistor

PXTA92

PACKAGE OUTLINE

Plastic surface mounted package; collector pad for good heat transfer; 3 leads

SOT89



DIMENSIONS (mm are the original dimensions)

| UNIT | A          | b <sub>1</sub> | b <sub>2</sub> | b <sub>3</sub> | c            | D          | E          | e   | e <sub>1</sub> | H <sub>E</sub> | L<br>min. | w    |
|------|------------|----------------|----------------|----------------|--------------|------------|------------|-----|----------------|----------------|-----------|------|
| mm   | 1.6<br>1.4 | 0.48<br>0.35   | 0.53<br>0.40   | 1.8<br>1.4     | 0.44<br>0.37 | 4.6<br>4.4 | 2.6<br>2.4 | 3.0 | 1.5            | 4.25<br>3.75   | 0.8       | 0.13 |

| OUTLINE VERSION | REFERENCES |       |      |  | EUROPEAN PROJECTION | ISSUE DATE |
|-----------------|------------|-------|------|--|---------------------|------------|
|                 | IEC        | JEDEC | EIAJ |  |                     |            |
| SOT89           |            |       |      |  |                     | 97-02-28   |

## PNP high-voltage transistor

PXTA92

**DEFINITIONS**

|   |   |
|---|---|
| <b>Data sheet status</b>  |   |
| Objective specification   | This data sheet contains target or goal specifications for product development.       |
| Preliminary specification   | This data sheet contains preliminary data; supplementary data may be published later. |
| Product specification   | This data sheet contains final product specifications.                                |
| <b>Limiting values</b>  |   |
| Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability. |   |
| <b>Application information</b>  |   |
| Where application information is given, it is advisory and does not form part of the specification.   |   |

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PNP high-voltage transistor

PXTA92

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**NOTES**

PNP high-voltage transistor

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**NOTES**

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