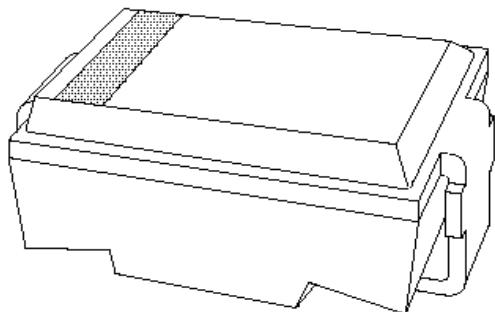


# **DATA SHEET**



## **SMA S1 series** **Controlled avalanche rectifiers**

Product specification

2000 Jan 19

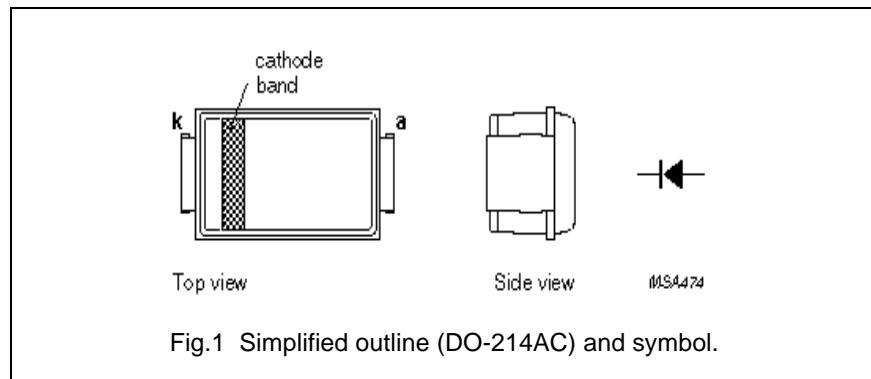
**Controlled avalanche rectifiers****SMA S1 series****FEATURES**

- Glass passivated
- High maximum operating temperature
- Ideal for surface mount automotive applications
- Low leakage current
- Excellent stability
- Guaranteed avalanche energy absorption capability
- UL 94V-0 classified plastic package
- Shipped in 12 mm embossed tape
- Marking: cathode, date code, type code
- Easy pick and place.

**DESCRIPTION**

DO-214AC surface mountable package with glass passivated chip.

The well-defined void-free case is of a transfer-moulded thermo-setting plastic. The small rectangular package has two J bent leads.

**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{RRM}$	repetitive peak reverse voltage S1A S1B S1D S1G S1J S1K S1M		–	50	V
			–	100	V
			–	200	V
			–	400	V
			–	600	V
			–	800	V
			–	1000	V
$V_R$	continuous reverse voltage S1A S1B S1D S1G S1J S1K S1M		–	50	V
			–	100	V
			–	200	V
			–	400	V
			–	600	V
			–	800	V
			–	1000	V

## Controlled avalanche rectifiers

## SMA S1 series

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{RMS}$	root mean square voltage S1A		–	35	V
	S1B			70	V
	S1D			140	V
	S1G			280	V
	S1J			420	V
	S1K			560	V
	S1M			700	V
$I_{F(AV)}$	average forward current	averaged over any 20 ms period; $T_{tp} = 110^\circ\text{C}$ ; see Fig.2	–	1.0	A
$I_{FSM}$	non-repetitive peak forward current S1A to S1J	$t = 8.3 \text{ ms half sine wave};$ $T_j = 25^\circ\text{C}$ prior to surge; $V_R = V_{RRMmax}$	–	30	A
	S1K and S1M			25	A
$T_{stg}$	storage temperature		–65	+175	°C
$T_j$	junction temperature	See Fig.3	–65	+175	°C

## ELECTRICAL CHARACTERISTICS

 $T_j = 25^\circ\text{C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_F$	forward voltage	$I_F = 1 \text{ A}$ ; see Fig.4	–	–	1.10	V
$I_R$	reverse current S1A to S1J S1K and S1M	$V_R = V_{RRMmax}$ ; see Fig.5	–	–	1	$\mu\text{A}$
		$V_R = V_{RRMmax}; T_j = 165^\circ\text{C}$ ; see Fig.5			50	$\mu\text{A}$
$t_{rr}$	reverse recovery time	when switched from $I_F = 0.5 \text{ A}$ to $I_R = 1 \text{ A}$ ; measured at $I_R = 0.25 \text{ A}$ ; see Fig.9	–	1	–	$\mu\text{s}$
$C_d$	diode capacitance	$V_R = 4 \text{ V}$ ; $f = 1 \text{ MHz}$ ; see Fig.6	–	8	–	pF

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th j-tp}$	thermal resistance from junction to tie-point; see Fig.7		27	K/W
$R_{th j-a}$	thermal resistance from junction to ambient	note 1	100	K/W
		note 2	150	K/W

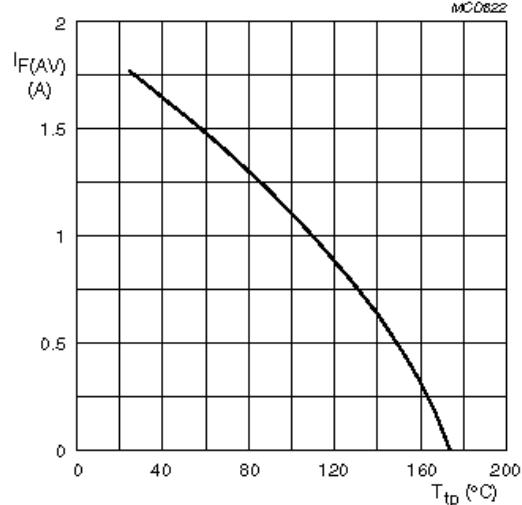
## Notes

1. Device mounted on  $\text{Al}_2\text{O}_3$  printed-circuit board, 0.7 mm thick; thickness of copper  $\geq 35 \mu\text{m}$ .
2. Device mounted on epoxy-glass printed-circuit board, 1.5 mm thick; thickness of copper  $\geq 40 \mu\text{m}$ .  
For more information please refer to the 'General Part of associated Handbook'.

## Controlled avalanche rectifiers

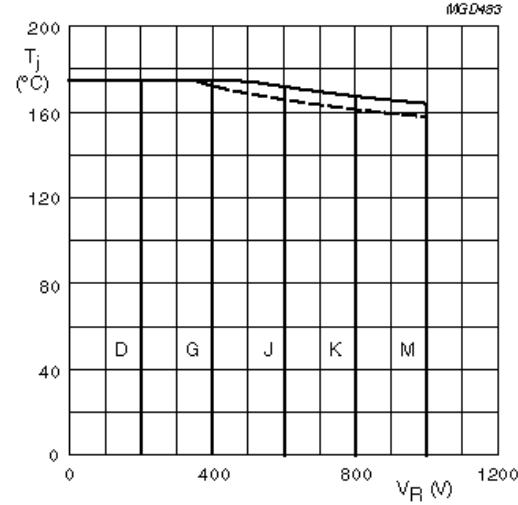
## SMA S1 series

## GRAPHICAL DATA



V<sub>R</sub> = V<sub>RRMmax</sub>; δ = 0.5; a = 1.57.

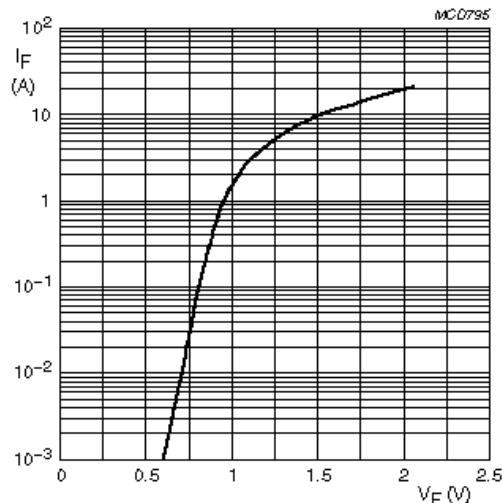
Fig.2 Maximum permissible average forward current as a function of tie-point temperature (including losses due to reverse leakage).



Device mounted as shown in Fig.8.

Solid line: Al<sub>2</sub>O<sub>3</sub> PCB.  
Dotted line: epoxy PCB.

Fig.3 Maximum permissible junction temperature as a function of reverse voltage.



T<sub>j</sub> = 25 °C.

Fig.4 Forward current as a function of forward voltage; typical values.

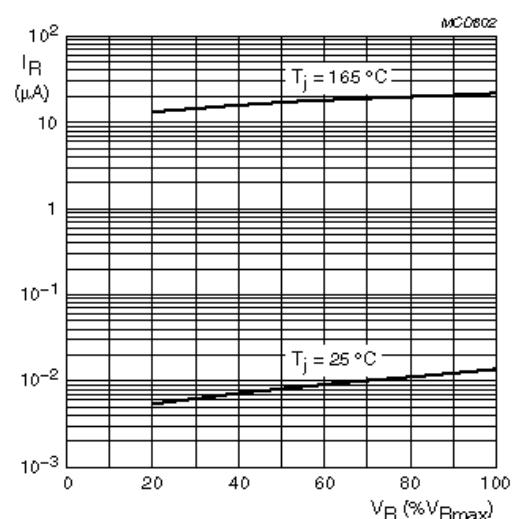
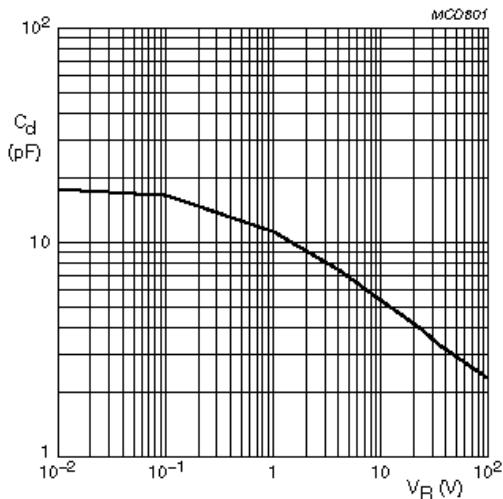


Fig.5 Reverse current as a function of reverse voltage; typical values.

## Controlled avalanche rectifiers

## SMA S1 series



$f = 1\text{MHz}; T_j = 25^\circ\text{C}$ .

Fig.6 Diode capacitance as a function of reverse voltage; typical values.

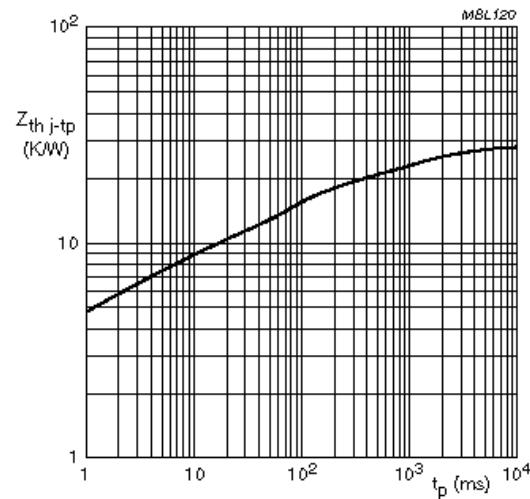
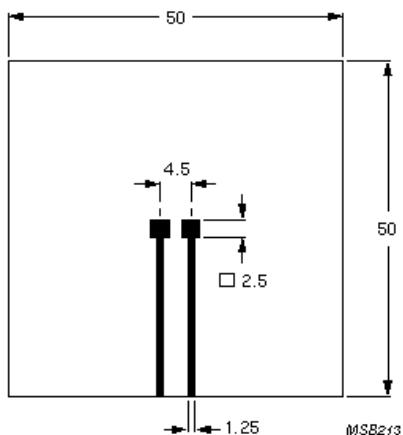


Fig.7 Transient thermal impedance as a function of pulse width.



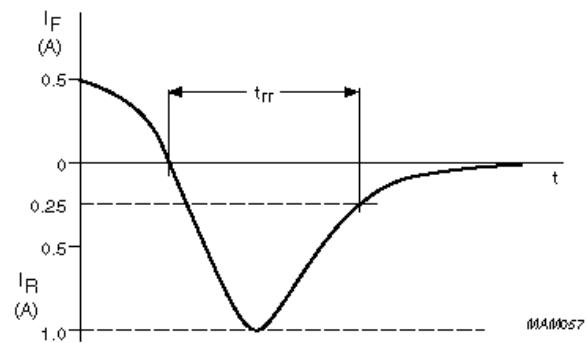
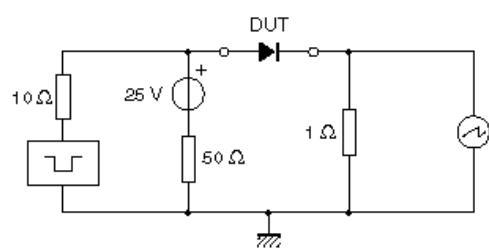
Dimensions in mm.

Material:  $\text{Al}_2\text{O}_3$  or epoxy-glass.

Fig.8 Printed-circuit board for surface mounting.

## Controlled avalanche rectifiers

## SMA S1 series



Input impedance oscilloscope:  $1 \text{ M}\Omega$ ,  $22 \text{ pF}$ ;  $t_r \leq 7 \text{ ns}$ .

Source impedance:  $50 \Omega$ ;  $t_r \leq 15 \text{ ns}$ .

Fig.9 Test circuit and reverse recovery time waveform and definition.

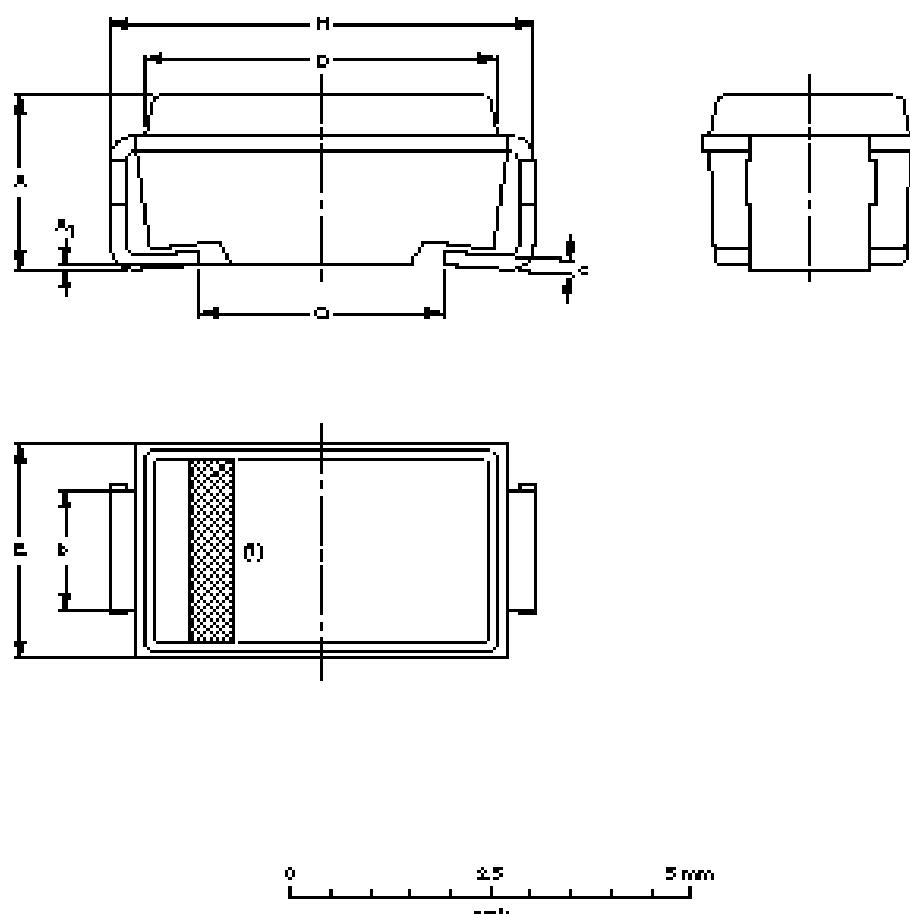
## Controlled avalanche rectifiers

## SMA S1 series

## PACKAGE OUTLINE

Transfer-moulded thermo-setting plastic small rectangular surface mounted package;  
2 connectors

S00124



DIMENSIONS (mm are the original dimensions)

UNIT	A	A <sub>1</sub>	b	c	D	E	H	Q
mm	2.3 2.0	0.05	1.6 1.4	0.2	4.5 4.3	2.3 2.4	5.5 5.1	3.3 2.7

Note:

1. The marking band indicates the cathode.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	EC	VIDEO	DIAG			
S00124		DO-214AC				20-10-88

## Controlled avalanche rectifiers

## SMA S1 series

### DEFINITIONS

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	

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