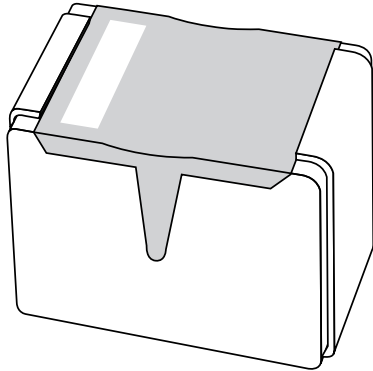


DATA SHEET



BAT254 Schottky barrier diode

Product specification
Supersedes data of 1996 Mar 19

1999 Apr 22

Schottky barrier diode

BAT254

FEATURES

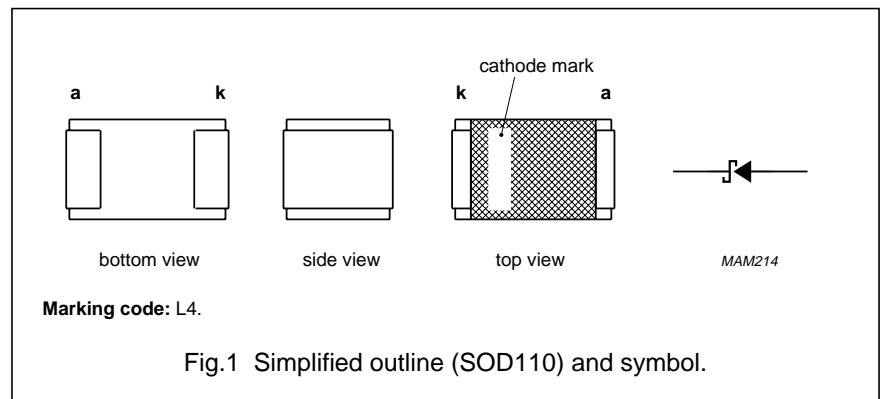
- Low forward voltage
- Guard ring protected
- Very small ceramic SMD package.

APPLICATIONS

- Ultra high-speed switching
- Voltage clamping
- Protection circuits
- Blocking diodes.

DESCRIPTION

Planar Schottky barrier diode encapsulated in a SOD110 very small ceramic SMD package.



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_R	continuous reverse voltage		–	30	V
I_F	continuous forward current		–	200	mA
I_{FRM}	repetitive peak forward current	$t_p \leq 1$ s; $\delta \leq 0.5$	–	300	mA
I_{FSM}	non-repetitive peak forward current	$t_p < 10$ ms	–	600	mA
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	125	°C
T_{amb}	operating ambient temperature		–65	+125	°C

Schottky barrier diode

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ELECTRICAL CHARACTERISTICS $T_{amb} = 25\text{ °C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
V_F	forward voltage	see Fig.2 $I_F = 0.1\text{ mA}$ $I_F = 1\text{ mA}$ $I_F = 10\text{ mA}$ $I_F = 30\text{ mA}$ $I_F = 100\text{ mA}$	240 320 400 500 800	mV mV mV mV mV
I_R	reverse current	$V_R = 25\text{ V}$; note 1; see Fig.3	2	μA
t_{rr}	reverse recovery time	when switched from $I_F = 10\text{ mA}$ to $I_R = 10\text{ mA}$; $R_L = 100\ \Omega$; measured at $I_R = 1\text{ mA}$; see Fig.5	5	ns
C_d	diode capacitance	$f = 1\text{ MHz}$; $V_R = 1\text{ V}$; see Fig.4	10	pF

Note

1. Pulse test: $t_p = 300\ \mu\text{s}$; $\delta = 0.02$.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	315	K/W

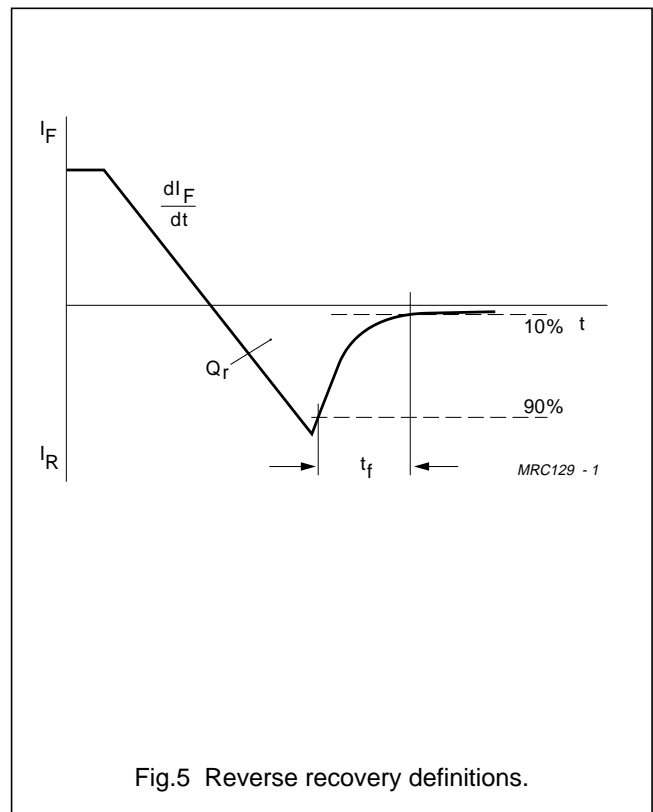
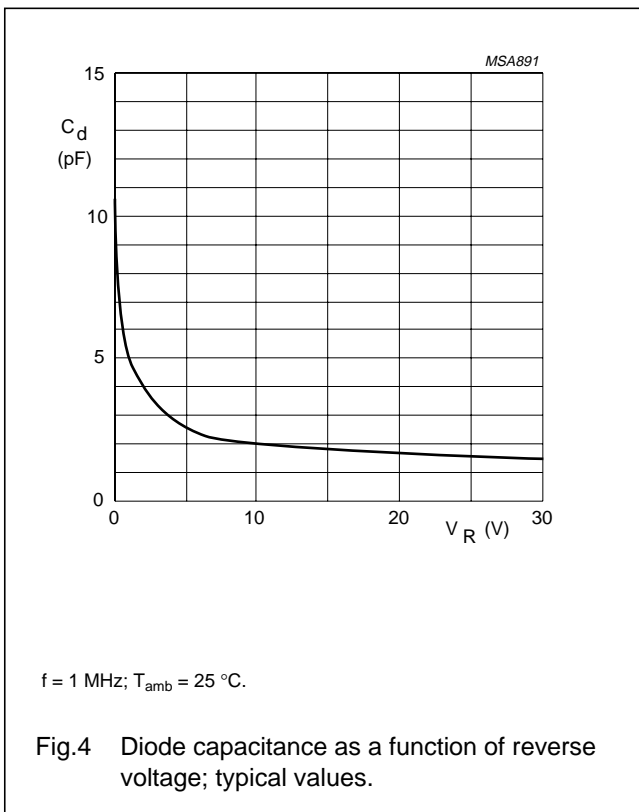
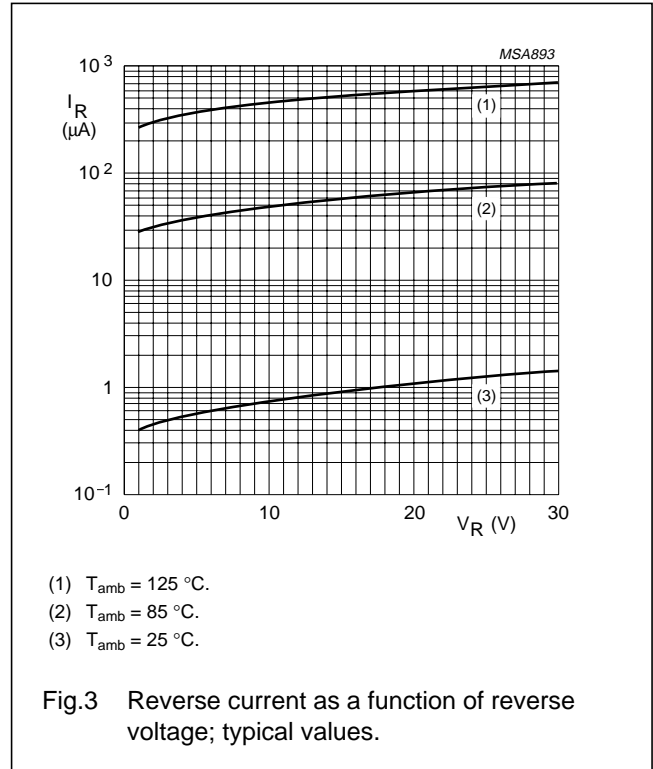
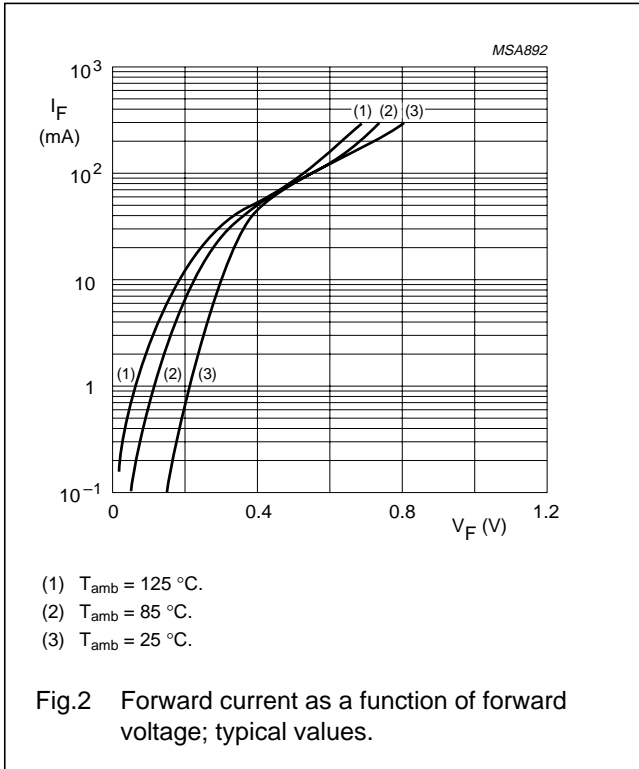
Note

1. Refer to SOD110 standard mounting conditions.

Schottky barrier diode

BAT254

GRAPHICAL DATA



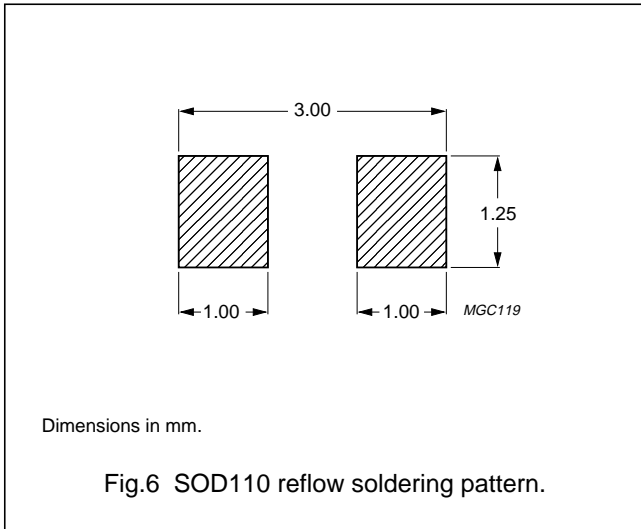
Schottky barrier diode

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MOUNTING

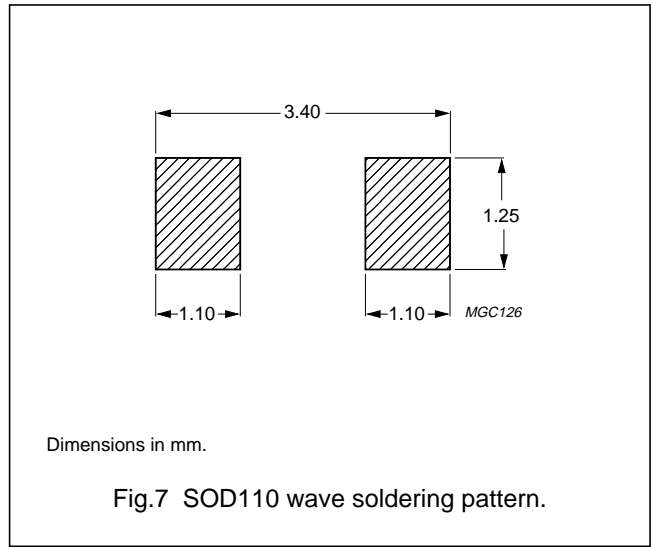
Reflow soldering

Follow standard reflow soldering techniques to ensure correct application of solder paste and placement of the SOD110 package (see Fig.6).



Wave soldering

Before wave soldering, attach SOD110 packages to the printed-circuit boards using a small dot of thermo-setting epoxy or UV-curing adhesive centred between the soldering lands (see Fig.7).



Schottky barrier diode

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PACKAGE OUTLINE

Very small ceramic rectangular surface mounted package

SOD110

DIMENSIONS (mm are the original dimensions)

UNIT	A max.	D	E	y
mm	1.6	2.10 1.90	1.40 1.10	0.1

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ		
SOD110					97-04-14

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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Schottky barrier diode

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