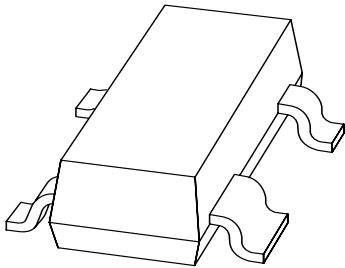


DATA SHEET



BCV65 NPN/PNP general purpose transistor

Product specification
Supersedes data of 1997 Apr 22

1999 Apr 22

NPN/PNP general purpose transistor

BCV65

FEATURES

- Low current (max. 100 mA)
- Low voltage (max. 30 V).

APPLICATIONS

- General purpose switching and amplification.

DESCRIPTION

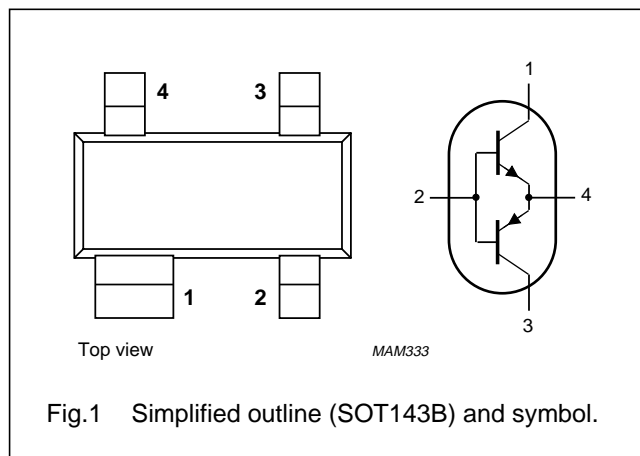
An NPN/PNP matched pair transistor in a SOT143B plastic package.

MARKING

TYPE NUMBER	MARKING CODE
BCV65	97p

PINNING

PIN	DESCRIPTION
1, 3	collector
2	common base
4	common emitter



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per transistor					
V_{CBO}	collector-base voltage	open emitter	–	30	V
V_{CEO}	collector-emitter voltage	open base	–	30	V
I_C	collector current (DC)		–	100	mA
I_{CM}	peak collector current		–	200	mA
I_{BM}	peak base current		–	200	mA
P_{tot}	total power dissipation (per device)	$T_{amb} \leq 25\text{ }^\circ\text{C}$	–	250	mW
T_{stg}	storage temperature		–65	+150	$^\circ\text{C}$
T_j	junction temperature		–	150	$^\circ\text{C}$
T_{amb}	operating ambient temperature		–65	+150	$^\circ\text{C}$

NPN/PNP general purpose transistor

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

$T_j = 25\text{ °C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Per transistor						
I_{CBO}	collector cut-off current	$I_E = 0; V_{CB} = 30\text{ V}$	–	–	15	nA
		$I_E = 0; V_{CB} = 30\text{ V}; T_j = 150\text{ °C}$	–	–	5	μA
h_{FE}	DC current gain	$I_C = 2\text{ mA}; V_{CE} = 5\text{ V}$	75	–	800	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 10\text{ mA}; I_B = 0.5\text{ mA}$	–	90	300	mV
		$I_C = 100\text{ mA}; I_B = 5\text{ mA}$	–	250	650	mV
V_{BEsat}	base-emitter saturation voltage	$I_C = 10\text{ mA}; I_B = 0.5\text{ mA}; \text{note 1}$	–	700	–	mV
		$I_C = 100\text{ mA}; I_B = 5\text{ mA}; \text{note 1}$	–	900	–	mV
V_{BE}	base-emitter voltage	$I_C = 2\text{ mA}; V_{CE} = 5\text{ V}; \text{note 2}$	580	650	750	mV
		$I_C = 10\text{ mA}; V_{CE} = 5\text{ V}; \text{note 2}$	–	–	820	mV

Notes

1. V_{BEsat} decreases by approximately $|1.7|$ mV/K with increasing temperature.
2. V_{BE} decreases by approximately $|2|$ mV/K with increasing temperature.

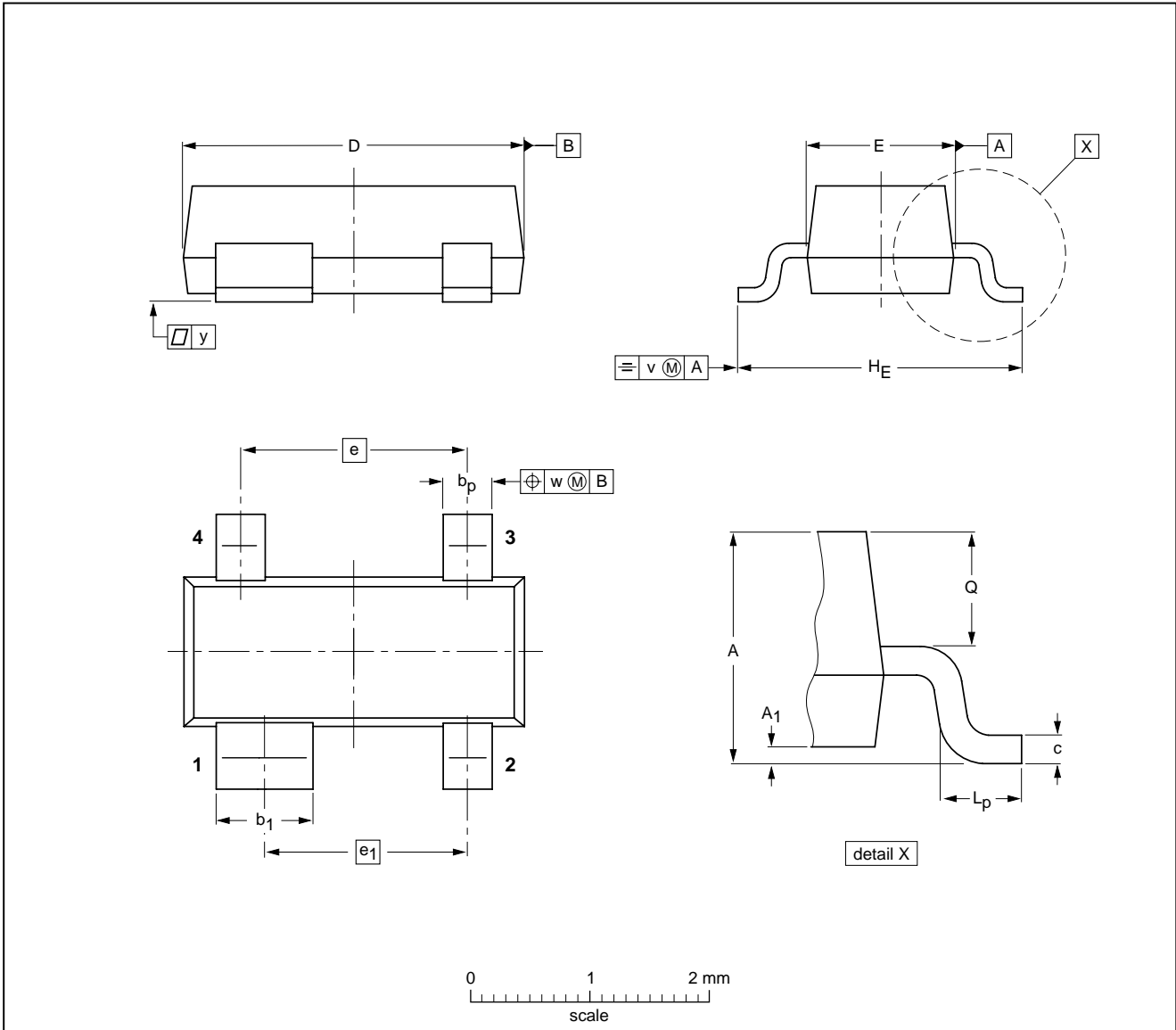
NPN/PNP general purpose transistor

BCV65

PACKAGE OUTLINE

Plastic surface mounted package; 4 leads

SOT143B



DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max	b _p	b ₁	c	D	E	e	e ₁	H _E	L _p	Q	v	w	y
mm	1.1 0.9	0.1	0.48 0.38	0.88 0.78	0.15 0.09	3.0 2.8	1.4 1.2	1.9	1.7	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT143B						97-02-28

NPN/PNP general purpose transistor

BCV65

DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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NPN/PNP general purpose transistor

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NOTES

NPN/PNP general purpose transistor

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NOTES

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