

**Silicon Diffused Power Transistor****BU4540AW****GENERAL DESCRIPTION**

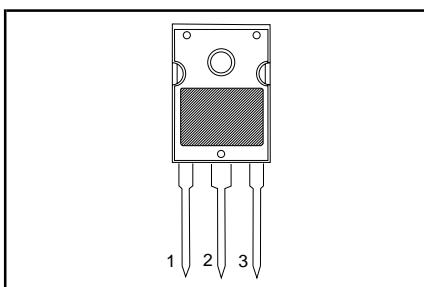
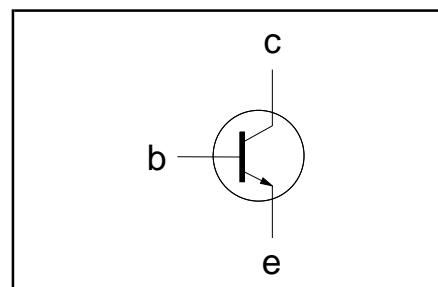
Enhanced performance, new generation, high-voltage, high-speed switching npn transistor in a plastic full pack envelope intended for use in horizontal deflection circuits of colour television receivers and p.c monitors. Features exceptional tolerance to base drive and collector current load variations resulting in a very low worst case dissipation.

**QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$V_{CESM}$	Collector-emitter voltage peak value	$V_{BE} = 0$	-	1500	V
$V_{CEO}$	Collector-emitter voltage (open base)		-	800	V
$I_C$	Collector current (DC)		-	25	A
$I_{CM}$	Collector current peak value		-	40	A
$P_{tot}$	Total power dissipation		-	125	W
$V_{CEsat}$	Collector-emitter saturation voltage		-	3.0	V
$I_{Csat}$	Collector saturation current		16	-	A
$t_f$	fall time	$T_{mb} \leq 25^\circ\text{C}$ $I_C = 16 \text{ A}; I_B = 4 \text{ A}$ $f = 32\text{kHz}$ $f = 110\text{kHz}$ $I_{Csat} = 16 \text{ A}; f = 32\text{kHz}$ $I_{Csat} = 8 \text{ A}; f = 110\text{kHz}$	8	-	A

**PINNING - SOT429**

PIN	DESCRIPTION
1	base
2	collector
3	emitter
tab	collector

**PIN CONFIGURATION****SYMBOL****LIMITING VALUES**

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CESM}$	Collector-emitter voltage peak value	$V_{BE} = 0 \text{ V}$	-	1500	V
$V_{CEO}$	Collector-emitter voltage (open base)		-	800	V
$I_C$	Collector current (DC)		-	25	A
$I_{CM}$	Collector current peak value		-	40	A
$I_B$	Base current (DC)		-	10	A
$I_{BM}$	Base current peak value		-	15	A
$-I_{BM}$	Reverse base current peak value <sup>1</sup>		-	10	A
$P_{tot}$	Total power dissipation		-	125	W
$T_{stg}$	Storage temperature	$T_{mb} \leq 25^\circ\text{C}$	-55	150	°C
$T_j$	Junction temperature		-	150	°C

**THERMAL RESISTANCES**

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$R_{th,j-mb}$	Junction to mounting base	-	-	1.0	K/W
$R_{th,j-a}$	Junction to ambient	in free air	45	-	K/W

<sup>1</sup> Turn-off current.

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**STATIC CHARACTERISTICS** $T_{mb} = 25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$I_{CES}$	Collector cut-off current <sup>2</sup>	$V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax}$	-	-	1.0	mA
$I_{CES}$		$V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax}$	-	-	2.0	mA
$I_{EBO}$	Emitter cut-off current	$T_j = 125^\circ\text{C}$	-	-	1.0	mA
$BV_{EBO}$	Base-emitter breakdown voltage	$V_{EB} = 7.5 \text{ V}; I_C = 0 \text{ A}$	7.5	14	-	V
$V_{CESat}$	Collector-emitter saturation voltage	$I_B = 1 \text{ mA}$	-	-	3.0	V
$V_{BEsat}$	Base-emitter saturation voltage	$I_C = 16 \text{ A}; I_B = 4 \text{ A}$	t.b.f	-	1.0	V
$h_{FE}$	DC current gain	$I_C = 16 \text{ A}; I_B = 4 \text{ A}$	-	t.b.f	-	V
$h_{FE}$		$I_C = 1 \text{ A}; V_{CE} = 5 \text{ V}$	4.2	5.35	6.5	
		$I_C = 16 \text{ A}; V_{CE} = 5 \text{ V}$				

**DYNAMIC CHARACTERISTICS** $T_{mb} = 25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$t_s$	Switching times (32 kHz line deflection dynamic test circuit).	$I_{Csat} = 16 \text{ A}; I_{B1} = 3.2 \text{ A}; (I_{B2} = -8 \text{ A})$			
$t_f$	Turn-off storage time		t.b.f	t.b.f	$\mu\text{s}$
$t_f$	Turn-off fall time		t.b.f	t.b.f	$\mu\text{s}$
$t_s$	Switching times (110 kHz line deflection dynamic test circuit).	$I_{Csat} = 8 \text{ A}; I_{B1} = 1.6 \text{ A}; (I_{B2} = -4.8 \text{ A})$			
$t_f$	Turn-off storage time		t.b.f	t.b.f	$\mu\text{s}$
$t_f$	Turn-off fall time		t.b.f	t.b.f	$\mu\text{s}$

<sup>2</sup> Measured with half sine-wave voltage (curve tracer).

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**MECHANICAL DATA***Dimensions in mm*

Net Mass: 5 g

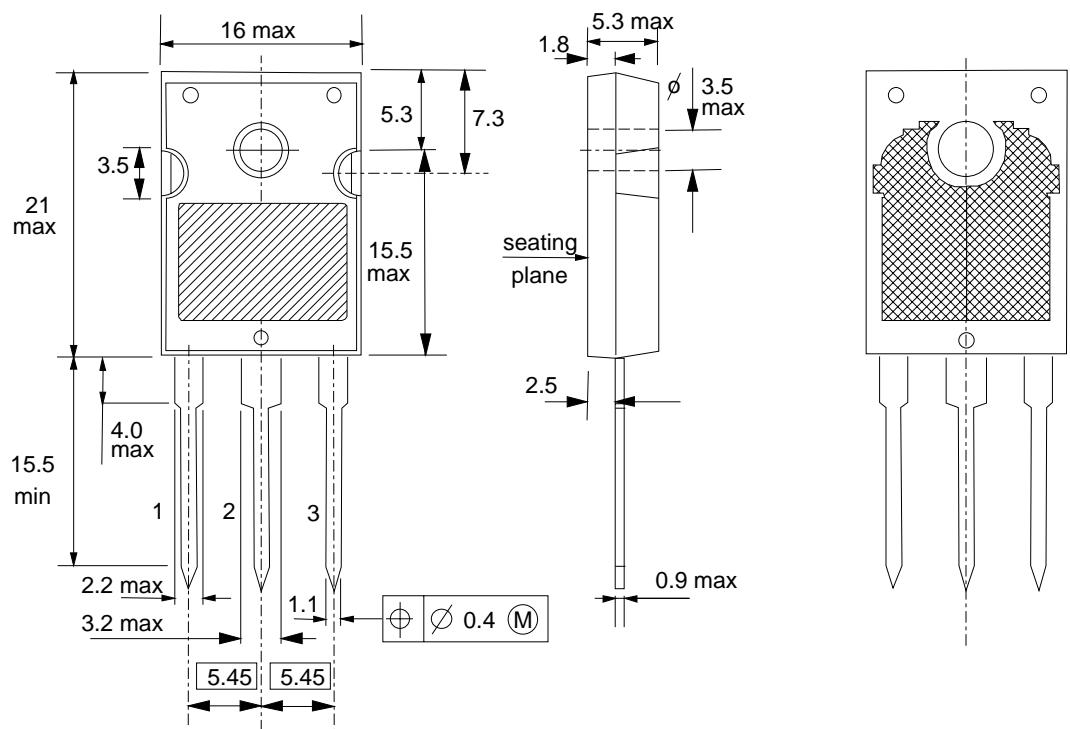


Fig.1. SOT429; pin 2 connected to mounting base.

**Notes**

1. Refer to mounting instructions for SOT429 envelope.
2. Epoxy meets UL94 V0 at 1/8".

**Silicon Diffused Power Transistor****BU4540AW****DEFINITIONS**

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
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西安劳动南路 88 号电子商城二楼 D23 号

TEL：029-81022619 13072977981 FAX:029-88789382