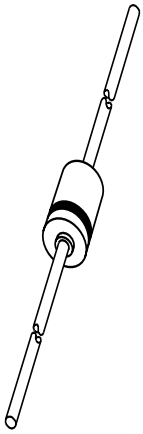
DATA SHEET



BYD12 series
Controlled avalanche rectifiers

Preliminary specification

1998 Dec 03

Philips
Semiconductors

PHILIPS

Controlled avalanche rectifiers BYD12 series

FEATURES

- Glass passivated
- High maximum operating temperature
- Low leakage current
- Excellent stability
- Guaranteed avalanche energy absorption capability
- Available in ammo-pack.

DESCRIPTION

Cavity free cylindrical glass SOD120 package through Implotec™(1) technology. This package is hermetically sealed and fatigue free as coefficients of expansion of all used parts are matched.

(1) Implotec is a trademark of Philips.

Fig.1 Simplified outline (SOD120) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{RRM}	repetitive peak reverse voltage				
	BYD12D		–	200	V
	BYD12G		–	400	V
	BYD12J		–	600	V
	BYD12K		–	800	V
	BYD12M		–	1000	V
V_R	continuous reverse voltage				
	BYD12D		–	200	V
	BYD12G		–	400	V
	BYD12J		–	600	V
	BYD12K		–	800	V
	BYD12M		–	1000	V
$I_{F(AV)}$	average forward current	T_{amb} = 25 °C; printed-circuit board mounting, pitch 5 mm, see Fig.6; averaged over any 20 ms period, see Fig.2	–	0.82	A
I_{FSM}	non-repetitive peak forward current	t = 10 ms half sinewave; T_j = 25 °C prior to surge; $V_R = V_{RRMmax}$	–	15	A
T_{stg}	storage temperature		–65	+175	°C
T_j	junction temperature	see Fig.3	–65	+175	°C

ELECTRICAL CHARACTERISTICS

T_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
V_F	forward voltage	I_F = 1 A; see Fig.4	1.05	V
I_R	reverse current	$V_R = V_{RRMmax}$	1	μA
		$V_R = V_{RRMmax}$; T_j = 165 °C; see Fig.5	100	μA

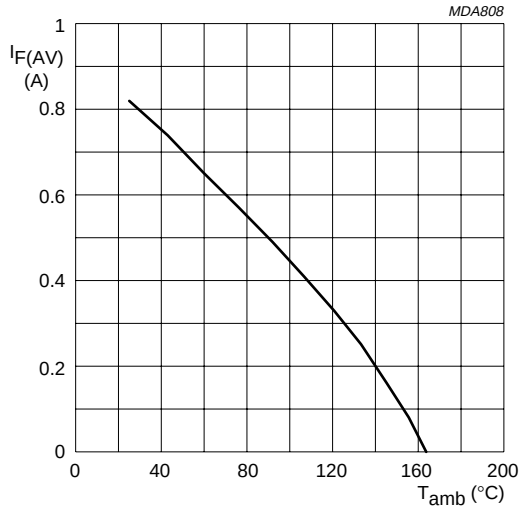
THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j\text{-}a}$	thermal resistance from junction to ambient	note 1	150	K/W

Note

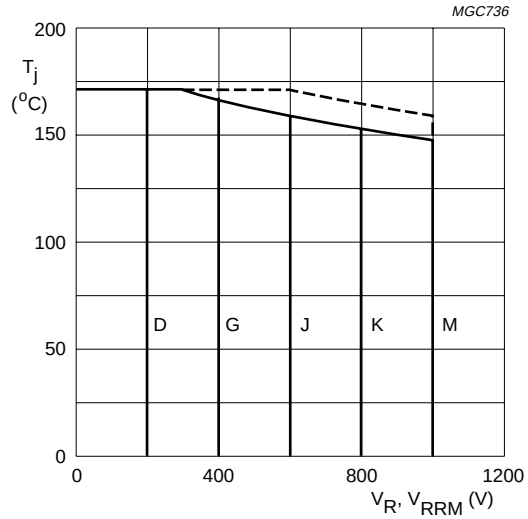
1. Device mounted on an epoxy-glass printed-circuit board, 1.5 mm thick; thickness of copper layer ≥40 μm, pitch 5 mm; see Fig.6.

GRAPHICAL DATA



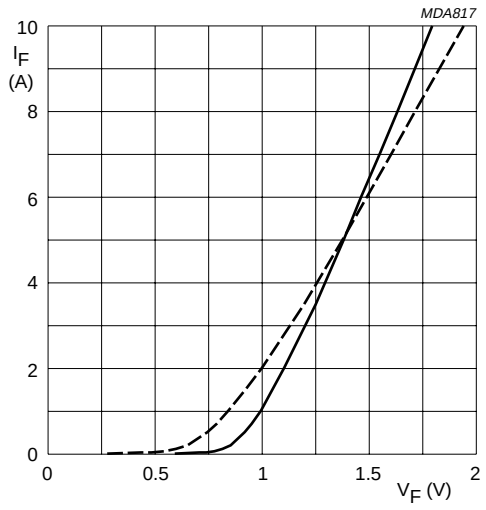
a = 1.57; $V_R = V_{RRMmax}$; δ = 0.5.
Device mounted as shown in Fig.6.

Fig.2 Maximum permissible average forward current as a function of ambient temperature (including losses due to reverse leakage).



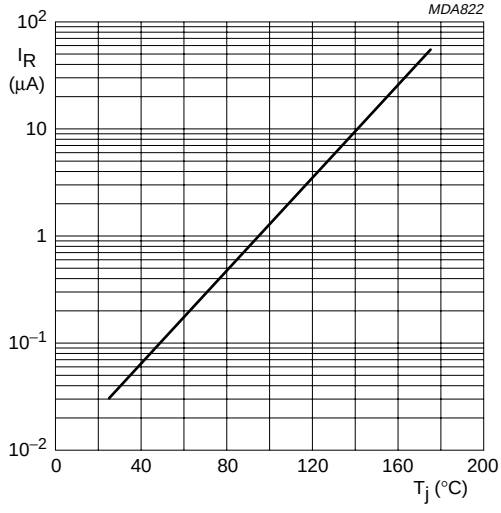
Solid line = V_R.
Dotted line = V_{RRM}; δ = 0.5.

Fig.3 Maximum permissible junction temperature as a function of reverse voltage.



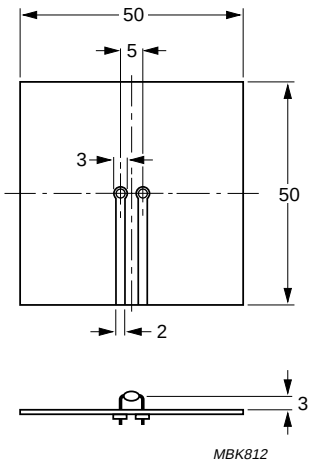
Solid line: T_j = 25 °C.
Dotted line: T_j = 175 °C.

Fig.4 Forward current as a function of forward voltage; typical values.



$V_R = V_{RRMmax}$.

Fig.5 Reverse current as a function of junction temperature; typical values.

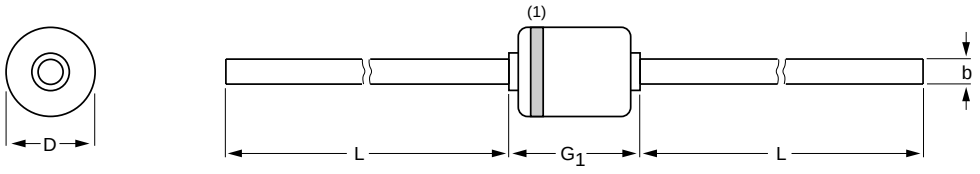


Dimensions in mm.

Fig.6 Device mounted on a printed-circuit board.

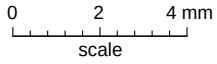
PACKAGE OUTLINE

Hermetically sealed glass package; axial leaded; 2 leads **SOD120**



DIMENSIONS (mm are the original dimensions)

UNIT	b	D max.	G_1 max.	L min.
mm	0.6	2.15	3.0	28



Note

1. The marking band indicates the cathode.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOD120						98-05-25

DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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